



# SPEC® CINT2006 Result

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## IBM Corporation

## SPECint®\_rate2006 = Not Run

## IBM System x3400 (Intel Xeon E5335)

## SPECint\_rate\_base2006 = 41.9

CPU2006 license: 11

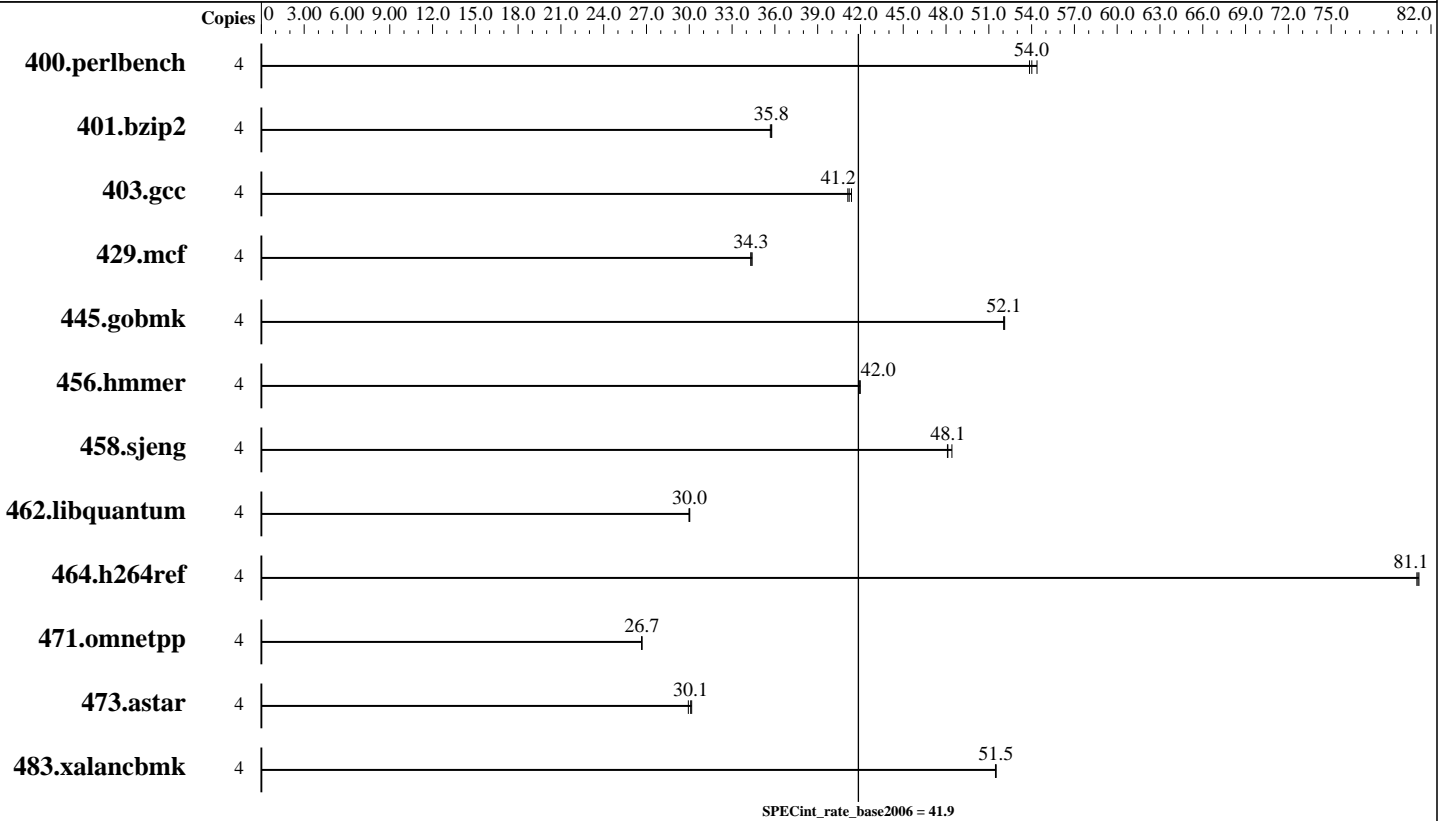
Test date: Sep-2007

Test sponsor: IBM Corporation

Hardware Availability: Feb-2007

Tested by: IBM Corporation

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon E5335  
 CPU Characteristics: 1333MHz system bus  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)  
 Disk Subsystem: 1 x 146 GB SAS, 15000 RPM  
 Other Hardware: None

### Software

Operating System: SLES 10 (x86\_64), 2.6.16.21-0.8-smp  
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20070824  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Multi-user, run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: MicroQuill SmartHeap 8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	719	54.4	<u>724</u>	<u>54.0</u>	726	53.9							
401.bzip2	4	1082	35.7	<u>1080</u>	<u>35.8</u>	1079	35.8							
403.gcc	4	<u>781</u>	<u>41.2</u>	783	41.1	778	41.4							
429.mcf	4	1060	34.4	<u>1062</u>	<u>34.3</u>	1063	34.3							
445.gobmk	4	806	52.0	<u>806</u>	<u>52.1</u>	805	52.1							
456.hammer	4	889	42.0	<u>889</u>	<u>42.0</u>	891	41.9							
458.sjeng	4	<u>1006</u>	<u>48.1</u>	1006	48.1	1000	48.4							
462.libquantum	4	2765	30.0	<u>2761</u>	<u>30.0</u>	2760	30.0							
464.h264ref	4	1091	81.2	<u>1092</u>	<u>81.1</u>	1093	81.0							
471.omnetpp	4	937	26.7	938	26.7	<u>937</u>	<u>26.7</u>							
473.astar	4	938	29.9	931	30.2	<u>933</u>	<u>30.1</u>							
483.xalancbmk	4	536	51.5	<u>536</u>	<u>51.5</u>	536	51.5							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs

-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.22.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.22.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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