SPEC® CINT2006 Result

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Intel Corporation

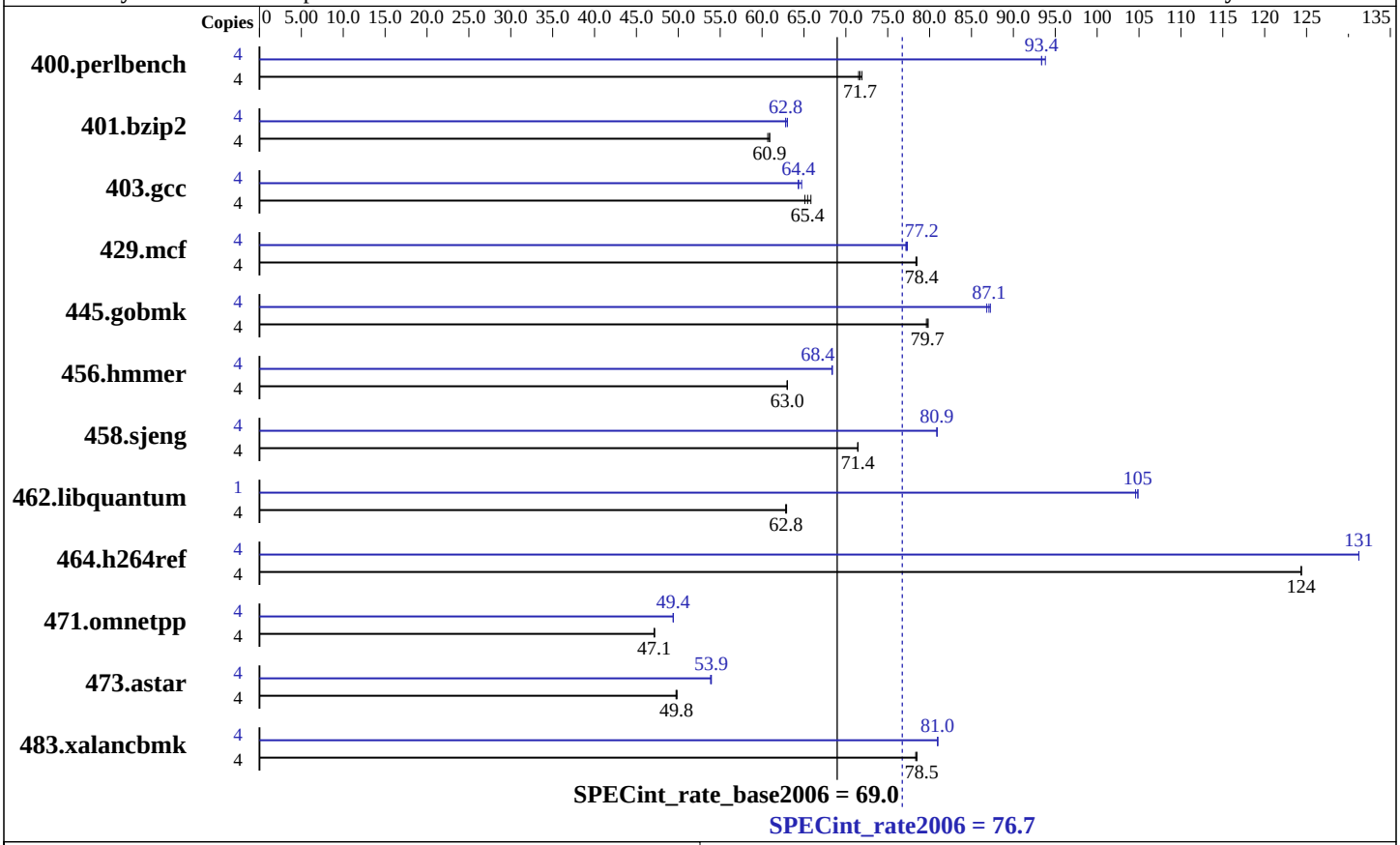
Asus P5E3 Deluxe (Intel Core 2 Extreme QX9650)

SPECint®_rate2006 = 76.7

SPECint_rate_base2006 = 69.0

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Nov-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Core 2 Extreme QX9650
CPU Characteristics:	3.00 GHz 1333 MHz FSB
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	4 GB (4x1GB Corsair TWIN3X2048-1333C9DHX DDR3-1333 CL9)
Disk Subsystem:	Seagate 320GB NCQ SATA, 16MB cache, 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows Vista64 Ultimate
Compiler:	Intel C++ Compiler for IA32 version 10.1 Build 20070913 Package ID: w_cc_p_10.1.011 Microsoft Visual Studio 2005 SP1 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.1 from http://www.microquill.com/

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	543	71.9	546	71.5	**545**	**71.7**	4	417	93.8	419	93.4	**419**	**93.4**
401.bzip2	4	636	60.7	634	60.9	**634**	**60.9**	4	612	63.0	615	62.8	**614**	**62.8**
403.gcc	4	**492**	**65.4**	489	65.8	495	65.1	4	497	64.7	**500**	**64.4**	501	64.3
429.mcf	4	466	78.4	465	78.5	**465**	**78.4**	4	472	77.3	473	77.2	**472**	**77.2**
445.gobmk	4	**526**	**79.7**	527	79.6	526	79.8	4	**482**	**87.1**	483	86.8	481	87.3
456.hmmer	4	592	63.0	**592**	**63.0**	593	63.0	4	**546**	**68.4**	546	68.3	546	68.4
458.sjeng	4	677	71.4	**678**	**71.4**	678	71.4	4	**598**	**80.9**	598	80.9	598	80.9
462.libquantum	4	1317	62.9	**1319**	**62.8**	1319	62.8	1	198	105	198	105	**198**	**105**
464.h264ref	4	**712**	**124**	712	124	712	124	4	674	131	674	131	**674**	**131**
471.omnetpp	4	530	47.2	**530**	**47.1**	530	47.1	4	506	49.4	506	49.4	**506**	**49.4**
473.astar	4	565	49.7	563	49.8	**564**	**49.8**	4	521	53.9	**521**	**53.9**	521	53.9
483.xalancbmk	4	352	78.3	352	78.5	**352**	**78.5**	4	341	80.9	341	81.0	**341**	**81.0**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
Product description located as of 11/2007:
http://www.asus.com/products.aspx?l1=3&l2=11&l3=572&l4=0&model=1872&modelmenu=1
The system bus runs at 1333 MHz
System was configured with Asus EN8800GTX discrete graphics card
Binaries were built on Windows Vista32
The following VS 2005 SP1 updates were applied: KB926601 and KB932232
The start command with the /affinity switch was used to bind processes to cores
OMP_NUM_THREADS was set to number of logical processors as seen by the OS
KMP_AFFINITY was set to physical,0
submit was disabled for 462.libquantum at peak.
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

C++ benchmarks:

```
icl -Qvc8
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`

Continued on next page

Base Portability Flags (Continued)

464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Base Optimization Flags

C benchmarks:
-fast -Qvec-guard-write /F512000000

C++ benchmarks:
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc8 -Qc99

C++ benchmarks:
icl -Qvc8

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qprefetch /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Continued on next page

Peak Optimization Flags (Continued)

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch /F512000000

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000

429.mcf: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo -Qprec-div- -Qansi-alias /F512000000

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias -Qopt-multi-version-aggressive /F512000000

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4 /F512000000

462.libquantum: -fast -Qunroll4 -Ob0 -Qprefetch -Qopt-streaming-stores:always -Qparallel -Qpar-runtime-control /F512000000

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qopt-ra-region-strategy=block -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qopt-ra-region-strategy=routine -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.html

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.xml`

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