



SPEC® CINT2006 Result

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IBM Corporation

SPECint®2006 = 17.8

IBM BladeCenter HS21 (Intel Xeon E5335)

SPECint_base2006 = 15.2

CPU2006 license: 11

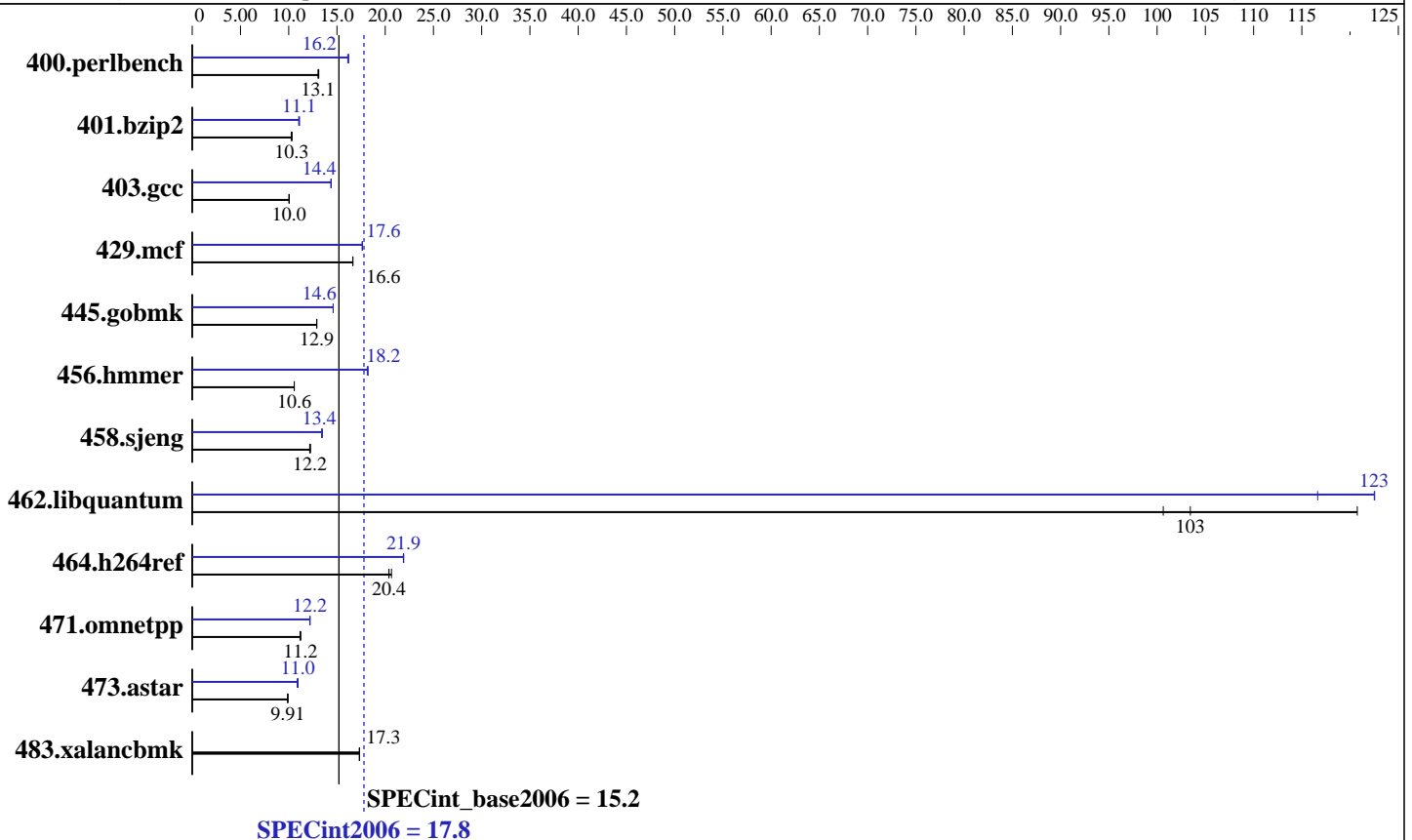
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Sep-2007

Hardware Availability: Feb-2007

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5335
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)
 Disk Subsystem: 1 x 36 GB SAS, 10000 RPM
 Other Hardware: Memory and I/O Expansion Unit (P/N 42C1600)

Software

Operating System: SLES 10 (x86_64), 2.6.16.21-0.8-smp
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20070824
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap 8.1



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	748	13.1	745	13.1	<u>748</u>	<u>13.1</u>	603	16.2	606	16.1	<u>603</u>	<u>16.2</u>
401.bzip2	<u>935</u>	<u>10.3</u>	941	10.3	934	10.3	868	11.1	875	11.0	<u>871</u>	<u>11.1</u>
403.gcc	802	10.0	<u>802</u>	<u>10.0</u>	801	10.1	559	14.4	<u>560</u>	<u>14.4</u>	560	14.4
429.mcf	547	16.7	<u>548</u>	<u>16.6</u>	548	16.6	517	17.7	518	17.6	<u>517</u>	<u>17.6</u>
445.gobmk	<u>813</u>	<u>12.9</u>	813	12.9	813	12.9	<u>718</u>	<u>14.6</u>	718	14.6	718	14.6
456.hmmmer	882	10.6	883	10.6	<u>883</u>	<u>10.6</u>	<u>513</u>	<u>18.2</u>	512	18.2	514	18.1
458.sjeng	994	12.2	986	12.3	<u>993</u>	<u>12.2</u>	897	13.5	901	13.4	<u>900</u>	<u>13.4</u>
462.libquantum	206	101	172	121	<u>200</u>	<u>103</u>	169	123	<u>169</u>	<u>123</u>	178	117
464.h264ref	1086	20.4	<u>1085</u>	<u>20.4</u>	1072	20.7	1009	21.9	1011	21.9	<u>1011</u>	<u>21.9</u>
471.omnetpp	556	11.2	<u>557</u>	<u>11.2</u>	560	11.2	<u>512</u>	<u>12.2</u>	513	12.2	512	12.2
473.astar	708	9.92	<u>708</u>	<u>9.91</u>	710	9.88	<u>641</u>	<u>11.0</u>	646	10.9	640	11.0
483.xalancbmk	<u>398</u>	<u>17.3</u>	398	17.3	398	17.3	<u>398</u>	<u>17.3</u>	398	17.3	398	17.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast -vec-guard-write -opt-malloc-options=3 -parallel
-par-runtime-control

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

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Peak Optimization Flags (Continued)

456.hmmcr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmarheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.02.xml>



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