



# SPEC<sup>®</sup> CFP2006 Result

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## Dell Inc.

SPECfp<sup>®</sup>\_rate2006 = 89.8

PowerEdge R900 (Intel Xeon E7220, 2.93 GHz)

SPECfp\_rate\_base2006 = 83.2

CPU2006 license: 55

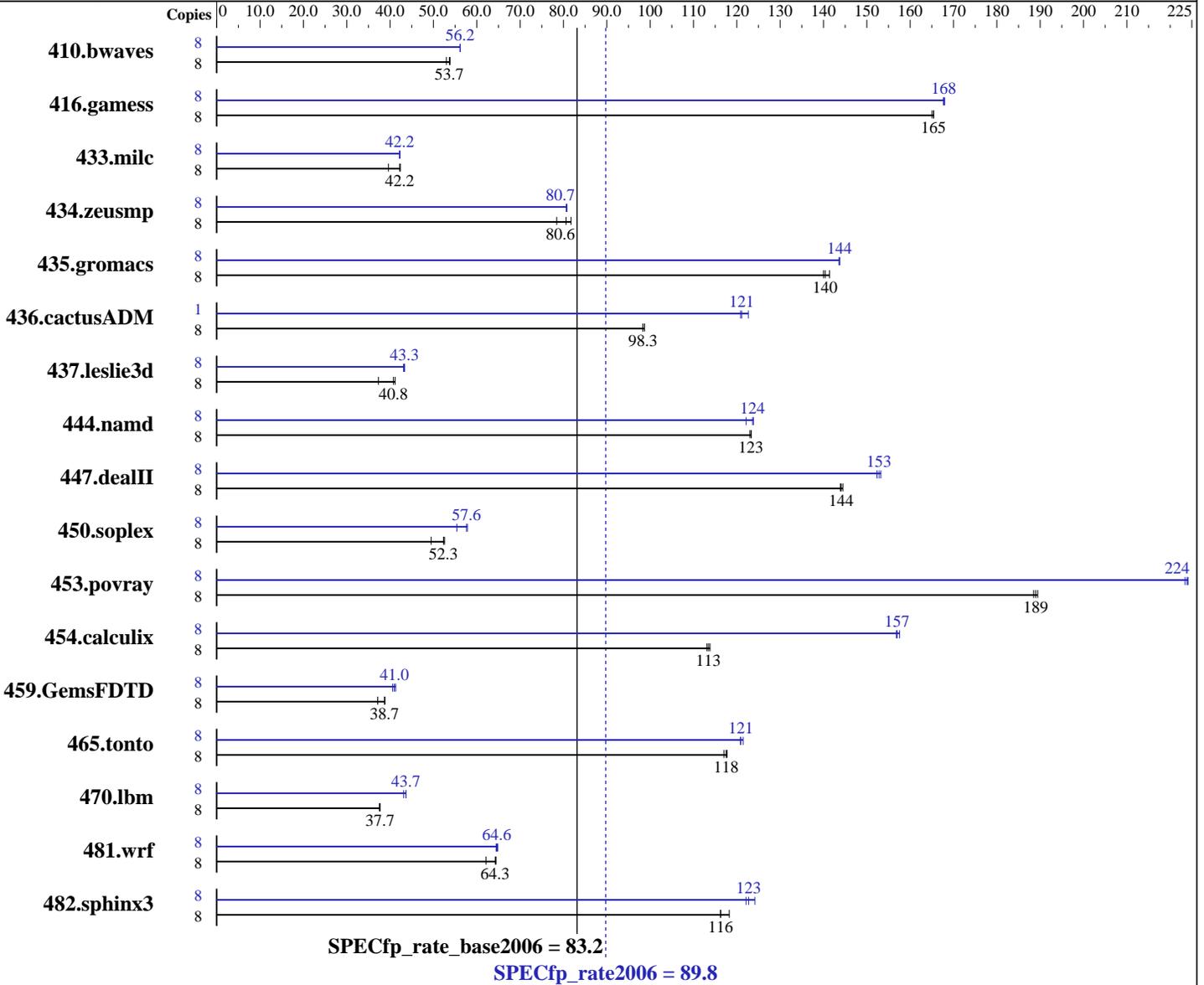
Test date: Oct-2007

Test sponsor: Dell Inc.

Hardware Availability: Nov-2007

Tested by: Dell Inc.

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon E7220  
 CPU Characteristics: 1066 MHz system bus  
 CPU MHz: 2933  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip  
 CPU(s) orderable: 2,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per core

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### Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10 SP1  
 Compiler: Intel C++ and Fortran Compiler for Linux32 and Linux64 version 10.1 Build 20070725  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Multi-user, run level 3  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit

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L3 Cache: None  
Other Cache: None  
Memory: 16 GB (16 x 1GB 667 MHz ECC CL5 FB-DIMM)  
Disk Subsystem: 1 x 73 GB SAS 15K RPM  
Other Hardware: None

Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	2051	53.0	<u>2025</u>	<u>53.7</u>	2019	53.8	8	1934	56.2	1937	56.1	<u>1935</u>	<u>56.2</u>
416.gamess	8	947	165	949	165	<u>947</u>	<u>165</u>	8	934	168	932	168	<u>934</u>	<u>168</u>
433.milc	8	1854	39.6	<u>1739</u>	<u>42.2</u>	1732	42.4	8	1741	42.2	1738	42.3	<u>1738</u>	<u>42.2</u>
434.zeusmp	8	928	78.4	890	81.8	<u>903</u>	<u>80.6</u>	8	902	80.7	<u>902</u>	<u>80.7</u>	901	80.8
435.gromacs	8	<u>407</u>	<u>140</u>	404	141	408	140	8	398	144	<u>397</u>	<u>144</u>	397	144
436.cactusADM	8	972	98.3	<u>972</u>	<u>98.3</u>	968	98.7	1	<u>98.6</u>	<u>121</u>	98.8	121	97.4	123
437.leslie3d	8	2016	37.3	<u>1845</u>	<u>40.8</u>	1826	41.2	8	1734	43.4	1745	43.1	<u>1736</u>	<u>43.3</u>
444.namd	8	521	123	<u>520</u>	<u>123</u>	520	123	8	<u>519</u>	<u>124</u>	518	124	525	122
447.dealII	8	636	144	<u>635</u>	<u>144</u>	634	144	8	597	153	601	152	<u>599</u>	<u>153</u>
450.soplex	8	1348	49.5	<u>1275</u>	<u>52.3</u>	1269	52.6	8	1204	55.4	<u>1158</u>	<u>57.6</u>	1154	57.8
453.povray	8	226	188	<u>225</u>	<u>189</u>	225	189	8	190	223	<u>190</u>	<u>224</u>	190	224
454.calculix	8	584	113	<u>582</u>	<u>113</u>	580	114	8	419	158	<u>420</u>	<u>157</u>	421	157
459.GemsFDTD	8	2284	37.2	2187	38.8	<u>2192</u>	<u>38.7</u>	8	2055	41.3	2089	40.6	<u>2070</u>	<u>41.0</u>
465.tonto	8	<u>670</u>	<u>118</u>	672	117	668	118	8	652	121	<u>651</u>	<u>121</u>	648	121
470.lbm	8	2930	37.5	2915	37.7	<u>2916</u>	<u>37.7</u>	8	2547	43.2	2515	43.7	<u>2517</u>	<u>43.7</u>
481.wrf	8	1438	62.1	<u>1391</u>	<u>64.3</u>	1387	64.4	8	1378	64.8	<u>1382</u>	<u>64.6</u>	1386	64.5
482.sphinx3	8	1318	118	1341	116	<u>1341</u>	<u>116</u>	8	1277	122	1256	124	<u>1270</u>	<u>123</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
'/usr/bin/taskset' used to bind processes to CPUs

## Platform Notes

BIOS Settings:  
Adjacent Cache Line Prefetch = Disabled (default Enabled)  
Hardware Prefetcher = Disabled (default Enabled)



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## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-fast

Fortran benchmarks:

-fast

Benchmarks using both Fortran and C:

-fast



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## Peak Compiler Invocation

C benchmarks (except as noted below):

```
/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/include
```

433.milc: icc

C++ benchmarks (except as noted below):

icpc

```
450.soplex: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/bin/icpc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/include
```

Fortran benchmarks (except as noted below):

ifort

```
437.leslie3d: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/bin/ifort
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/include
```

Benchmarks using both Fortran and C:

icc ifort

## Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

## Peak Optimization Flags

C benchmarks:

```
433.milc: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32
```

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## Peak Optimization Flags (Continued)

470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-scalar-rep- -prefetch -opt-malloc-options=3

482.sphinx3: -fast -unroll2

### C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias  
-auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast  
-opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4  
-ansi-alias

### Fortran benchmarks:

410.bwaves: -fast -prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0  
-ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch  
-opt-malloc-options=3

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0  
-prefetch

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto

### Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch  
-auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

481.wrf: -fast -auto-ilp32



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.22.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.22.xml>

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