



# SPEC® CINT2006 Result

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## IBM Corporation

**SPECint®2006 = 14.3**

IBM BladeCenter HS21 XM (Intel Xeon E5310)

**SPECint\_base2006 = 12.3**

CPU2006 license: 11

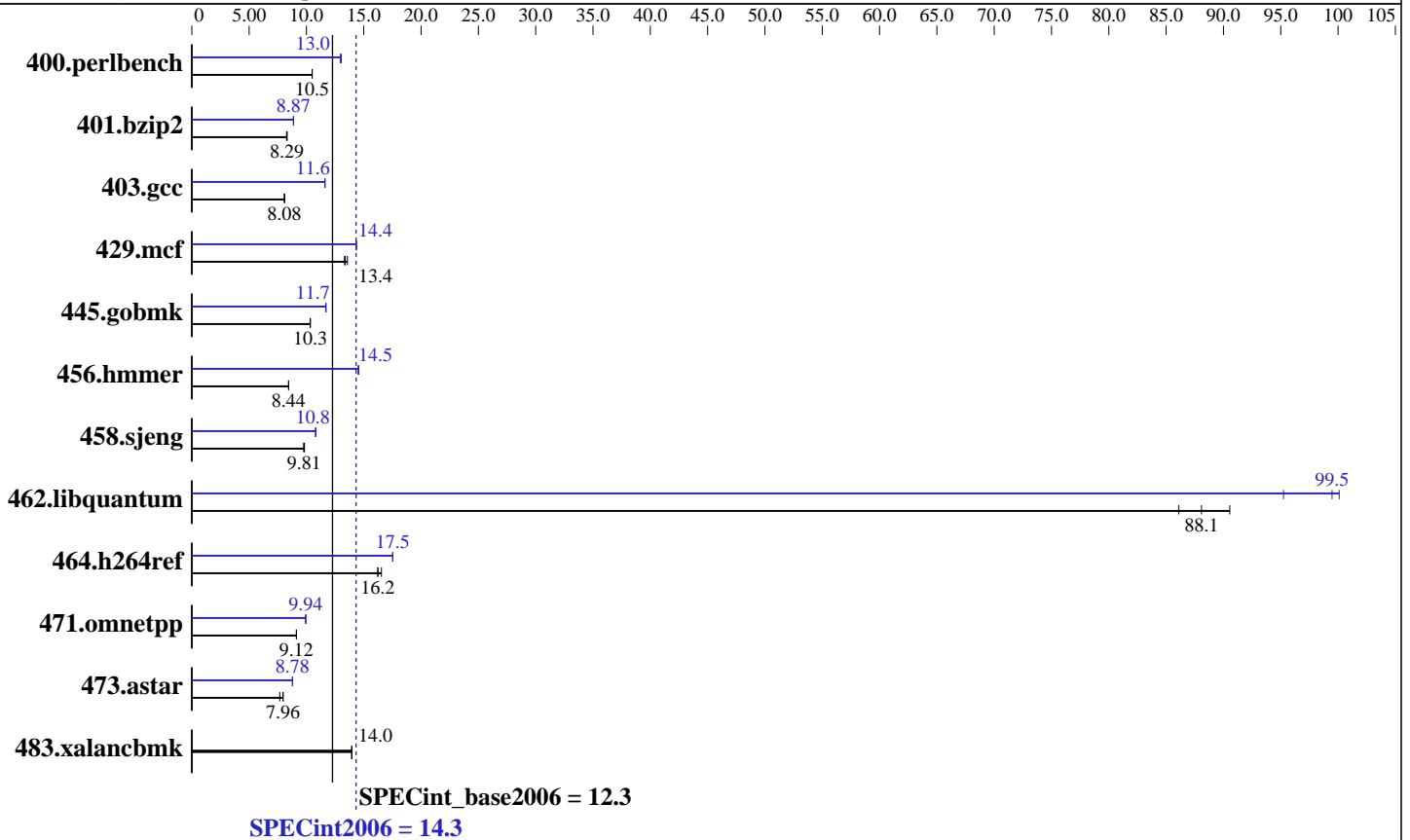
Test date: Aug-2007

Test sponsor: IBM Corporation

Hardware Availability: Feb-2007

Tested by: IBM Corporation

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon E5310  
 CPU Characteristics: 1066MHz system bus  
 CPU MHz: 1600  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 x 2GB DRR2-5300F ECC)  
 Disk Subsystem: 1 x 36 GB SAS, 10000 RPM  
 Other Hardware: None

### Software

Operating System: SLES 10 (x86\_64), 2.6.16.21-0.8-smp  
 Compiler: Intel C++ Compiler for Linux version 10.1  
 Build 20070824  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Multi-user, run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap 8.1



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<u>931</u>	<u>10.5</u>	931	10.5	931	10.5	754	12.9	<u>751</u>	<u>13.0</u>	749	13.0
401.bzip2	1161	8.31	<u>1164</u>	<u>8.29</u>	1167	8.27	1089	8.86	1087	8.88	<u>1088</u>	<u>8.87</u>
403.gcc	<u>996</u>	<u>8.08</u>	997	8.08	995	8.09	<u>693</u>	<u>11.6</u>	694	11.6	693	11.6
429.mcf	685	13.3	<u>682</u>	<u>13.4</u>	672	13.6	635	14.4	637	14.3	<u>635</u>	<u>14.4</u>
445.gobmk	1015	10.3	<u>1015</u>	<u>10.3</u>	1015	10.3	897	11.7	897	11.7	<u>897</u>	<u>11.7</u>
456.hammer	<u>1105</u>	<u>8.44</u>	1104	8.45	1105	8.44	<u>641</u>	<u>14.5</u>	643	14.5	641	14.6
458.sjeng	1231	9.83	<u>1234</u>	<u>9.81</u>	1241	9.75	1118	10.8	1121	10.8	<u>1120</u>	<u>10.8</u>
462.libquantum	241	86.1	<u>235</u>	<u>88.1</u>	229	90.6	218	95.2	<u>208</u>	<u>99.5</u>	207	100
464.h264ref	<u>1363</u>	<u>16.2</u>	1338	16.5	1364	16.2	1263	17.5	1262	17.5	<u>1263</u>	<u>17.5</u>
471.omnetpp	685	9.12	<u>685</u>	<u>9.12</u>	684	9.13	628	9.94	630	9.92	<u>629</u>	<u>9.94</u>
473.astar	914	7.68	881	7.96	<u>882</u>	<u>7.96</u>	799	8.79	800	8.77	<u>799</u>	<u>8.78</u>
483.xalanbmk	494	14.0	495	13.9	<u>494</u>	<u>14.0</u>	494	14.0	495	13.9	<u>494</u>	<u>14.0</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to physical,0

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalanbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -vec-guard-write -opt-malloc-options=3 -parallel  
-par-runtime-control

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## Base Optimization Flags (Continued)

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

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## Peak Optimization Flags (Continued)

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch  
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive  
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.28.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.28.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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