



SPEC® CINT2006 Result

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Supermicro Motherboard X7DB3

SPECint®_rate2006 = 80.2

SPECint_rate_base2006 = 67.8

CPU2006 license: 001176

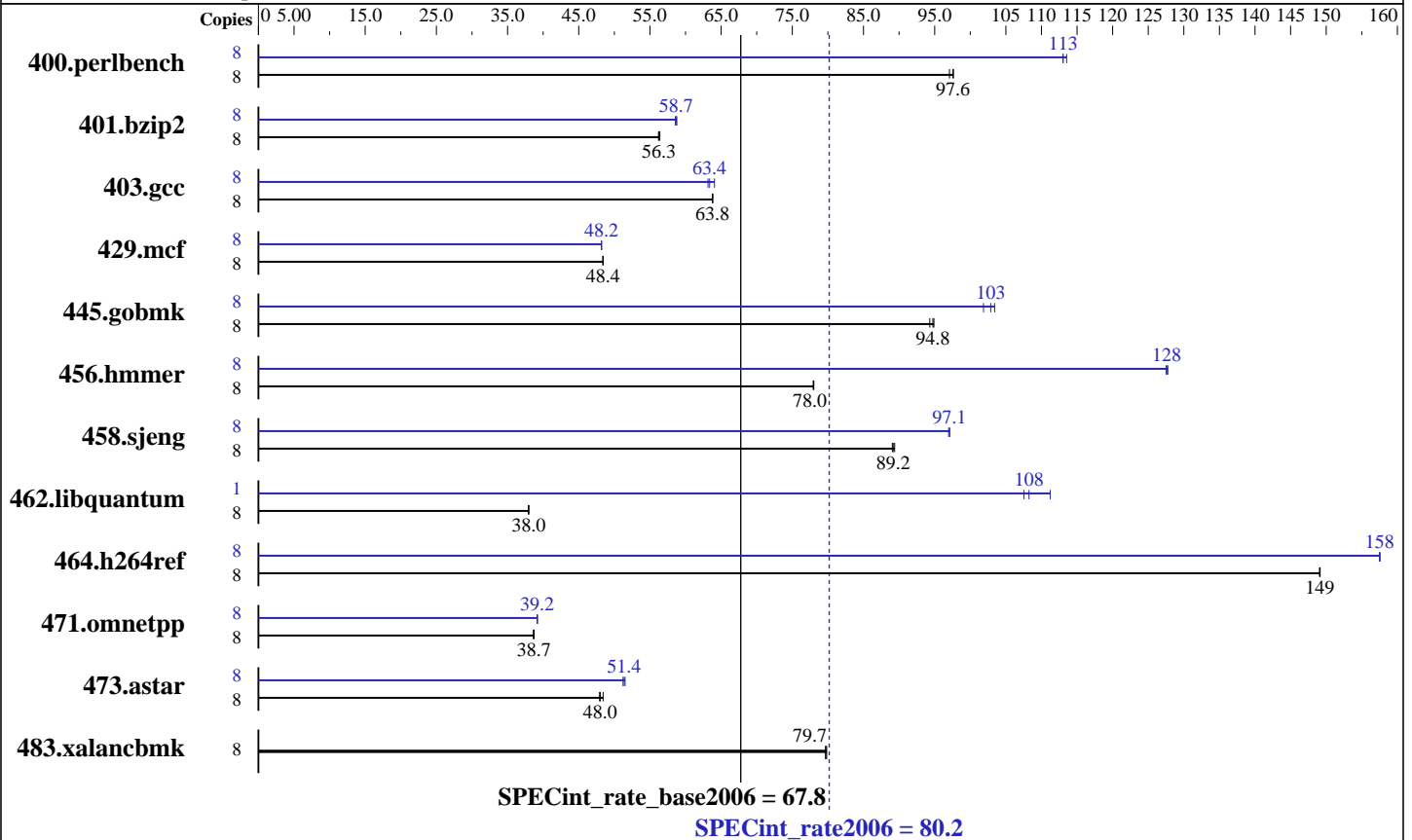
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Sep-2007

Hardware Availability: May-2006

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5320
 CPU Characteristics: Quad Core, 1.86GHz
 CPU MHz: 1860
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 * 2 GB DDR2 5300, CL-5-5-5, ECC)
 Disk Subsystem: 500 GB SATA, 7200RPM
 Other Hardware: None

Software

Operating System: 64-Bit Suse Linux Enterprise Server 10 w/ SP1
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: None



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	801	97.6	801	97.6	805	97.1	8	691	113	691	113	688	114
401.bzip2	8	1372	56.3	1374	56.2	1369	56.4	8	1318	58.6	1313	58.8	1315	58.7
403.gcc	8	1009	63.8	1010	63.8	1009	63.8	8	1005	64.1	1016	63.4	1020	63.2
429.mcf	8	1507	48.4	1507	48.4	1508	48.4	8	1514	48.2	1513	48.2	1514	48.2
445.gobmk	8	886	94.8	884	94.9	890	94.3	8	816	103	811	103	824	102
456.hmmmer	8	957	78.0	957	78.0	958	77.9	8	585	128	585	128	584	128
458.sjeng	8	1083	89.4	1087	89.1	1086	89.2	8	997	97.1	997	97.1	998	97.0
462.libquantum	8	4366	38.0	4365	38.0	4369	37.9	1	193	108	191	108	186	111
464.h264ref	8	1187	149	1187	149	1188	149	8	1124	158	1124	157	1124	158
471.omnetpp	8	1291	38.7	1294	38.6	1292	38.7	8	1276	39.2	1276	39.2	1277	39.2
473.astar	8	1160	48.4	1172	47.9	1169	48.0	8	1096	51.2	1093	51.4	1090	51.5
483.xalancbmk	8	692	79.8	693	79.7	693	79.7	8	692	79.8	693	79.7	693	79.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with CSE-825TQ-R700LPV case,
 To ensure system stability, a 500W (minimum) ATX power supply [4-pin (+12V), 8-pin (+12V) and 24-pin are required]
 Product description located as of <http://www.supermicro.com/products/motherboard/Xeon1333/5000P/X7DB3.cfm>
 The system bus runs at 1333 MHz
 Bios settings:
 Hardware Prefetcher: Enabled
 Adjacent Sector Prefetch: Disabled
 All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmmer,
 for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:
 icc
 C++ benchmarks:
 icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3
C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc
401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.29.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.29.xml>

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