



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®_rate2006 = 64.0

ProLiant DL380 G5
(2.66 GHz, Intel Xeon processor 5150)

SPECint_rate_base2006 = 56.0

CPU2006 license: 3

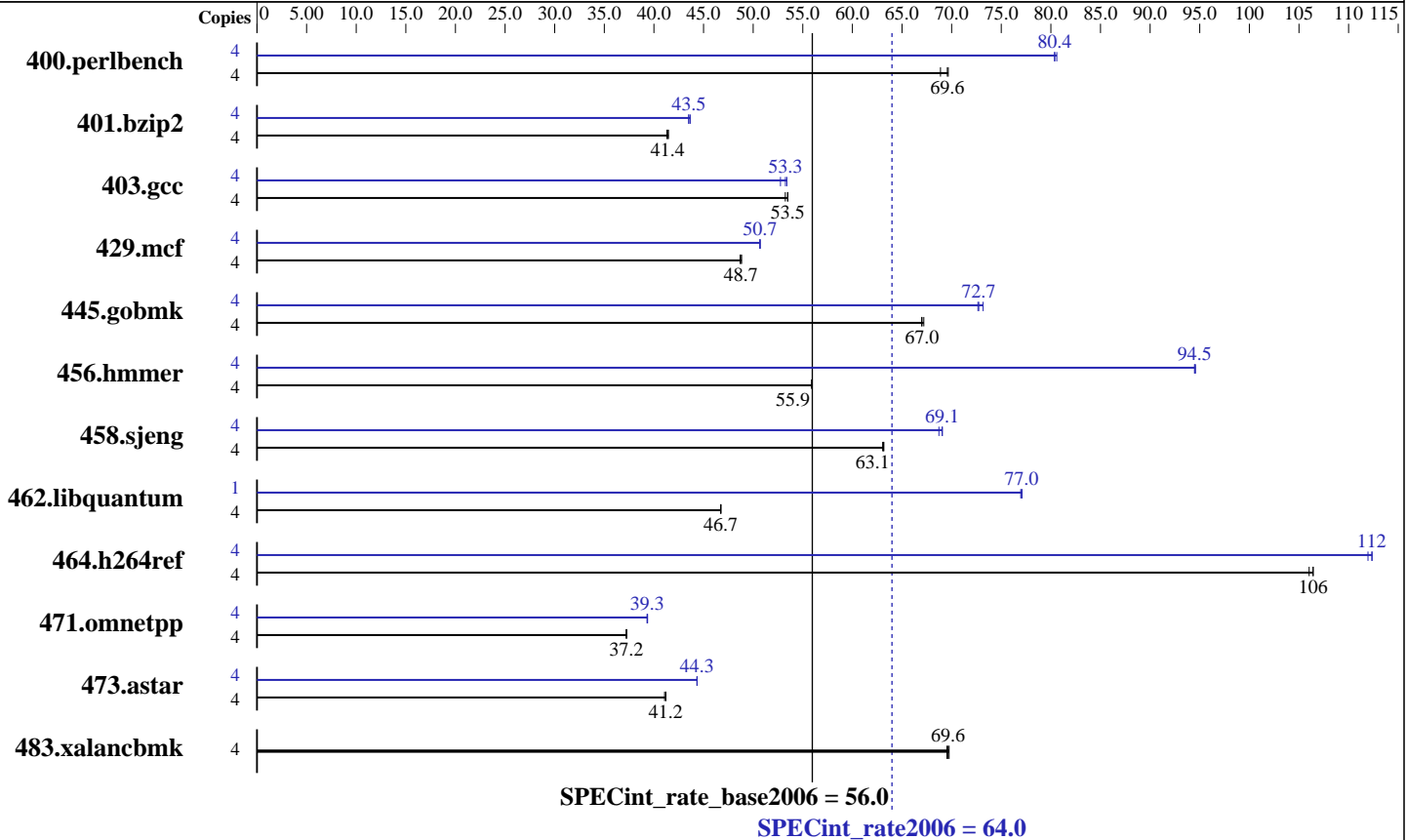
Test date: Aug-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jun-2006

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon 5150
 CPU Characteristics: 2.66 GHz, 2x4 MB L2 shared, 1333 MHz system bus
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1 or 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (8x1 GB PC2-5300F CL5)
 Disk Subsystem: 1x72 GB 10 K SAS
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) SP1
 kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64
 version 10.1
 Build 20070725
 Auto Parallel: Yes
 File System: ext2
 System State: Multi-user run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library 8.1
 binutils-2.17.50



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	562	69.6	561	69.7	567	68.9	4	485	80.6	486	80.4	486	80.4
401.bzip2	4	935	41.3	931	41.5	933	41.4	4	884	43.7	887	43.5	887	43.5
403.gcc	4	605	53.2	602	53.5	602	53.5	4	610	52.7	605	53.3	603	53.4
429.mcf	4	747	48.8	748	48.7	749	48.7	4	719	50.7	720	50.7	720	50.7
445.gobmk	4	626	67.0	626	67.0	624	67.2	4	578	72.6	573	73.2	577	72.7
456.hammer	4	668	55.9	667	55.9	667	55.9	4	395	94.6	395	94.5	395	94.5
458.sjeng	4	766	63.2	767	63.1	768	63.0	4	701	69.1	704	68.7	701	69.1
462.libquantum	4	1774	46.7	1774	46.7	1773	46.8	1	269	77.0	269	77.0	269	77.1
464.h264ref	4	835	106	832	106	832	106	4	788	112	791	112	788	112
471.omnetpp	4	672	37.2	671	37.2	671	37.2	4	635	39.3	635	39.4	636	39.3
473.astar	4	682	41.2	684	41.1	682	41.2	4	633	44.3	633	44.4	633	44.3
483.xalancbmk	4	396	69.7	397	69.5	396	69.6	4	396	69.7	397	69.5	396	69.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M

Platform Notes

BIOS configuration:
Power Regulator set to Static High Performance Mode
Adjacent Sector Prefetch Disabled

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

Continued on next page



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Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-flags.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-flags.xml>

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For other inquiries, please contact webmaster@spec.org.

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