



SPEC® CINT2006 Result

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Hewlett-Packard Company ProLiant DL380 G5 (2.66 GHz, Intel Xeon processor 5150)	SPECint®_rate2006 = 33.6 SPECint_rate_base2006 = 29.5
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CPU2006 license: 3

Test sponsor: Hewlett-Packard Company

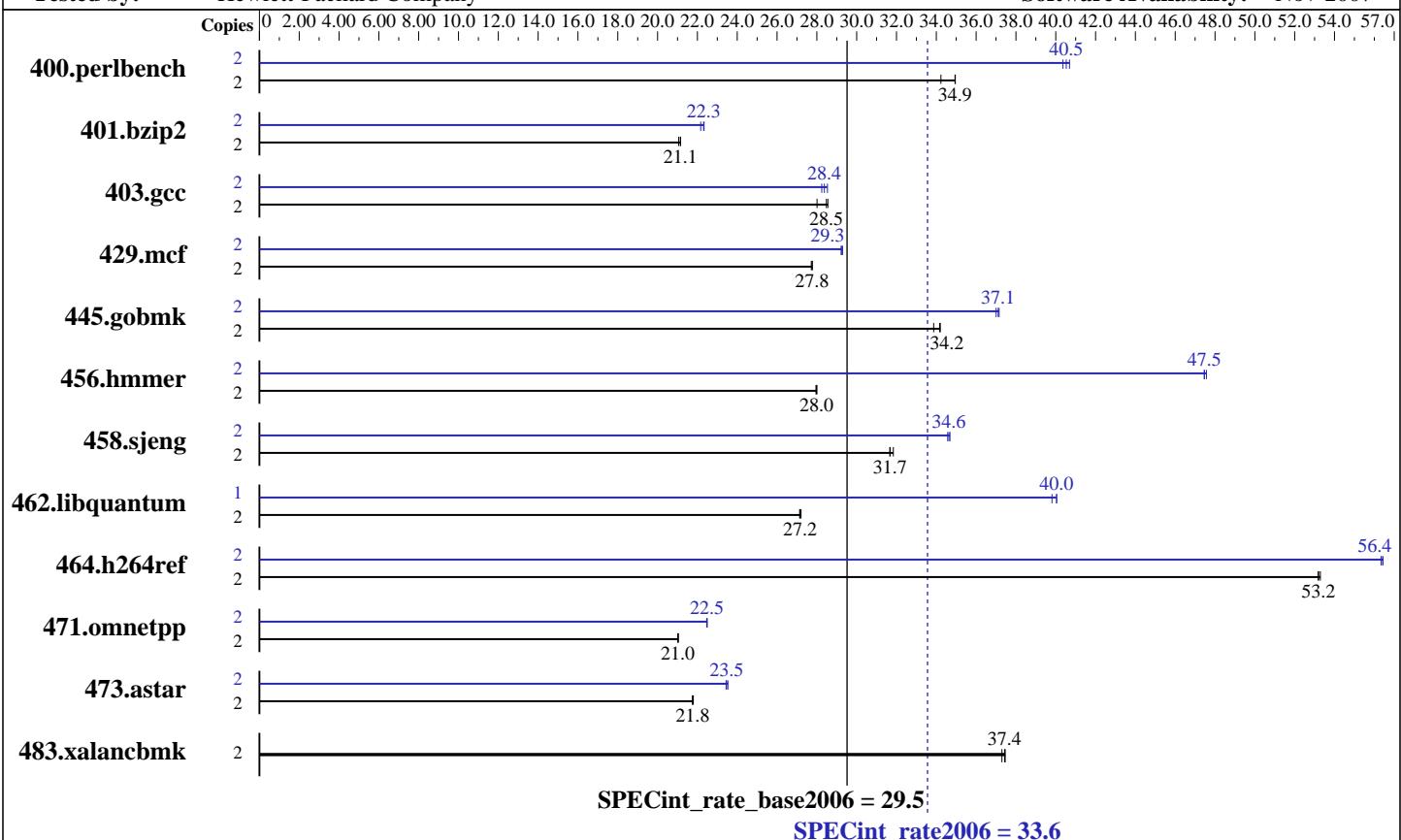
Tested by: Hewlett-Packard Company

Test date: Aug-2007

Aug-2007

Hardware Availability: Jun-2006

Software Availability: Nov-2007



Hardware		Software	
CPU Name:	Intel Xeon 5150	Operating System:	SuSE Linux Enterprise Server 10 (x86_64) SP1 kernel 2.6.16.46-0.12-smp
CPU Characteristics:	2.66 GHz, 2x4 MB L2 shared, 1333 MHz system bus	Compiler:	Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725
CPU MHz:	2666	Auto Parallel:	Yes
FPU:	Integrated	File System:	ext2
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip	System State:	Multi-user run level 3
CPU(s) orderable:	1 or 2 chips	Base Pointers:	32-bit
Primary Cache:	32 KB I + 32 KB D on chip per core	Peak Pointers:	32/64-bit
Secondary Cache:	4 MB I+D on chip per chip	Other Software:	MicroQuill SmartHeap Library 8.1 binutils-2.17.50
L3 Cache:	None		
Other Cache:	None		
Memory:	8 GB (8x1 GB PC2-5300F CL5)		
Disk Subsystem:	1x72 GB 10 K SAS		
Other Hardware:	None		



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	559	35.0	571	34.2	559	34.9	2	482	40.5	484	40.4	480	40.7
401.bzip2	2	912	21.2	916	21.1	913	21.1	2	864	22.3	865	22.3	871	22.2
403.gcc	2	565	28.5	575	28.0	564	28.6	2	570	28.2	564	28.5	567	28.4
429.mcf	2	657	27.8	658	27.7	657	27.8	2	624	29.2	623	29.3	623	29.3
445.gobmk	2	614	34.2	613	34.2	619	33.9	2	565	37.1	567	37.0	565	37.2
456.hammer	2	666	28.0	667	28.0	667	28.0	2	393	47.5	392	47.6	393	47.5
458.sjeng	2	760	31.8	764	31.7	764	31.7	2	699	34.6	700	34.6	698	34.7
462.libquantum	2	1526	27.2	1527	27.1	1523	27.2	1	517	40.0	520	39.8	517	40.0
464.h264ref	2	832	53.2	831	53.3	832	53.2	2	784	56.4	786	56.3	785	56.4
471.omnetpp	2	594	21.0	595	21.0	595	21.0	2	556	22.5	556	22.5	556	22.5
473.astar	2	644	21.8	645	21.8	645	21.8	2	598	23.5	597	23.5	599	23.4
483.xalancbmk	2	369	37.4	368	37.5	370	37.3	2	369	37.4	368	37.5	370	37.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M

Platform Notes

BIOS configuration:

Power Regulator set to Static High Performance Mode
Adjacent Sector Prefetch Disabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

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Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmpllr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmr: /home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmr: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -O0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-flags.xml>

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