SPEC® CINT2006 Result

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Hewlett-Packard Company

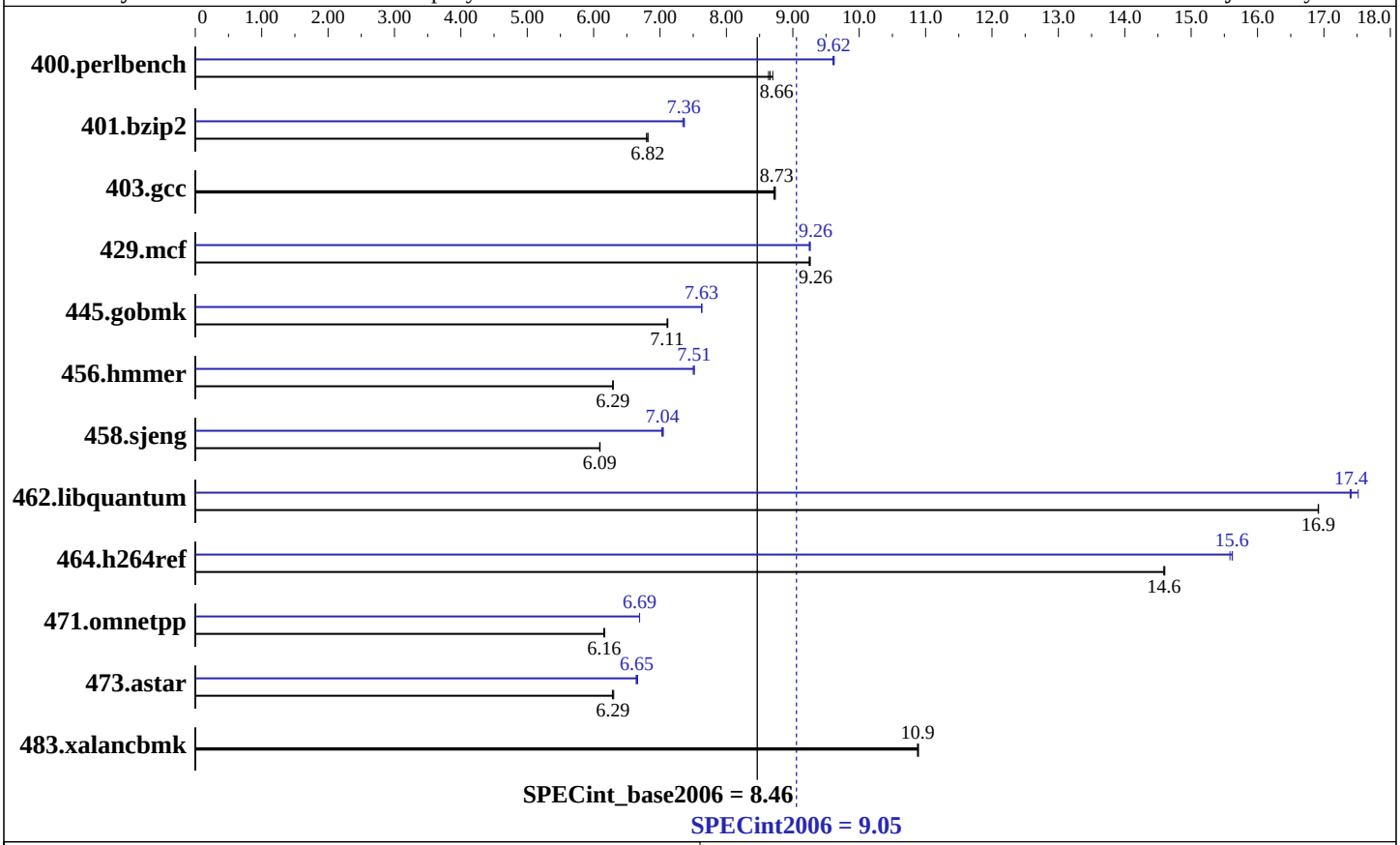
ProLiant DL580 G4
(2.6 GHz, Intel Xeon processor 7110M)

SPECint®2006 = 9.05

SPECint_base2006 = 8.46

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Sep-2007
Hardware Availability: Aug-2006
Software Availability: May-2007



Hardware

CPU Name:	Intel Xeon 7110M
CPU Characteristics:	2.6 GHz, 800 MHz system bus
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1,2,4 chips
Primary Cache:	12 K micro-ops I + 16 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (16x2 GB PC2-3200R)
Disk Subsystem:	1x72 GB 15K SAS
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for IA32/EM64T applications, Version 10.0 Build 20070426 Package ID: l_cc_p_10.0.023
Auto Parallel:	No
File System:	ext2
System State:	Multi-user run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap 8.1 libraries

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**1129**	**8.66**	1123	8.70	1132	8.63	1015	9.62	1017	9.60	**1016**	**9.62**
401.bzip2	1416	6.82	**1416**	**6.82**	1420	6.80	1314	7.34	1310	7.37	**1311**	**7.36**
403.gcc	921	8.74	924	8.72	**922**	**8.73**	921	8.74	924	8.72	**922**	**8.73**
429.mcf	986	9.25	**985**	**9.26**	985	9.26	985	9.26	986	9.25	**985**	**9.26**
445.gobmk	1474	7.12	**1476**	**7.11**	1476	7.11	**1375**	**7.63**	1376	7.63	1375	7.63
456.hmmer	1483	6.29	1484	6.29	**1483**	**6.29**	1244	7.50	1241	7.52	**1242**	**7.51**
458.sjeng	1985	6.10	**1986**	**6.09**	1987	6.09	1717	7.05	1722	7.03	**1719**	**7.04**
462.libquantum	**1225**	**16.9**	1225	16.9	1225	16.9	1191	17.4	1183	17.5	**1190**	**17.4**
464.h264ref	1515	14.6	1517	14.6	**1517**	**14.6**	1417	15.6	1420	15.6	**1417**	**15.6**
471.omnetpp	1016	6.15	**1015**	**6.16**	1014	6.16	934	6.69	934	6.69	**934**	**6.69**
473.astar	1117	6.28	**1116**	**6.29**	1114	6.30	**1056**	**6.65**	1054	6.66	1057	6.64
483.xalancbmk	**634**	**10.9**	633	10.9	634	10.9	**634**	**10.9**	633	10.9	634	10.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

```
BIOS configuration:
 Power Regulator Disabled
 Adjacent Sector Prefetch Enabled
 Hardware Prefetcher Enabled
 Hyper-Threading Technology Enabled
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xP -ipo -O3 -static -no-prec-div

C++ benchmarks:
-xP -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xP -ipo -O3 -no-prec-div -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xP -ipo -O3 -no-prec-div`

403.gcc: `basepeak = yes`

429.mcf: `-xP -ipo -O3 -static -no-prec-div -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xP -O2 -ipo -no-prec_div -ansi-alias`

456.hmmer: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xP -ipo -O3 -static -no-prec-div -unroll2 -ansi-alias`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xP -ipo -O3 -static -no-prec-div -unroll4`

462.libquantum: `Same as 458.sjeng`

464.h264ref: `Same as 456.hmmer`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xP -O3 -ipo -no-prec_div -ansi-alias -Wl,-z,muldefs -L/cpu2006/SmartHeap_8.1/lib -lsmartheap`

473.astar: `Same as 471.omnetpp`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/hp-ic10-flags.20090714.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/hp-ic10-flags.20090714.xml`

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