



SPEC® CINT2006 Result

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Intel Corporation

(Test Sponsor: Advanced Micro Devices)

Intel S5000PAL Server Board, Intel Xeon processor X5355, 2.66 GHz

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 63.0

CPU2006 license: 49

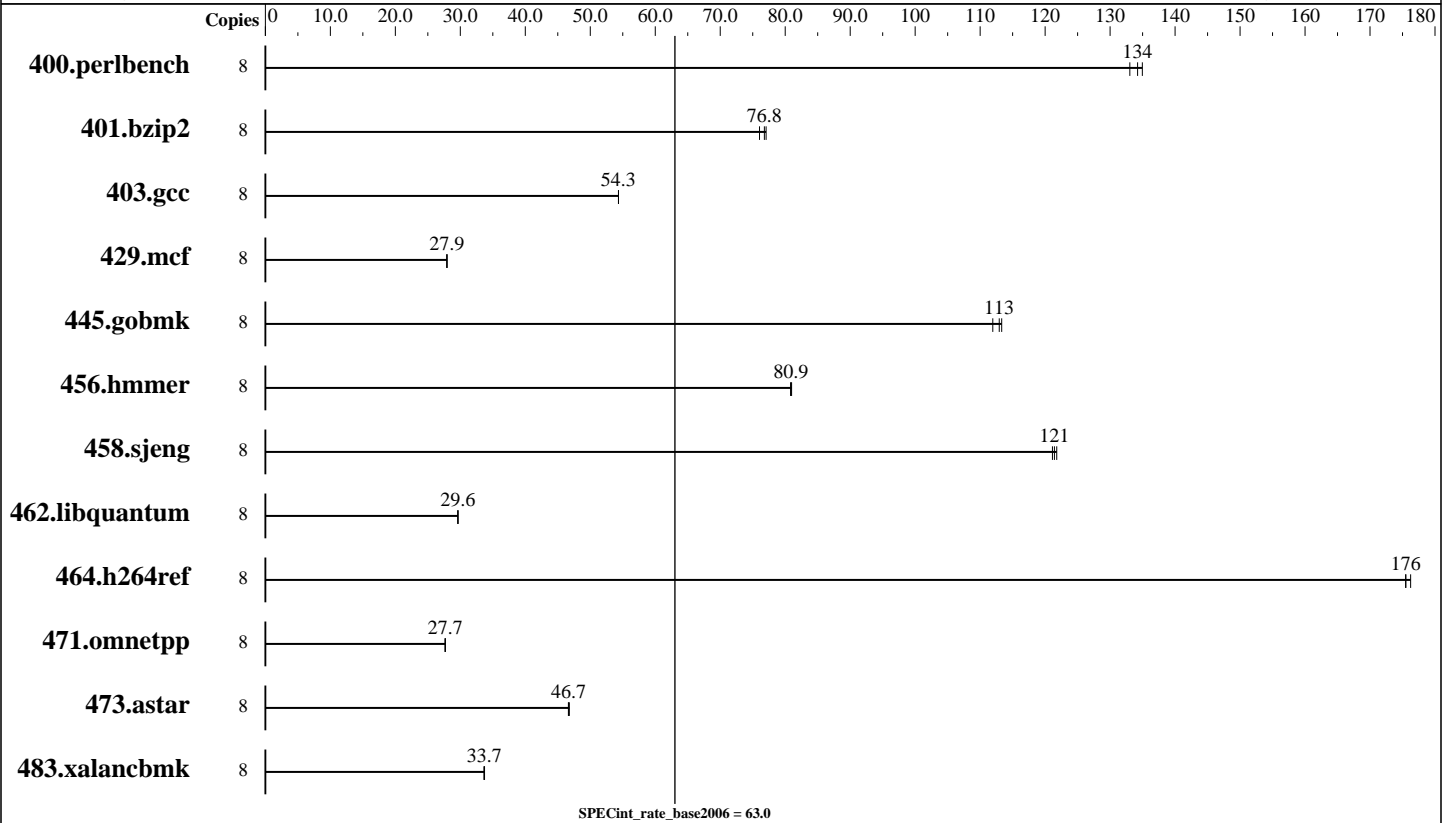
Test sponsor: Advanced Micro Devices

Tested by: Advanced Micro Devices

Test date: Sep-2007

Hardware Availability: May-2007

Software Availability: Jul-2007



Hardware

CPU Name: Intel Xeon X5355
 CPU Characteristics: 2666
 CPU MHz: Integrated
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2GB, DDR2-667 FBDIMM CL5 Reg Dual Rank)
 Disk Subsystem: 1x250GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 SP1 64-bit kernel
 Compiler: gcc, g++ 4.1.2
 Auto Parallel: No
 File System: ext3
 System State: Multi-user, run level 3
 Base Pointers: 64-bit
 Peak Pointers: Not Applicable
 Other Software: None



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	579	135	<u>582</u>	<u>134</u>	588	133							
401.bzip2	8	1002	77.1	1015	76.0	<u>1005</u>	<u>76.8</u>							
403.gcc	8	1185	54.3	1185	54.3	<u>1185</u>	<u>54.3</u>							
429.mcf	8	2613	27.9	<u>2612</u>	<u>27.9</u>	2610	28.0							
445.gobmk	8	750	112	<u>743</u>	<u>113</u>	741	113							
456.hammer	8	922	81.0	<u>923</u>	<u>80.9</u>	923	80.8							
458.sjeng	8	799	121	<u>797</u>	<u>121</u>	795	122							
462.libquantum	8	5595	29.6	5594	29.6	<u>5594</u>	<u>29.6</u>							
464.h264ref	8	1004	176	<u>1009</u>	<u>176</u>	1009	175							
471.omnetpp	8	1806	27.7	<u>1807</u>	<u>27.7</u>	1807	27.7							
473.astar	8	1204	46.6	1201	46.8	<u>1203</u>	<u>46.7</u>							
483.xalancbmk	8	1637	33.7	<u>1639</u>	<u>33.7</u>	1641	33.6							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'numactl' was used to bind copies to the cores

General Notes

"powersave -f" used to set speed to maximum

The test system can be assembled using a Delta TDPS-600AB 600W 12V power supply.

Base Compiler Invocation

C benchmarks:

gcc

C++ benchmarks:

g++

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

429.mcf: -DSPEC_CPU_LP64
 445.gobmk: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 464.h264ref: -DSPEC_CPU_LP64
 471.omnetpp: -DSPEC_CPU_LP64
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-O3 -fno-inline-functions -funroll-loops

C++ benchmarks:

-O3 -funroll-loops

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.04.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.04.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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