SPEC® CINT2006 Result
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Acer Incorporated

Acer Altos G540 (Intel Xeon E5310)

SPECint®_rate2006 = 39.3

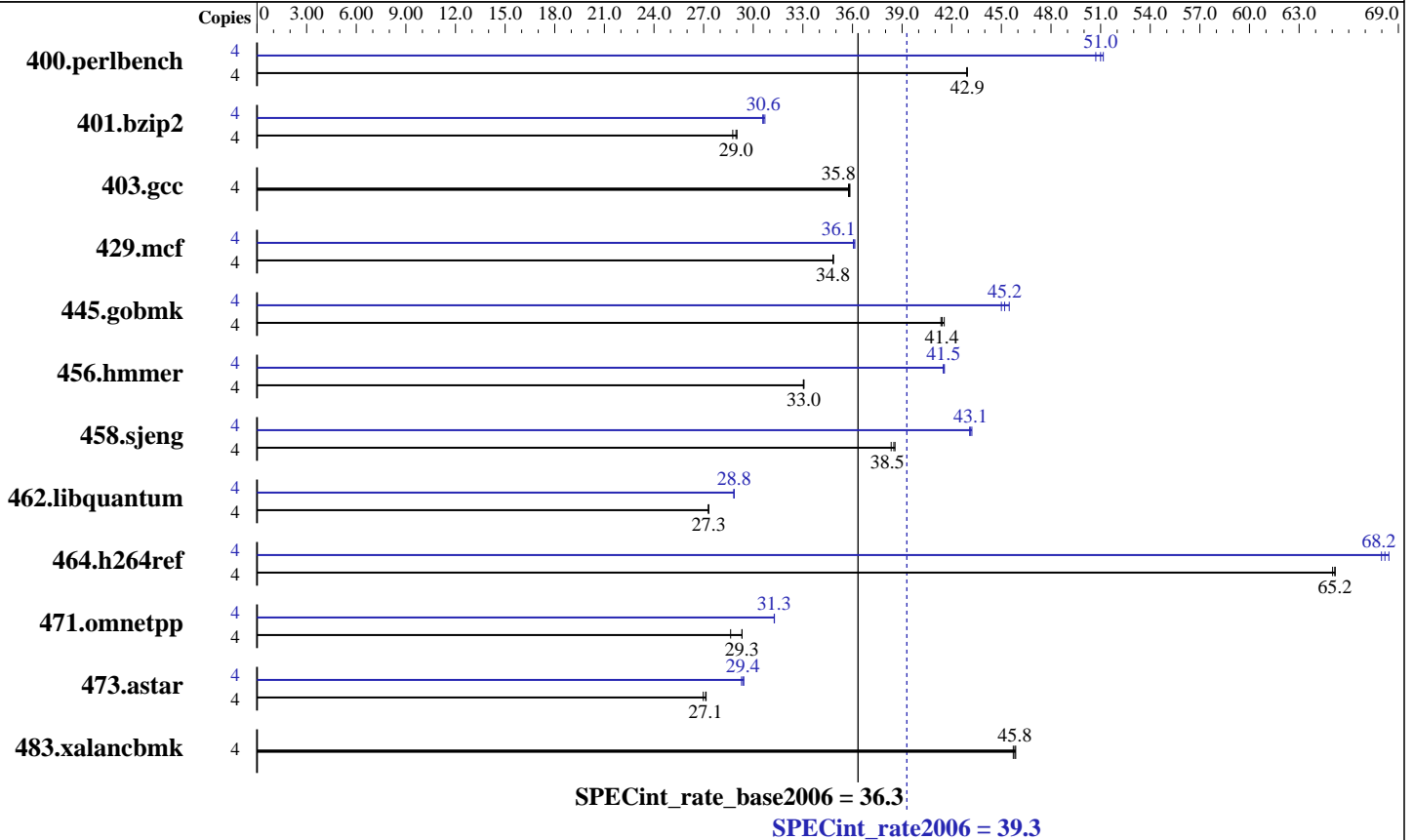SPECint_rate_base2006 = 36.3

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Jul-2007
Hardware Availability: Nov-2006
Software Availability: Jun-2007

Copies

Benchmark	Peak	Base
400.perlbench	51.0	42.9
401.bzip2	30.6	29.0
403.gcc		35.8
429.mcf	36.1	34.8
445.gobmk	45.2	41.4
456.hmmer	41.5	33.0
458.sjeng	43.1	38.5
462.libquantum	28.8	27.3
464.h264ref	68.2	65.2
471.omnetpp	31.3	29.3
473.astar	29.4	27.1
483.xalancbmk		45.8

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Hardware

CPU Name:	Intel Xeon E5310
CPU Characteristics:	1066MHz system bus
CPU MHz:	1666
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)
Disk Subsystem:	1 x 73 GB 10000RPM SAS HDD
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 EM64T (kernel 2.6.16.21-0.8-smp)
Compiler:	Intel C++ Compiler for Linux32 version 10.0 Build 20070426 Package ID: l_cc_p_10.0.023
Auto Parallel:	No
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap Library 8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	911	42.9	**910**	**42.9**	910	42.9	4	764	51.2	**766**	**51.0**	771	50.7
401.bzip2	4	1330	29.0	**1333**	**29.0**	1342	28.8	4	1257	30.7	1262	30.6	**1261**	**30.6**
403.gcc	4	898	35.8	**899**	**35.8**	900	35.8	4	898	35.8	**899**	**35.8**	900	35.8
429.mcf	4	1047	34.8	**1047**	**34.8**	1047	34.8	4	1012	36.1	1009	36.1	**1011**	**36.1**
445.gobmk	4	**1013**	**41.4**	1010	41.5	1015	41.3	4	923	45.5	932	45.0	**928**	**45.2**
456.hmmer	4	**1129**	**33.0**	1129	33.1	1130	33.0	4	900	41.5	**899**	**41.5**	898	41.6
458.sjeng	4	1263	38.3	1255	38.6	**1257**	**38.5**	4	1120	43.2	**1122**	**43.1**	1123	43.1
462.libquantum	4	3035	27.3	3037	27.3	**3036**	**27.3**	4	**2874**	**28.8**	2875	28.8	2873	28.8
464.h264ref	4	1358	65.2	**1359**	**65.2**	1361	65.0	4	1294	68.4	**1298**	**68.2**	1302	68.0
471.omnetpp	4	**853**	**29.3**	853	29.3	873	28.6	4	799	31.3	**799**	**31.3**	800	31.3
473.astar	4	1041	27.0	1034	27.2	**1035**	**27.1**	4	**956**	**29.4**	954	29.4	959	29.3
483.xalancbmk	4	602	45.9	**602**	**45.8**	604	45.7	4	602	45.9	**602**	**45.8**	604	45.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

taskset was used to bind copies to cores

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -fast

Continued on next page

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Base Optimization Flags (Continued)

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

401.bzip2: `/opt/intel/cce/10.0.023/bin/icc`
      `-L/opt/intel/cce/10.0.023/lib`
      `-I/opt/intel/cce/10.0.023/include`

456.hmmer: `/opt/intel/cce/10.0.023/bin/icc`
      `-L/opt/intel/cce/10.0.023/lib`
      `-I/opt/intel/cce/10.0.023/include`

C++ benchmarks:
```
icpc
```

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias`
      `-prefetch`

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Peak Optimization Flags (Continued)

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast`

403.gcc: basepeak = yes

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo`
`-no-prec_div -ansi-alias`

456.hmmer: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2`
`-ansi-alias`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4 -Ob0`
`-prefetch -opt-streaming-stores always`

464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo`
`-no-prec_div -ansi-alias -Wl,-z,muldefs`
`-L/spec/cpu2006.1.0/lib -lsmartheap`

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.16.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.16.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 13:07:52 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 4 September 2007.