



# SPEC<sup>®</sup> CFP2006 Result

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ASUS Computer International

(Test Sponsor: Intel Corporation)

SPECfp<sup>®</sup>\_rate2006 = 23.0

Asus G2S (Intel Core 2 Duo X7800)

SPECfp\_rate\_base2006 = 22.4

CPU2006 license: 13

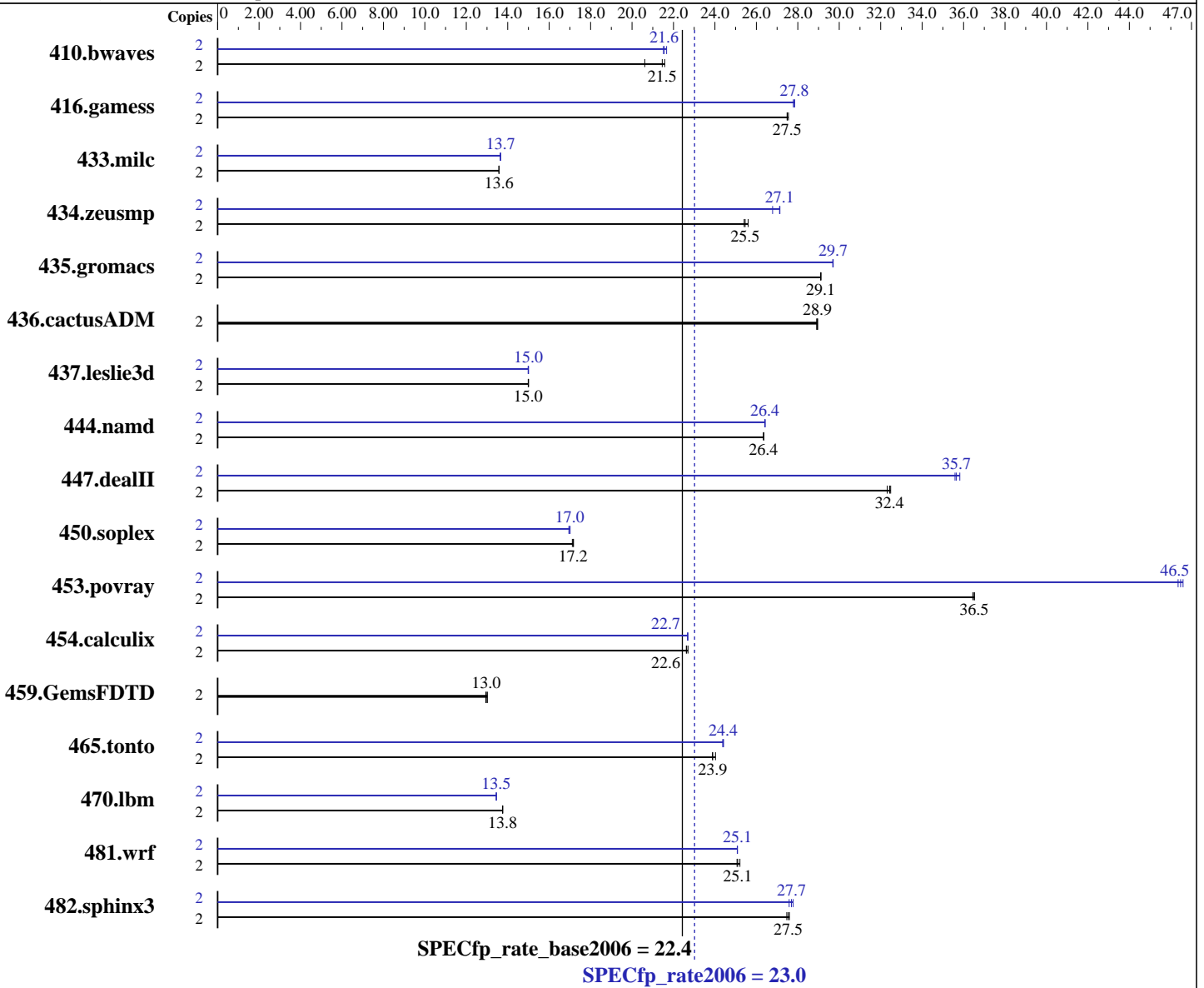
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Jun-2007

Hardware Availability: Jun-2007

Software Availability: May-2007



## Hardware

CPU Name: Intel Core 2 Duo X7800  
 CPU Characteristics: 2.60 GHz, 4MB L2, 800 MHz bus  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip

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## Software

Operating System: Windows Vista32 Ultimate  
 Compiler: Intel C++ Compiler for IA32 version 10.0  
 Build 20070426 Package ID: W\_CC\_P\_10.0.025  
 Intel Fortran Compiler for IA32 version 10.0  
 Build 20070426 Package ID: W\_FC\_P\_10.0.025  
 Microsoft Visual Studio .Net 2003 (for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default

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L3 Cache: None  
Other Cache: None  
Memory: 2 GB (2x1GB Hynix DDR2-667 CL5)  
Disk Subsystem: 160GB Hitachi SATA, 5400RPM  
Other Hardware: None

Base Pointers: 32-bit  
Peak Pointers: 32-bit  
Other Software: None  
SmartHeap Library Version 8.0 from  
<http://www.microquill.com/>

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	1319	20.6	1260	21.6	<u>1267</u>	<u>21.5</u>	2	<u>1261</u>	<u>21.6</u>	1254	21.7	1263	21.5
416.gamess	2	<u>1425</u>	<u>27.5</u>	1425	27.5	1422	27.5	2	1409	27.8	1406	27.8	<u>1407</u>	<u>27.8</u>
433.milc	2	1352	13.6	<u>1352</u>	<u>13.6</u>	1352	13.6	2	1346	13.6	<u>1345</u>	<u>13.7</u>	1344	13.7
434.zeusmp	2	711	25.6	<u>715</u>	<u>25.5</u>	716	25.4	2	<u>671</u>	<u>27.1</u>	680	26.8	671	27.1
435.gromacs	2	491	29.1	490	29.1	<u>491</u>	<u>29.1</u>	2	481	29.7	<u>481</u>	<u>29.7</u>	481	29.7
436.cactusADM	2	<u>826</u>	<u>28.9</u>	825	29.0	827	28.9	2	<u>826</u>	<u>28.9</u>	825	29.0	827	28.9
437.leslie3d	2	1254	15.0	<u>1254</u>	<u>15.0</u>	1253	15.0	2	<u>1254</u>	<u>15.0</u>	1255	15.0	1253	15.0
444.namd	2	609	26.4	609	26.3	<u>609</u>	<u>26.4</u>	2	607	26.4	<u>607</u>	<u>26.4</u>	607	26.4
447.dealII	2	<u>706</u>	<u>32.4</u>	708	32.3	704	32.5	2	639	35.8	643	35.6	<u>642</u>	<u>35.7</u>
450.soplex	2	974	17.1	<u>972</u>	<u>17.2</u>	971	17.2	2	981	17.0	984	16.9	<u>982</u>	<u>17.0</u>
453.povray	2	291	36.5	292	36.4	<u>291</u>	<u>36.5</u>	2	228	46.6	230	46.4	<u>229</u>	<u>46.5</u>
454.calculix	2	<u>729</u>	<u>22.6</u>	727	22.7	729	22.6	2	<u>727</u>	<u>22.7</u>	727	22.7	727	22.7
459.GemsFDTD	2	<u>1636</u>	<u>13.0</u>	1640	12.9	1630	13.0	2	<u>1636</u>	<u>13.0</u>	1640	12.9	1630	13.0
465.tonto	2	819	24.0	<u>824</u>	<u>23.9</u>	824	23.9	2	<u>806</u>	<u>24.4</u>	807	24.4	806	24.4
470.lbm	2	1997	13.8	1998	13.8	<u>1998</u>	<u>13.8</u>	2	<u>2043</u>	<u>13.5</u>	2043	13.5	2043	13.4
481.wrf	2	886	25.2	891	25.1	<u>890</u>	<u>25.1</u>	2	891	25.1	<u>891</u>	<u>25.1</u>	890	25.1
482.sphinx3	2	1413	27.6	1419	27.5	<u>1415</u>	<u>27.5</u>	2	1413	27.6	<u>1408</u>	<u>27.7</u>	1404	27.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

The system bus runs at 667 MHz  
System was configured with an nVIDIA 8600M GT graphics card  
Binaries were built on Windows XP Professional SP2  
The start command with the /affinity switch was used to bind processes to cores

## Base Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99  
  
C++ benchmarks:  
icl -Qvc7.1

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**Tested by:** Intel Corporation

**Test date:** Jun-2007

**Hardware Availability:** Jun-2007

**Software Availability:** May-2007

## Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc7.1 -Qc99 ifort

## Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore

444.namd: -TP

447.dealII: -DDEAL\_II\_MEMBER\_VAR\_SPECIALIZATION\_BUG  
-DBOOST\_NO\_INTRINSIC\_WCHAR\_T

453.povray: -DSPEC\_CPU\_WINDOWS\_ICL

454.calculix: -DSPEC\_CPU\_NOZMODIFIER -Qlowercase

481.wrf: -DSPEC\_CPU\_WINDOWS\_ICL

## Base Optimization Flags

C benchmarks:

-fast /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:

-fast -Qcxx\_features /F950000000 shlw32m.lib  
-link /FORCE:MULTIPLE

Fortran benchmarks:

-fast /F950000000

Benchmarks using both Fortran and C:

-fast /F950000000

## Peak Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc7.1 -Qc99 ifort



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## Peak Portability Flags

436.cactusADM: -Qlowercase /assume:underscore  
444.namd: -TP  
447.dealII: -DDEAL\_II\_MEMBER\_VAR\_SPECIALIZATION\_BUG  
-DBOOST\_NO\_INTRINSIC\_WCHAR\_T  
453.povray: -DSPEC\_CPU\_WINDOWS\_ICL  
454.calculix: -DSPEC\_CPU\_NOZMODIFIER -Qlowercase  
481.wrf: -DSPEC\_CPU\_WINDOWS\_ICL

## Peak Optimization Flags

### C benchmarks:

433.milc: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll2 -Oa  
/F950000000 shlw32m.lib -link /FORCE:MULTIPLE  
470.lbm: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll2  
-Qscalar-rep- -Qprefetch /F950000000 shlw32m.lib  
-link /FORCE:MULTIPLE  
482.sphinx3: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll2  
/F950000000 shlw32m.lib -link /FORCE:MULTIPLE

### C++ benchmarks:

444.namd: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Oa  
-Qcxx\_features /F950000000 shlw32m.lib  
-link /FORCE:MULTIPLE  
447.dealII: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qprefetch  
-Qcxx\_features /F950000000 shlw32m.lib  
-link /FORCE:MULTIPLE  
450.soplex: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qcxx\_features  
/F950000000 shlw32m.lib -link /FORCE:MULTIPLE  
453.povray: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qansi-alias  
-Qcxx\_features /F950000000 shlw32m.lib  
-link /FORCE:MULTIPLE

### Fortran benchmarks:

410.bwaves: -fast /F950000000  
416.gamess: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll2 -Ob0  
-Qansi-alias -Qscalar-rep- /F950000000  
434.zeusmp: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -QxT -O2 -Qprec\_div-  
-Qunroll0 -Qscalar-rep- /F950000000

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## Peak Optimization Flags (Continued)

437.leslie3d: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast /F950000000

459.GemsFDTD: basepeak = yes

465.tonto: Same as 437.leslie3d

Benchmarks using both Fortran and C:

435.gromacs: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Oa  
/F950000000

436.cactusADM: basepeak = yes

454.calculix: -fast /F950000000

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.18.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.18.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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