



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint®_rate2006 = 96.0

IBM BladeCenter HS21 (Intel Xeon X5355)

SPECint_rate_base2006 = 89.9

CPU2006 license: 11

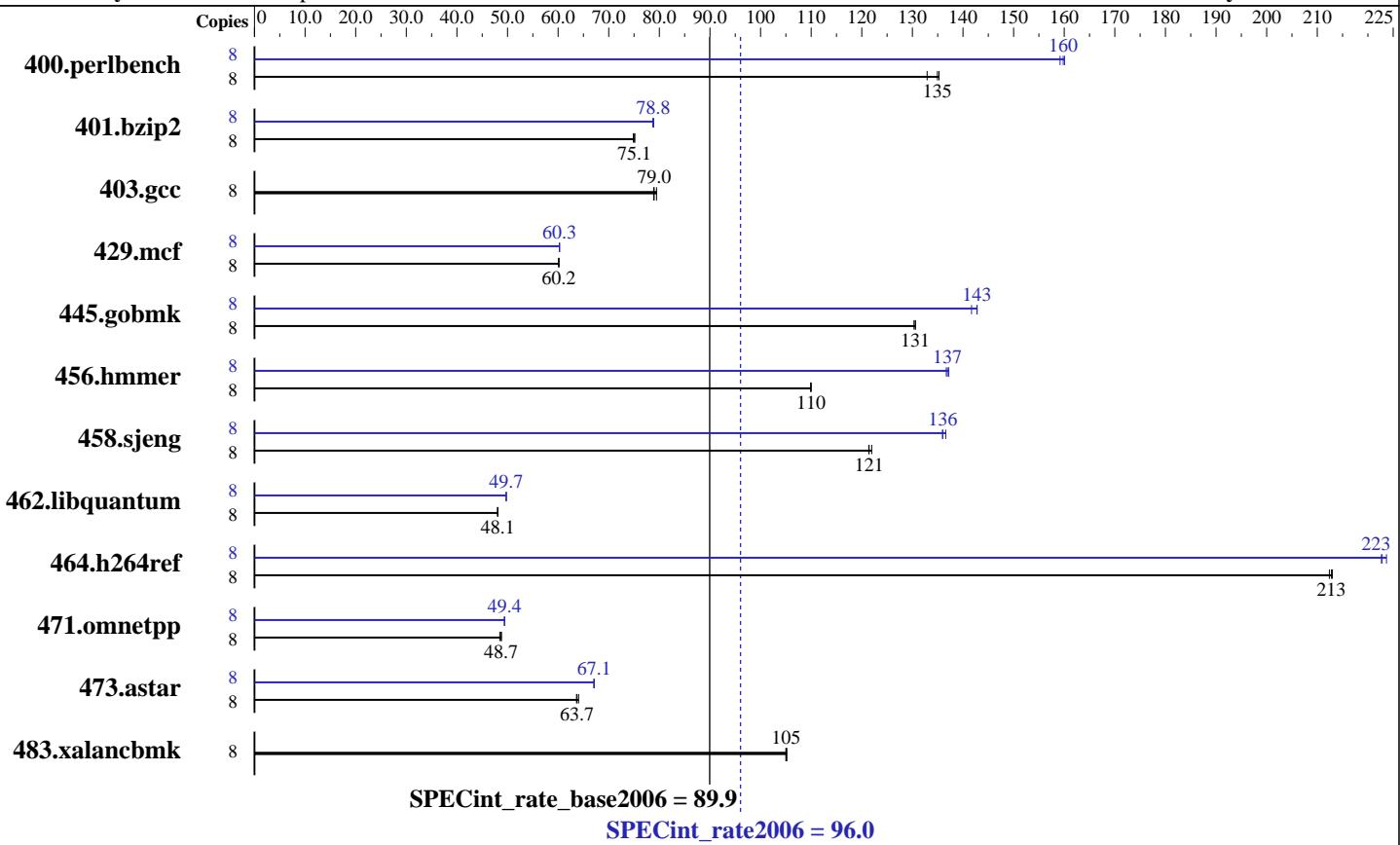
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jul-2007

Hardware Availability: Apr-2007

Software Availability: Jul-2007



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	1333MHz system bus
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2GB DDR2-5300F ECC)
Disk Subsystem:	1 x 36 GB SAS, 10000 RPM
Other Hardware:	Memory and I/O Expansion Unit (P/N 42C1600)

Software

Operating System:	SLES 10 (x86_64), 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for Linux version 10.0 Build 20070426 Package ID: l_cc_p_10.0.023
Auto Parallel:	No
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap 8.1



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint_rate2006 = 96.0

IBM BladeCenter HS21 (Intel Xeon X5355)

SPECint_rate_base2006 = 89.9

CPU2006 license: 11

Test date: Jul-2007

Test sponsor: IBM Corporation

Hardware Availability: Apr-2007

Tested by: IBM Corporation

Software Availability: Jul-2007

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	588	133	579	135	578	135	8	488	160	489	160	491	159
401.bzip2	8	1031	74.9	1027	75.1	1028	75.1	8	979	78.9	981	78.7	979	78.8
403.gcc	8	815	79.0	811	79.4	816	78.9	8	815	79.0	811	79.4	816	78.9
429.mcf	8	1213	60.2	1214	60.1	1213	60.2	8	1210	60.3	1211	60.3	1211	60.3
445.gobmk	8	644	130	643	131	643	131	8	592	142	588	143	588	143
456.hammer	8	679	110	679	110	679	110	8	544	137	546	137	545	137
458.sjeng	8	794	122	797	121	797	121	8	712	136	712	136	709	137
462.libquantum	8	3448	48.1	3449	48.1	3450	48.0	8	3332	49.7	3332	49.7	3332	49.7
464.h264ref	8	834	212	831	213	832	213	8	795	223	795	223	791	224
471.omnetpp	8	1032	48.5	1025	48.8	1026	48.7	8	1012	49.4	1012	49.4	1011	49.4
473.astar	8	881	63.7	877	64.0	883	63.6	8	836	67.2	837	67.1	838	67.0
483.xalancbmk	8	525	105	525	105	526	105	8	525	105	525	105	526	105

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

taskset utility used to bind CPU(s) to processes

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint_rate2006 = 96.0

IBM BladeCenter HS21 (Intel Xeon X5355)

SPECint_rate_base2006 = 89.9

CPU2006 license: 11

Test date: Jul-2007

Test sponsor: IBM Corporation

Hardware Availability: Apr-2007

Tested by: IBM Corporation

Software Availability: Jul-2007

Base Optimization Flags (Continued)

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/spec/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
401.bzip2: /opt/intel/cce/10.0.023/bin/icc  
           -L/opt/intel/cce/10.0.023/lib  
           -I/opt/intel/cce/10.0.023/include
```

```
456.hmmr: /opt/intel/cce/10.0.023/bin/icc  
           -L/opt/intel/cce/10.0.023/lib  
           -I/opt/intel/cce/10.0.023/include
```

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

456.hmmr: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
               -prefetch
```

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint_rate2006 = 96.0

IBM BladeCenter HS21 (Intel Xeon X5355)

SPECint_rate_base2006 = 89.9

CPU2006 license: 11

Test date: Jul-2007

Test sponsor: IBM Corporation

Hardware Availability: Apr-2007

Tested by: IBM Corporation

Software Availability: Jul-2007

Peak Optimization Flags (Continued)

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec_div -ansi-alias

456.hmmer: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Obo
-prefetch -opt-streaming-stores always

464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec_div -ansi-alias -Wl,-z,muldefs
-L/spec/cpu2006.1.0/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.44.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.44.xml>



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint_rate2006 = 96.0

IBM BladeCenter HS21 (Intel Xeon X5355)

SPECint_rate_base2006 = 89.9

CPU2006 license: 11

Test date: Jul-2007

Test sponsor: IBM Corporation

Hardware Availability: Apr-2007

Tested by: IBM Corporation

Software Availability: Jul-2007

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.

Report generated on Tue Jul 22 12:32:21 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 23 August 2007.