



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

SPECint®_rate2006 = 39.8

PRIMERGY TX200 S3, Intel Xeon processor E5320, 1.86 GHz

SPECint_rate_base2006 = 36.9

CPU2006 license: 22

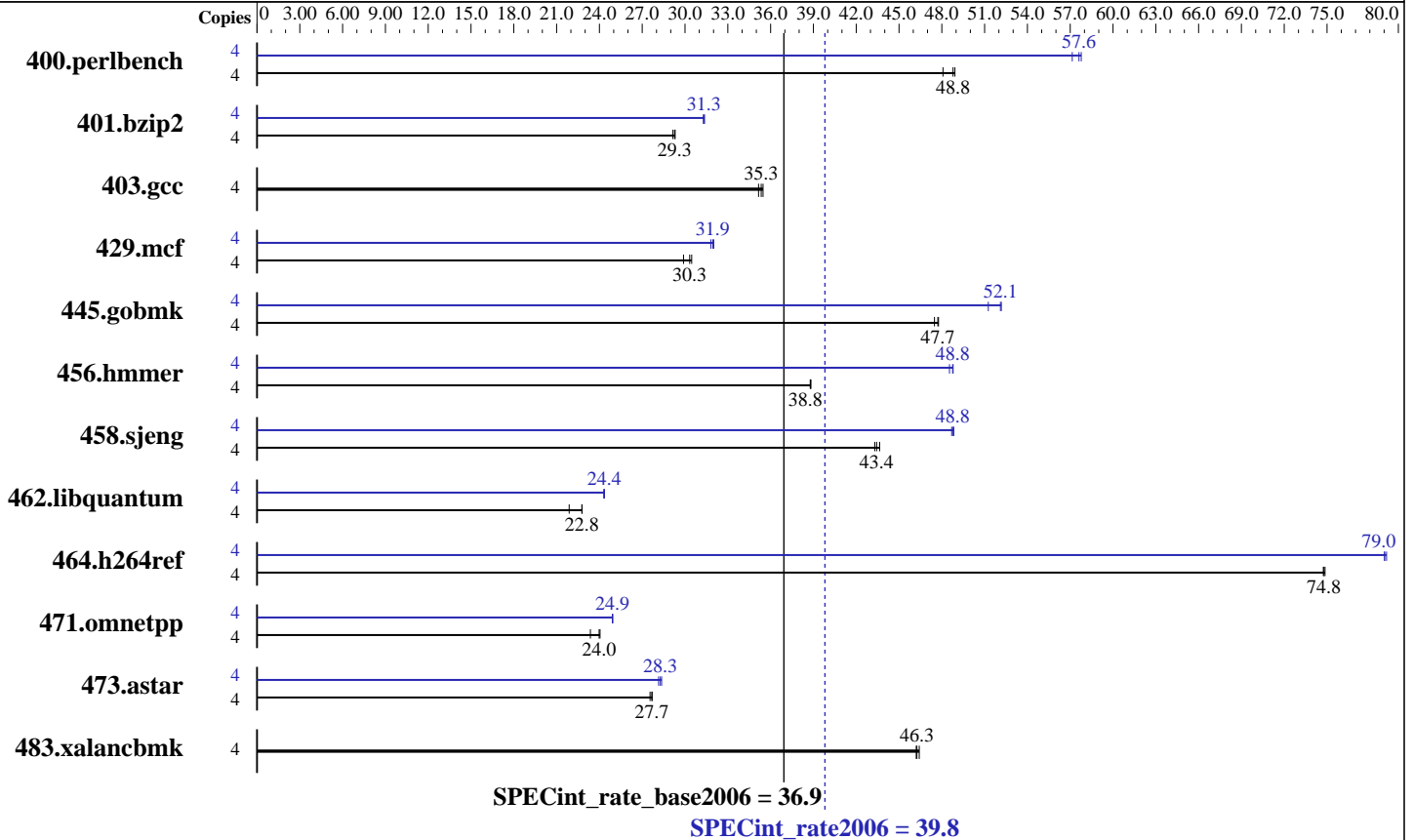
Test date: Jul-2007

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Nov-2006

Tested by: Fujitsu Siemens Computers

Software Availability: Jun-2007



Hardware

CPU Name: Intel Xeon E5320
 CPU Characteristics: 1067 MHz system bus
 CPU MHz: 1867
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
 Disk Subsystem: Seagate ST336754SS (SAS, 36GB, 15000rpm)
 Other Hardware: None

Software

Operating System: SUSE LINUX Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070308, Package-ID: I_cc_p_10.0.023
 Auto Parallel: No
 File System: ext2
 System State: Multiuser, Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Smart Heap Library, Version 8.1
 binutils-2.17.tar.gz, Version 2.17



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	801	48.8	813	48.1	799	48.9	4	678	57.6	684	57.1	676	57.8
401.bzip2	4	1319	29.3	1317	29.3	1324	29.2	4	1232	31.3	1231	31.4	1234	31.3
403.gcc	4	908	35.5	911	35.3	916	35.2	4	908	35.5	911	35.3	916	35.2
429.mcf	4	1220	29.9	1203	30.3	1197	30.5	4	1147	31.8	1142	31.9	1139	32.0
445.gobmk	4	878	47.8	879	47.7	884	47.5	4	804	52.2	805	52.1	819	51.3
456.hmmmer	4	961	38.8	962	38.8	962	38.8	4	765	48.8	765	48.8	769	48.5
458.sjeng	4	1118	43.3	1109	43.6	1114	43.4	4	992	48.8	994	48.7	991	48.8
462.libquantum	4	3788	21.9	3639	22.8	3639	22.8	4	3410	24.3	3404	24.4	3403	24.4
464.h264ref	4	1184	74.7	1184	74.8	1182	74.9	4	1118	79.2	1120	79.0	1120	79.0
471.omnetpp	4	1070	23.4	1043	24.0	1040	24.0	4	1004	24.9	1003	24.9	1003	24.9
473.astar	4	1019	27.5	1014	27.7	1015	27.7	4	990	28.4	998	28.1	993	28.3
483.xalancbmk	4	595	46.4	597	46.2	597	46.3	4	595	46.4	597	46.2	597	46.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmmer in peak were built with 64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

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Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xT -O3 -ipo -no-prec-div -ansi-alias

-L/opt/SmartHeap_8_1/lib -lsmarheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast -prefetch
-L/opt/SmartHeap_8_1/lib -lsmarheap

445.gobmk: Same as 400.perlbench

456.hmmer: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll2

458.sjeng: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll4

462.libquantum: -prof_gen(pass 1) -prof_use(pass 2) -fast -prefetch
-opt-streaming-stores always

464.h264ref: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -fast -ansi-alias
-L/opt/SmartHeap_8_1/lib -lsmarheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/FSC_Intel_flags.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/FSC_Intel_flags.xml



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For other inquiries, please contact webmaster@spec.org.

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