



SPEC® CINT2006 Result

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Supermicro Motherboard X7DB3

SPECint®2006 = 16.4

SPECint_base2006 = 15.7

CPU2006 license: 001176

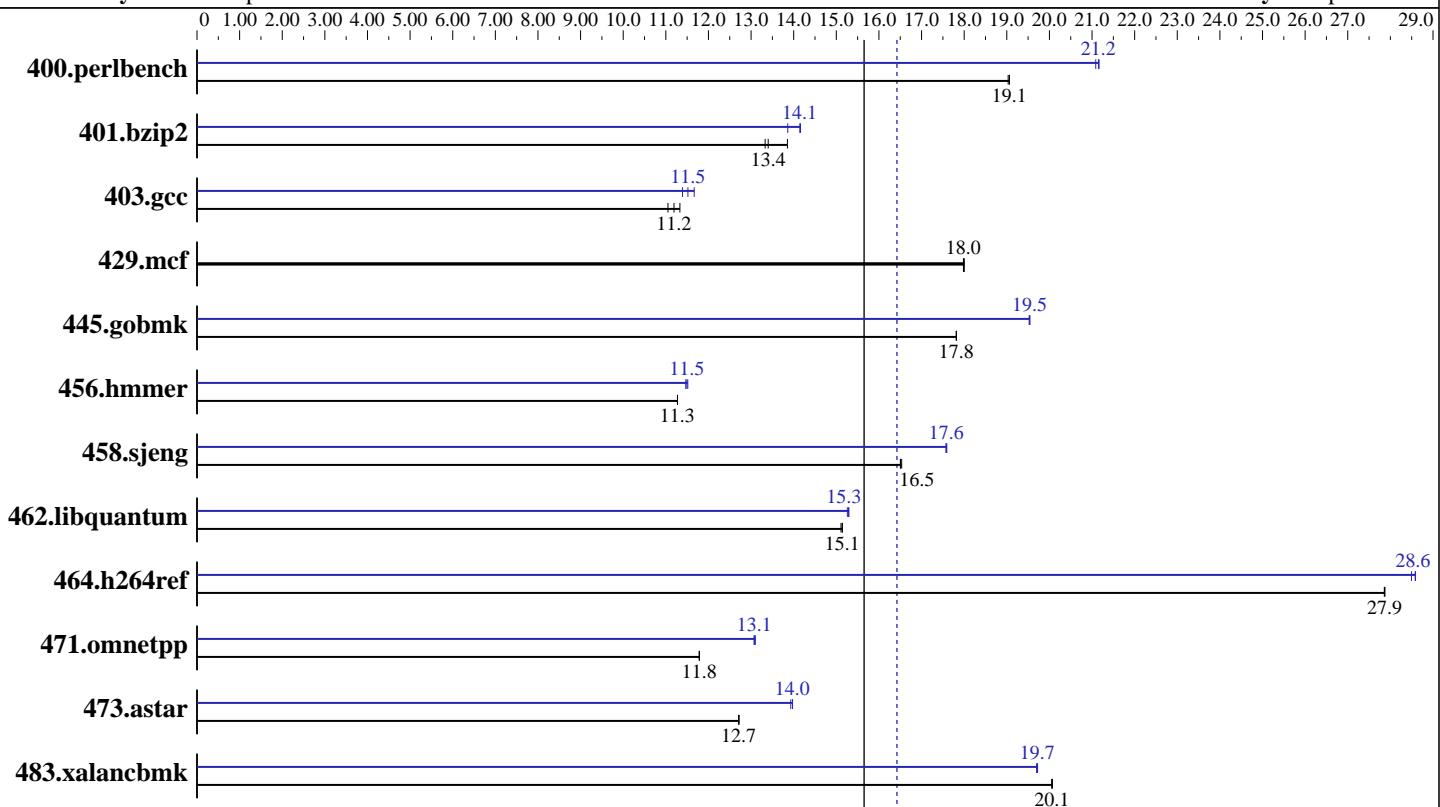
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Apr-2007

Hardware Availability: May-2007

Software Availability: Apr-2007



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Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	2.66GHz, 1333 MHz bus
CPU MHz:	2660
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8 X 1GB ECC PC2-5300, CL5, FBDIMM)
Disk Subsystem:	WD2500YS-01SHB1 250GB SATA II, 7200RPM, 4 * ST316081 160GB SATA RAID-10
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise Edition W/ SP1
Compiler:	Intel C++ Compiler for IA32 version 9.1
	Build no 20070322Z
	Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.0 from http://www.microquill.com/



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	513	19.0	513	19.1	513	19.1	462	21.2	463	21.1	462	21.2
401.bzip2	720	13.4	724	13.3	697	13.9	682	14.1	696	13.9	682	14.2
403.gcc	719	11.2	711	11.3	728	11.1	690	11.7	699	11.5	707	11.4
429.mcf	507	18.0	507	18.0	507	18.0	507	18.0	507	18.0	507	18.0
445.gobmk	589	17.8	589	17.8	589	17.8	537	19.5	537	19.5	537	19.5
456.hmmer	828	11.3	828	11.3	828	11.3	812	11.5	814	11.5	810	11.5
458.sjeng	733	16.5	733	16.5	732	16.5	688	17.6	688	17.6	689	17.6
462.libquantum	1369	15.1	1368	15.1	1371	15.1	1355	15.3	1358	15.3	1355	15.3
464.h264ref	794	27.9	794	27.9	794	27.9	774	28.6	777	28.5	774	28.6
471.omnetpp	530	11.8	530	11.8	530	11.8	477	13.1	478	13.1	478	13.1
473.astar	552	12.7	552	12.7	552	12.7	502	14.0	502	14.0	504	13.9
483.xalancbmk	344	20.1	344	20.1	344	20.1	350	19.7	350	19.7	350	19.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with CSE-825TQ-R700LPV case,
To ensure system stability, a 500W (minimum) ATX power supply [4-pin (+12V), 8-pin (+12V) and 24-pin are required]
Product description located as of <http://www.supermicro.com/products/motherboard/Xeon1333/5000P/X7DB3.cfm>
The system bus runs at 1333 MHz

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32

464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib
```

```
-link /FORCE:MULTIPLE
```

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32  
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000  
shlw32m.lib -link /FORCE:MULTIPLE
```

401.bzip2: Same as 400.perlbench

403.gcc: Same as 400.perlbench

429.mcf: basepeak = yes

445.gobmk: Same as 400.perlbench

456.hammer: Same as 400.perlbench

458.sjeng: Same as 400.perlbench

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Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -O2 -Qipo
-Qprec-div- -Qunroll4 -Ob2 -Qsfalign16 -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

483.xalancbmk: Same as 471.omnetpp

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.xml>

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For other inquiries, please contact webmaster@spec.org.

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