



SPEC® CINT2006 Result

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Intel Corporation

Supermicro X7DB+ (Intel Xeon processor 5110, 1.60 GHz)

SPECint®2006 = 12.6

SPECint_base2006 = 11.4

CPU2006 license: 13

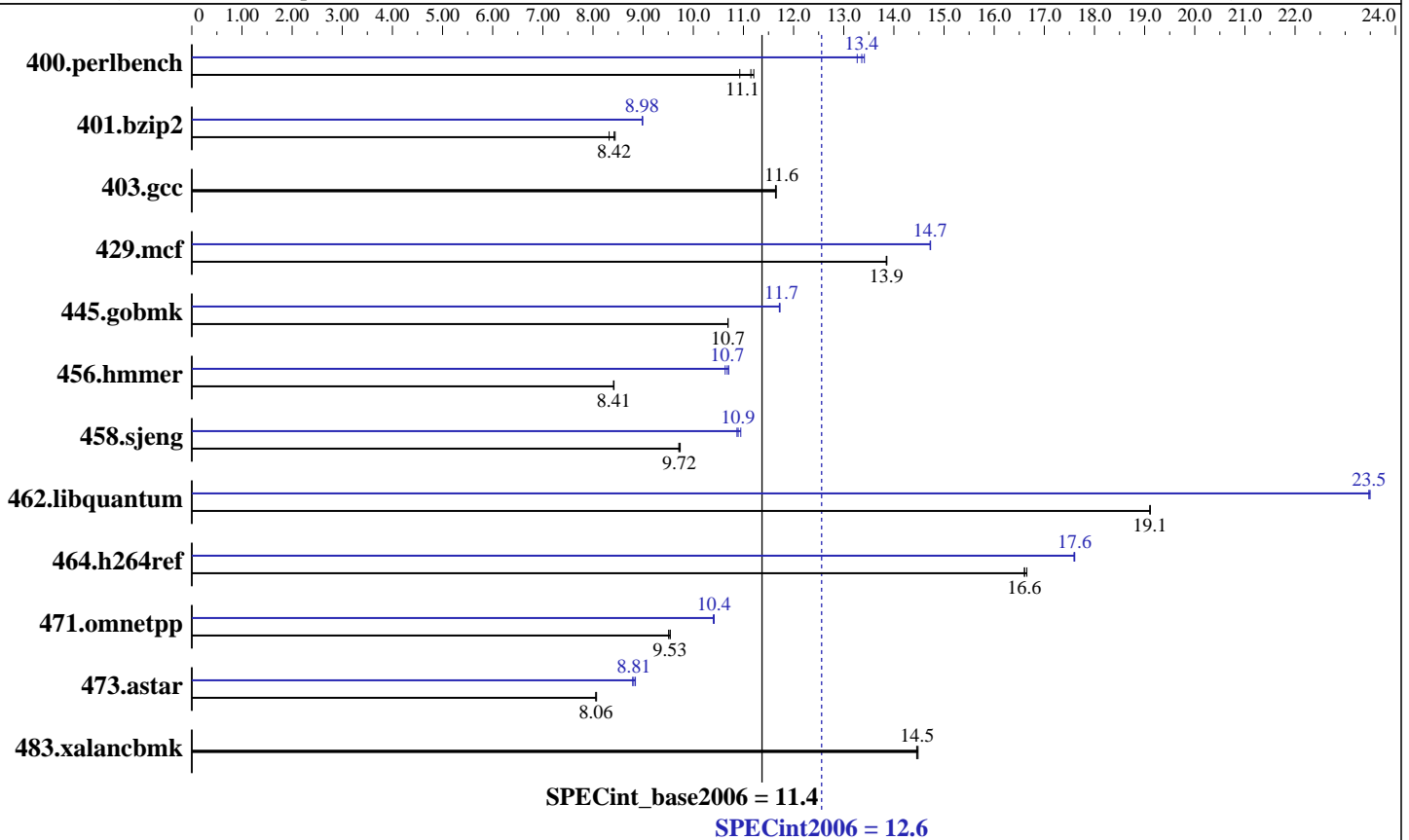
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Jun-2007

Hardware Availability: Nov-2006

Software Availability: Jun-2007



Hardware

CPU Name: Intel Xeon 5110
 CPU Characteristics: Dual Core, 1.60 GHz
 CPU MHz: 1600
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (8 * 1GB Samsung DDR2 5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: Seagate, SCSI, 73GB, 10Krpm, 1 disk only
 Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp for x86_64
 Compiler: Intel C++ Compiler for Linux32 version 10.0 Build 20070426 Package ID: l_cc_p_10.0.023
 Auto Parallel: No
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap library V8.1 Binutils 2.17.50.0.15



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Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|----------------|-------------|-------------|-------------|-------------|-------------|-------------|------------|-------------|-------------|-------------|-------------|-------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 872 | 11.2 | 894 | 10.9 | 876 | 11.1 | 736 | 13.3 | 728 | 13.4 | 731 | 13.4 |
| 401.bzip2 | 1160 | 8.32 | 1143 | 8.44 | 1146 | 8.42 | 1073 | 8.99 | 1075 | 8.98 | 1075 | 8.98 |
| 403.gcc | 691 | 11.6 | 692 | 11.6 | 691 | 11.7 | 691 | 11.6 | 692 | 11.6 | 691 | 11.7 |
| 429.mcf | 658 | 13.9 | 658 | 13.9 | 658 | 13.9 | 619 | 14.7 | 619 | 14.7 | 619 | 14.7 |
| 445.gobmk | 981 | 10.7 | 981 | 10.7 | 981 | 10.7 | 895 | 11.7 | 895 | 11.7 | 895 | 11.7 |
| 456.hmmer | 1108 | 8.42 | 1109 | 8.41 | 1109 | 8.41 | 877 | 10.6 | 874 | 10.7 | 871 | 10.7 |
| 458.sjeng | 1244 | 9.72 | 1242 | 9.74 | 1246 | 9.71 | 1105 | 10.9 | 1111 | 10.9 | 1113 | 10.9 |
| 462.libquantum | 1085 | 19.1 | 1084 | 19.1 | 1084 | 19.1 | 883 | 23.5 | 883 | 23.5 | 882 | 23.5 |
| 464.h264ref | 1329 | 16.7 | 1333 | 16.6 | 1333 | 16.6 | 1258 | 17.6 | 1257 | 17.6 | 1257 | 17.6 |
| 471.omnetpp | 655 | 9.53 | 655 | 9.54 | 657 | 9.51 | 600 | 10.4 | 601 | 10.4 | 600 | 10.4 |
| 473.astar | 870 | 8.07 | 871 | 8.06 | 871 | 8.06 | 797 | 8.81 | 799 | 8.79 | 794 | 8.85 |
| 483.xalanbmk | 476 | 14.5 | 477 | 14.5 | 477 | 14.5 | 476 | 14.5 | 477 | 14.5 | 477 | 14.5 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Bios settings:
Hardware Prefetcher: Enabled
Adjacent Sector Prefetch: Enabled
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalanbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec_div -ansi-alias

456.hmmer: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always

464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec_div -ansi-alias -Wl,-z,muldefs -L/spec/cpu2006.1.0/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.35.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.35.xml>



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