



# SPEC® CINT2006 Result

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## Intel Corporation

Supermicro X7DB+ (Intel Xeon processor 5140, 2.33 GHz)

SPECint®\_rate2006 = 55.4

SPECint\_rate\_base2006 = 51.1

CPU2006 license: 13

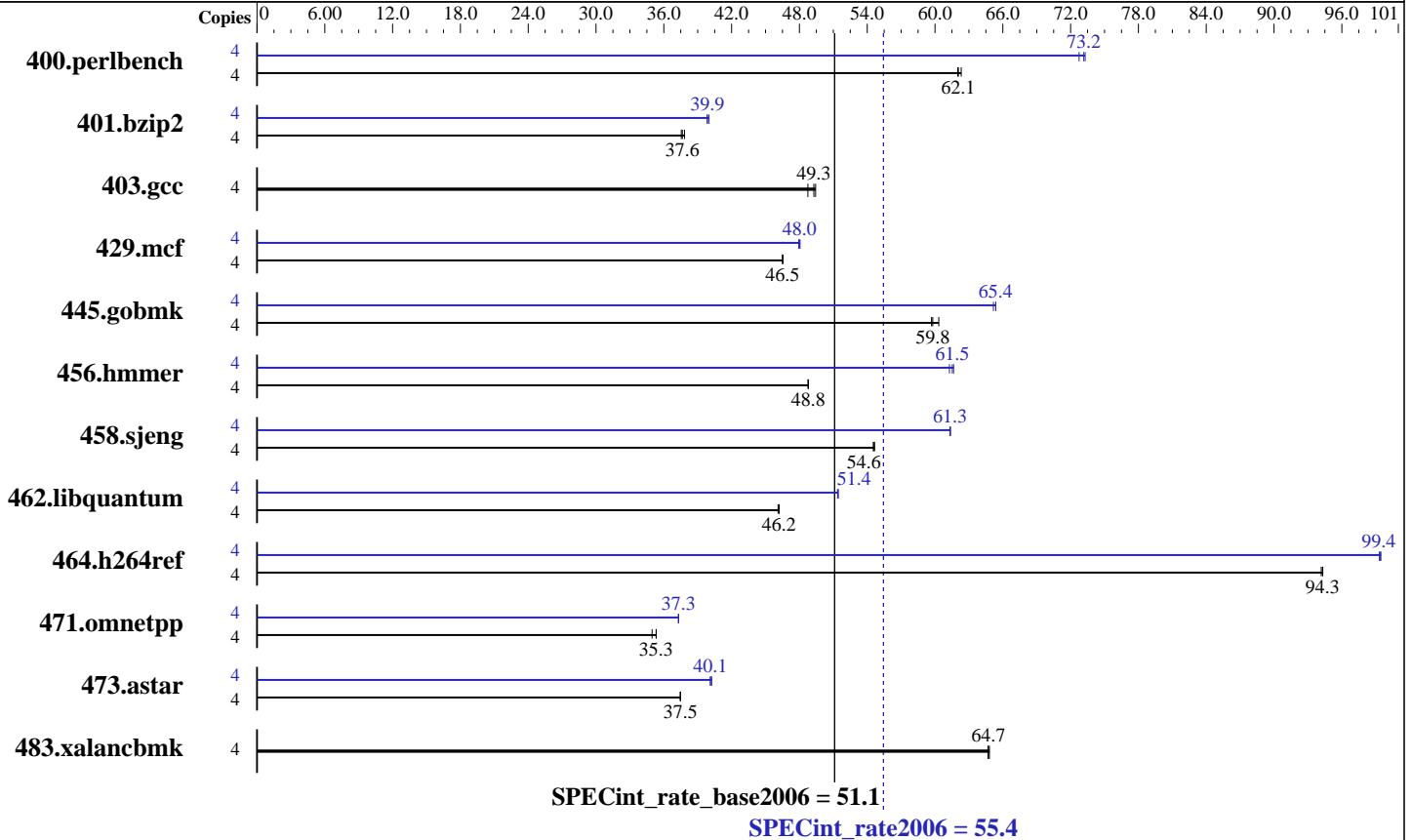
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: May-2007

Hardware Availability: Nov-2006

Software Availability: Jun-2007



### Hardware

CPU Name: Intel Xeon 5140  
 CPU Characteristics: Dual Core, 2.33 GHz  
 CPU MHz: 2333  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (8 \* 1GB Samsung DDR2 5300F, 2 rank, CL5-5-5, ECC)  
 Disk Subsystem: Seagate, SCSI, 73GB, 10Krpm, 1 disk only  
 Other Hardware: None

### Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp for x86\_64  
 Compiler: Intel C++ Compiler for Linux32 version 10.0 Build 20070426 Package ID: l\_cc\_p\_10.0.023  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Multi-user, run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: SmartHeap library V8.1 Binutils 2.17.50.0.15



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	630	62.0	627	62.3	<b>630</b>	<b>62.1</b>	4	537	72.8	<b>534</b>	<b>73.2</b>	533	73.3
401.bzip2	4	1028	37.5	<b>1025</b>	<b>37.6</b>	1020	37.8	4	965	40.0	969	39.8	<b>968</b>	<b>39.9</b>
403.gcc	4	661	48.7	<b>653</b>	<b>49.3</b>	651	49.4	4	661	48.7	<b>653</b>	<b>49.3</b>	651	49.4
429.mcf	4	784	46.5	<b>784</b>	<b>46.5</b>	784	46.5	4	<b>760</b>	<b>48.0</b>	759	48.1	761	47.9
445.gobmk	4	703	59.7	695	60.3	<b>702</b>	<b>59.8</b>	4	<b>642</b>	<b>65.4</b>	644	65.2	642	65.4
456.hmmmer	4	765	48.8	<b>765</b>	<b>48.8</b>	765	48.8	4	609	61.3	<b>607</b>	<b>61.5</b>	605	61.6
458.sjeng	4	886	54.7	887	54.5	<b>887</b>	<b>54.6</b>	4	789	61.3	789	61.4	<b>789</b>	<b>61.3</b>
462.libquantum	4	1794	46.2	1797	46.1	<b>1794</b>	<b>46.2</b>	4	<b>1612</b>	<b>51.4</b>	1613	51.4	1611	51.4
464.h264ref	4	939	94.3	<b>939</b>	<b>94.3</b>	940	94.2	4	<b>891</b>	<b>99.4</b>	890	99.5	891	99.3
471.omnetpp	4	<b>708</b>	<b>35.3</b>	708	35.3	715	35.0	4	<b>670</b>	<b>37.3</b>	670	37.3	670	37.3
473.astar	4	749	37.5	<b>749</b>	<b>37.5</b>	750	37.4	4	698	40.2	<b>700</b>	<b>40.1</b>	700	40.1
483.xalancbmk	4	426	64.8	<b>426</b>	<b>64.7</b>	427	64.7	4	426	64.8	<b>426</b>	<b>64.7</b>	427	64.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Bios settings:  
Hardware Prefetcher: Enabled  
Adjacent Sector Prefetch: Disabled  
ulimit -s unlimited used to set stack size to unlimited  
taskset command used to assign copies to cores  
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmmer,  
for peak, are compiled in 64-bit mode

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/spec/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc  
-L/opt/intel/cce/10.0.023/lib  
-I/opt/intel/cce/10.0.023/include

456.hmmer: /opt/intel/cce/10.0.023/bin/icc  
-L/opt/intel/cce/10.0.023/lib  
-I/opt/intel/cce/10.0.023/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

Continued on next page



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## Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec\_div -ansi-alias

456.hmmer: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0  
-prefetch -opt-streaming-stores always

464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec\_div -ansi-alias -Wl,-z,muldefs  
-L/spec/cpu2006.1.0/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090715.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090715.00.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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