



SPEC® CFP2006 Result

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Supermicro Motherboard X7DA8+

SPECfp®2006 = 10.0
SPECfp_base2006 = 9.32

CPU2006 license: 001176

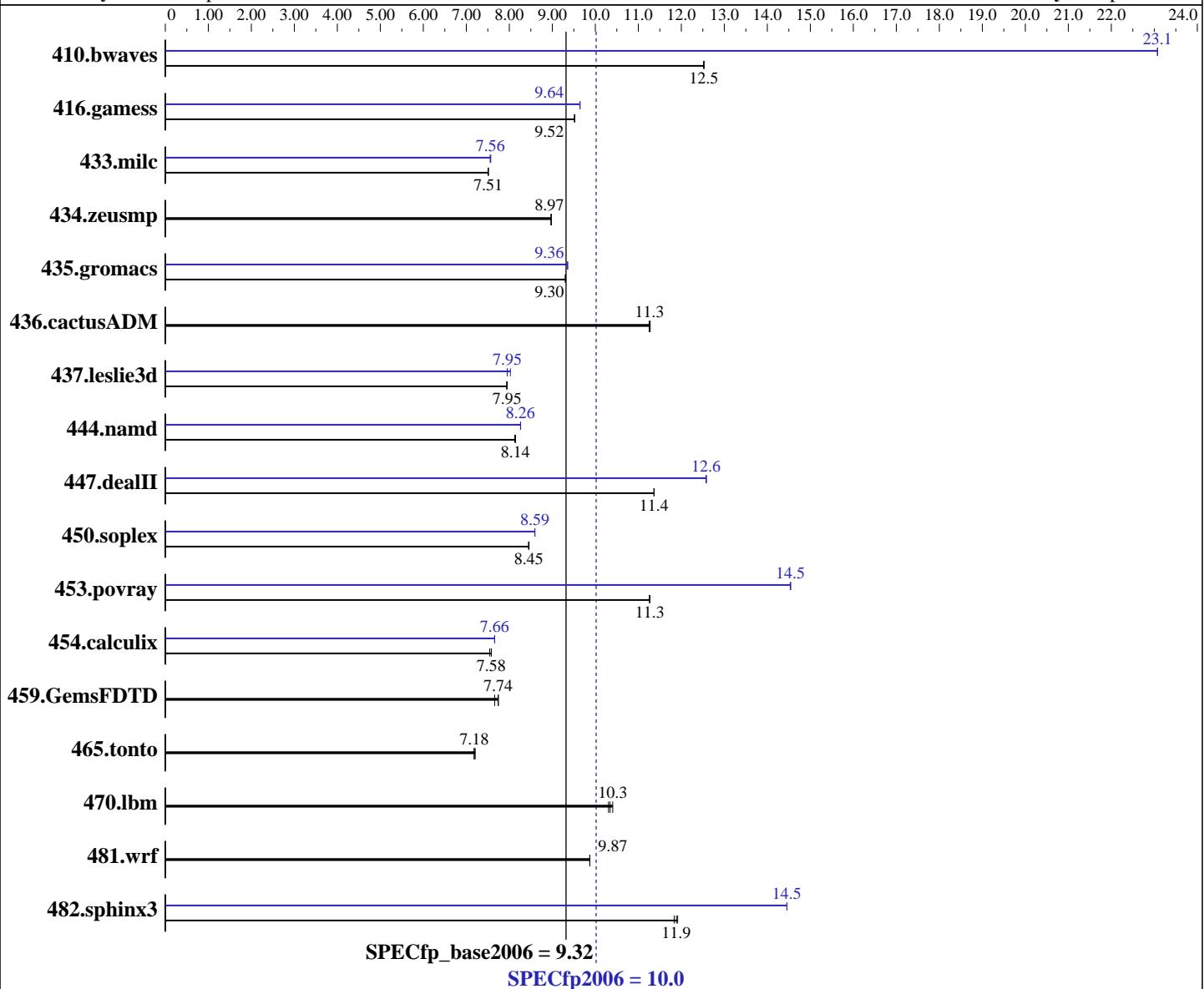
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Apr-2007

Hardware Availability: May-2007

Software Availability: Apr-2007



Hardware	
CPU Name:	Intel Xeon L5310
CPU Characteristics:	1.60GHz, 1066 MHz Bus
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

Software	
Operating System:	Windows Server 2003 Enterprise Edition W/ SP1
Compiler:	Intel C++ Compiler for IA32 version 9.1
	Build no 20070322Z
Auto Parallel:	Intel Fortran Compiler for IA32 version 9.1
File System:	Build no 20070322Z
System State:	Microsoft Visual Studio .Net 2003 (for libraries)
Yes	Yes
NTFS	NTFS
Default	Default

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8 X 2GB ECC PC2-5300, CL5, FBDIMM)
Disk Subsystem: 750GB IDE, 7200RPM
Other Hardware: None

Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: SmartHeap Library Version 8.0

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	1086	12.5	1085	12.5	1085	12.5	589	23.1	589	23.1	589	23.1
416.gamess	2057	9.52	2057	9.52	2058	9.51	2030	9.64	2030	9.64	2031	9.64
433.milc	1222	7.51	1223	7.51	1222	7.51	1213	7.57	1215	7.56	1215	7.56
434.zeusmp	1014	8.97	1015	8.97	1014	8.98	1014	8.97	1015	8.97	1014	8.98
435.gromacs	767	9.31	767	9.30	767	9.30	764	9.35	763	9.36	763	9.36
436.cactusADM	1061	11.3	1060	11.3	1062	11.3	1061	11.3	1060	11.3	1062	11.3
437.leslie3d	1183	7.95	1184	7.94	1183	7.95	1171	8.03	1182	7.95	1182	7.95
444.namd	985	8.14	987	8.12	985	8.14	970	8.26	971	8.26	971	8.26
447.dealII	1006	11.4	1007	11.4	1006	11.4	909	12.6	910	12.6	910	12.6
450.soplex	986	8.46	987	8.45	987	8.45	971	8.59	970	8.59	970	8.59
453.povray	473	11.3	472	11.3	472	11.3	366	14.5	366	14.5	366	14.5
454.calculix	1088	7.58	1088	7.58	1094	7.54	1077	7.66	1077	7.66	1077	7.66
459.GemsFDTD	1370	7.74	1385	7.66	1370	7.74	1370	7.74	1385	7.66	1370	7.74
465.tonto	1370	7.18	1365	7.21	1370	7.18	1370	7.18	1365	7.21	1370	7.18
470.lbm	1328	10.3	1333	10.3	1320	10.4	1328	10.3	1333	10.3	1320	10.4
481.wrf	1131	9.87	1131	9.87	1131	9.87	1131	9.87	1131	9.87	1131	9.87
482.sphinx3	1636	11.9	1647	11.8	1640	11.9	1348	14.5	1348	14.5	1348	14.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with CSE-833S-R760 case,
To ensure system stability, a 500W (minimum) ATX power supply [4-pin (+12V), 8-pin (+12V) and 24-pin are required]
Product description located as of <http://www.supermicro.com/products/motherboard/Xeon1333/5000X/X7DA8+.cfm>
The system bus runs at 1066 MHz

Base Compiler Invocation

C benchmarks:

 icl -Qvc7.1 -Qc99

C++ benchmarks:

 icl -Qvc7.1

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
-DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-fast /F9500000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qcxx_features /F9500000000 shlw32m.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:
-fast /F9500000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
-fast /F9500000000 -link /FORCE:MULTIPLE

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort



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Peak Portability Flags

```
436.cactusADM: -Qlowercase /assume:underscore
 444.namd: -TP
 447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
   -DBOOST_NO_INTRINSIC_WCHAR_T
 453.povray: -DSPEC_CPU_WINDOWS_ICL
 454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
 481.wrf: -DSPEC_CPU_WINDOWS_ICL
```

Peak Optimization Flags

C benchmarks:

```
433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000
           shlw32m.lib          -link /FORCE:MULTIPLE
```

```
470.lbm: basepeak = yes
```

```
482.sphinx3: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxB -Qipo -O3
              -Qprec-div- /F950000000 shlw32m.lib
              -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F950000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

Fortran benchmarks:

```
410.bwaves: -QxW -Qparallel -Qipo -O3 -Qprec-div- /F950000000
             libguide.lib libguide40.lib          -link /FORCE:MULTIPLE
```

```
416.gamess: Same as 410.bwaves
```

```
434.zeusmp: basepeak = yes
```

```
437.leslie3d: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000
               -link /FORCE:MULTIPLE
```

```
459.GemsFDTD: basepeak = yes
```

```
465.tonto: basepeak = yes
```

Benchmarks using both Fortran and C:

```
435.gromacs: -QxW -Qparallel -Qipo -O3 -Qprec-div- /F950000000
              shlw32m.lib libguide.lib libguide40.lib
              -link /FORCE:MULTIPLE
```

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Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000
-link /FORCE:MULTIPLE

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.1.

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