



SPEC[®] CFP2006 Result

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HITACHI

SPECfp[®]2006 = 16.6

BladeSymphony BS1000 (Intel Xeon 5160)

SPECfp_base2006 = 16.1

CPU2006 license: 872

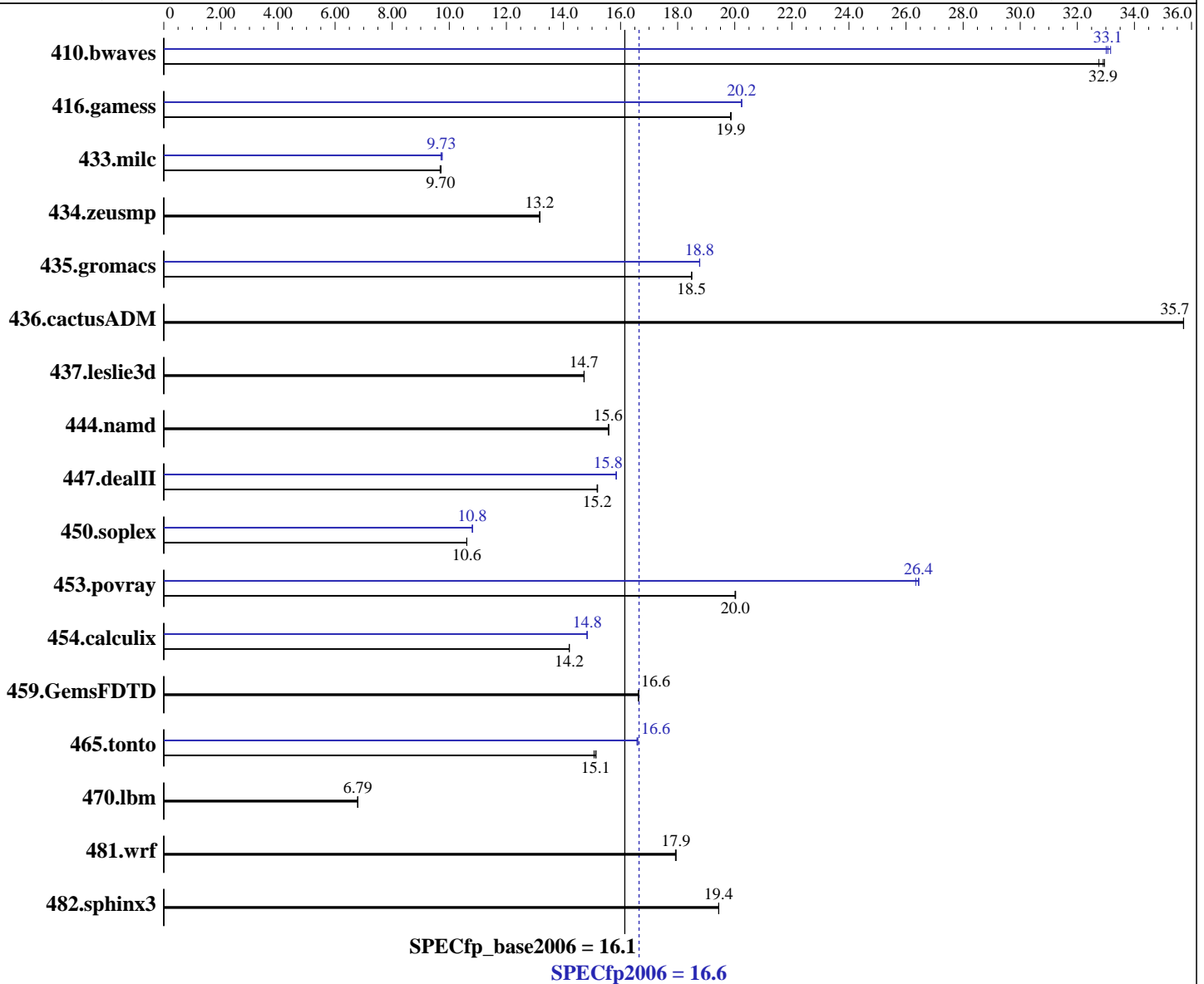
Test sponsor: HITACHI

Tested by: HITACHI

Test date: May-2007

Hardware Availability: Sep-2006

Software Availability: Dec-2006



Hardware

CPU Name: Intel Xeon 5160
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip

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Software

Operating System: Microsoft Windows Server 2003 R2, Enterprise x64 Edition
 Compiler: Intel C++ Compiler for EM64T version 9.1 Build 20061104
 Intel Fortran Compiler for EM64T version 9.1 Build 20061104
 Microsoft Visual Studio 2005 (for libraries)
 Auto Parallel: Yes
 File System: NTFS

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L3 Cache: None
Other Cache: None
Memory: 16 GB(8 x 2 GB PC2-4200F)
Disk Subsystem: 1 x 73GB 10000rpm SAS
Other Hardware: None

System State: Default
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other Software: MicroQuill SmartHeap Library 8.0

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	415	32.8	413	32.9	412	33.0	410	33.2	411	33.1	412	33.0
416.gamess	986	19.9	986	19.8	985	19.9	967	20.2	968	20.2	968	20.2
433.milc	947	9.70	948	9.68	945	9.71	945	9.71	941	9.76	943	9.73
434.zeusmp	691	13.2	691	13.2	691	13.2	691	13.2	691	13.2	691	13.2
435.gromacs	386	18.5	386	18.5	386	18.5	380	18.8	380	18.8	380	18.8
436.cactusADM	335	35.7	335	35.7	335	35.7	335	35.7	335	35.7	335	35.7
437.leslie3d	639	14.7	639	14.7	638	14.7	639	14.7	639	14.7	638	14.7
444.namd	515	15.6	515	15.6	515	15.6	515	15.6	515	15.6	515	15.6
447.dealII	753	15.2	754	15.2	753	15.2	722	15.8	722	15.8	722	15.9
450.soplex	786	10.6	786	10.6	786	10.6	772	10.8	771	10.8	772	10.8
453.povray	266	20.0	266	20.0	266	20.0	202	26.3	201	26.4	201	26.4
454.calculix	581	14.2	581	14.2	581	14.2	556	14.8	556	14.8	556	14.8
459.GemsFDTD	638	16.6	638	16.6	638	16.6	638	16.6	638	16.6	638	16.6
465.tonto	653	15.1	650	15.1	651	15.1	593	16.6	593	16.6	594	16.6
470.lbm	2022	6.79	2023	6.79	2022	6.79	2022	6.79	2023	6.79	2022	6.79
481.wrf	623	17.9	623	17.9	623	17.9	623	17.9	623	17.9	623	17.9
482.sphinx3	1003	19.4	1003	19.4	1003	19.4	1003	19.4	1003	19.4	1003	19.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
ic1 -Qvc8 -Qc99

C++ benchmarks:
ic1 -Qvc8

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ic1 -Qvc8 -Qc99 ifort



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Base Portability Flags

```

410.bwaves: -DSPEC_CPU_P64
416.gamess: -DSPEC_CPU_P64
433.milc: -D_Complex= -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -D_Complex= -DSPEC_CPU_P64
436.cactusADM: -D_Complex= -DSPEC_CPU_P64 -Qlowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.deallI: -D_Complex= -DSPEC_CPU_P64 -DBOOST_NO_INTRINSIC_WCHAR_T
-DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -D_Complex= -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER
-Qlowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -D_Complex= -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -D_Complex= -DSPEC_CPU_P64

```

Base Optimization Flags

```

C benchmarks:
-fast -Qparallel /F950000000 shlw32M.lib

C++ benchmarks:
-fast -Qparallel -Qcxx-features /F950000000 shlw32M.lib

Fortran benchmarks:
-fast -Qparallel /F950000000 shlw32M.lib

Benchmarks using both Fortran and C:
-fast -Qparallel /F950000000 shlw32M.lib

```

Peak Compiler Invocation

```

C benchmarks:
icl -Qvc8 -Qc99

C++ benchmarks:
icl -Qvc8

Fortran benchmarks:
ifort

```

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Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icl -Qvc8 -Qc99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000
shlW32M.lib

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features
/F950000000 shlW32M.lib

450.soplex: Same as 447.dealII

453.povray: Same as 447.dealII

Fortran benchmarks:

410.bwaves: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qparallel
/F950000000 shlW32M.lib

416.gamess: -fast /F950000000 shlW32M.lib

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: Same as 410.bwaves

Benchmarks using both Fortran and C:

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Peak Optimization Flags (Continued)

435.gromacs: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000
shlW32M.lib

436.cactusADM: basepeak = yes

454.calculix: Same as 435.gromacs

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/ic91_fp.20090715.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/ic91_fp.20090715.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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