SPEC® CINT2006 Result

Intel Corporation

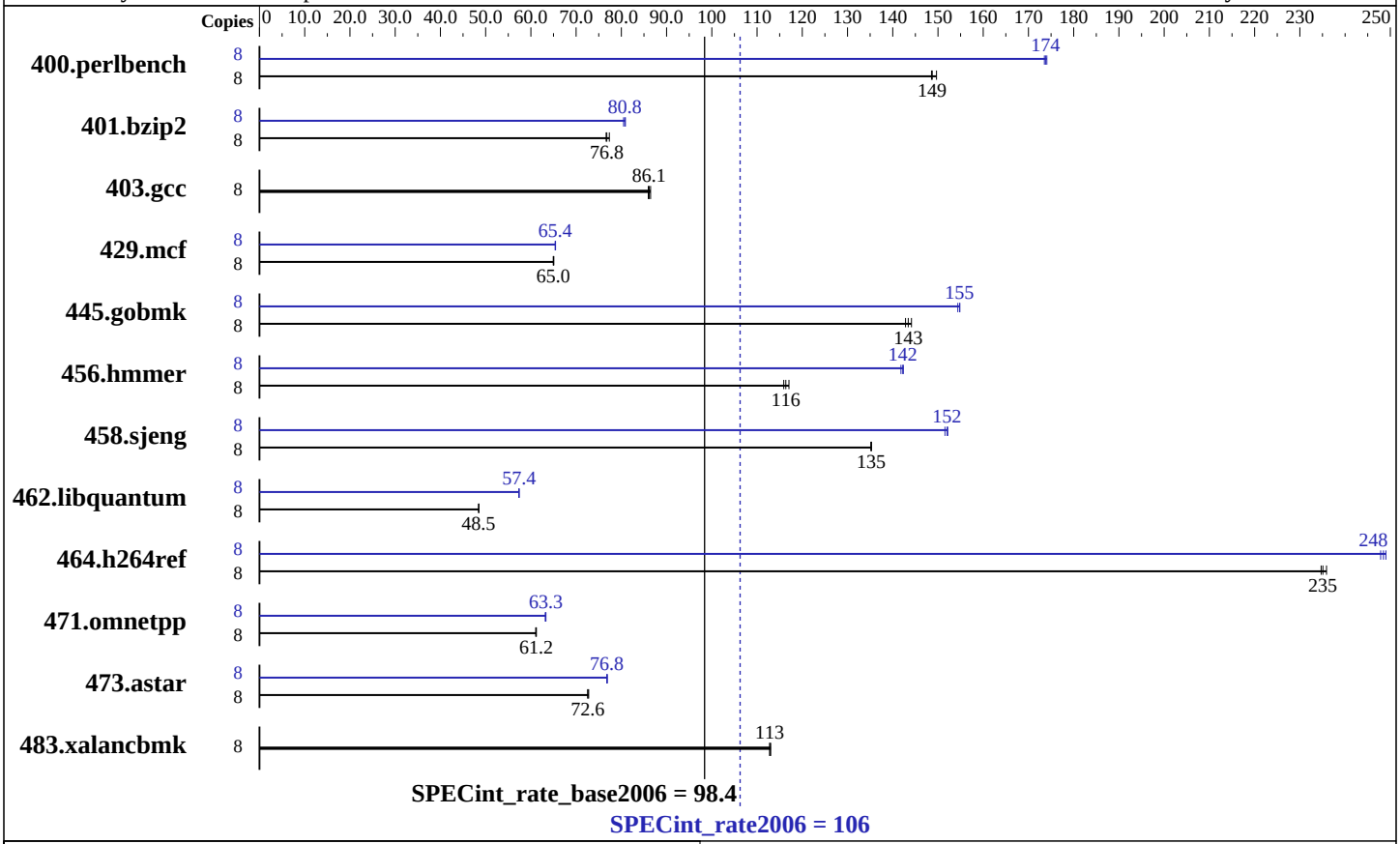
Supermicro X7DB8+ (Intel Xeon processor X5365, 3.00 GHz)

SPECint®_rate2006 = 106

SPECint_rate_base2006 = 98.4

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: May-2007
Hardware Availability: Sep-2007
Software Availability: Jun-2007



Hardware

CPU Name:	Intel Xeon X5365
CPU Characteristics:	Quad Core, 3.0 GHz
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 * 2GB Samsung DDR2 5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem:	Seagate, SCSI, 73GB, 10Krpm, 1 disk only
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp for x86_64
Compiler:	Intel C++ Compiler for Linux32 version 10.0 Build 20070426 Package ID: l_cc_p_10.0.023
Auto Parallel:	No
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap library V8.1 Binutils 2.17.50.0.15

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	526	149	522	150	**526**	**149**	8	449	174	**450**	**174**	450	174
401.bzip2	8	998	77.3	**1005**	**76.8**	1007	76.6	8	**956**	**80.8**	959	80.5	954	80.9
403.gcc	8	**748**	**86.1**	745	86.5	749	86.0	8	**748**	**86.1**	745	86.5	749	86.0
429.mcf	8	1121	65.1	1123	65.0	**1123**	**65.0**	8	1115	65.4	**1115**	**65.4**	1116	65.4
445.gobmk	8	**585**	**143**	588	143	582	144	8	544	154	**542**	**155**	542	155
456.hmmer	8	644	116	**641**	**116**	638	117	8	526	142	**525**	**142**	524	142
458.sjeng	8	716	135	**716**	**135**	715	135	8	**637**	**152**	636	152	639	152
462.libquantum	8	3419	48.5	**3420**	**48.5**	3420	48.5	8	**2888**	**57.4**	2888	57.4	2891	57.3
464.h264ref	8	**753**	**235**	751	236	754	235	8	714	248	**712**	**248**	711	249
471.omnetpp	8	817	61.2	**817**	**61.2**	819	61.1	8	**790**	**63.3**	790	63.3	792	63.2
473.astar	8	**774**	**72.6**	772	72.8	774	72.6	8	**731**	**76.8**	730	76.9	731	76.8
483.xalancbmk	8	488	113	489	113	**489**	**113**	8	488	113	489	113	**489**	**113**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Bios settings:
Hardware Prefetcher:      Disabled
Adjacent Sector Prefetch: Disabled
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode
taskset was used to bind processes to cores
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast`

C++ benchmarks:
`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`
`-L/spec/cpu2006.1.0/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/cce/10.0.023/bin/icc`

456.hmmer: `/opt/intel/cce/10.0.023/bin/icc`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias`
`-prefetch`

401.bzip2: `-L/opt/intel/cce/10.0.023/lib -I/opt/intel/cce/10.0.023/include`
`-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast`

Continued on next page

Peak Optimization Flags (Continued)

403.gcc: `basepeak = yes`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec_div -ansi-alias`

456.hmmer: `-L/opt/intel/cce/10.0.023/lib -I/opt/intel/cce/10.0.023/include -prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec_div -ansi-alias -Wl,-z,muldefs -L/spec/cpu2006.1.0/lib -lsmartheap`

473.astar: `Same as 471.omnetpp`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090715.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090715.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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