



# SPEC® CFP2006 Result

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## Bull SAS

NovaScale R440  
(Intel Xeon processor 5130,2.00GHz)

**SPECfp®\_rate2006 = 36.1**

**SPECfp\_rate\_base2006 = 35.7**

CPU2006 license: 20

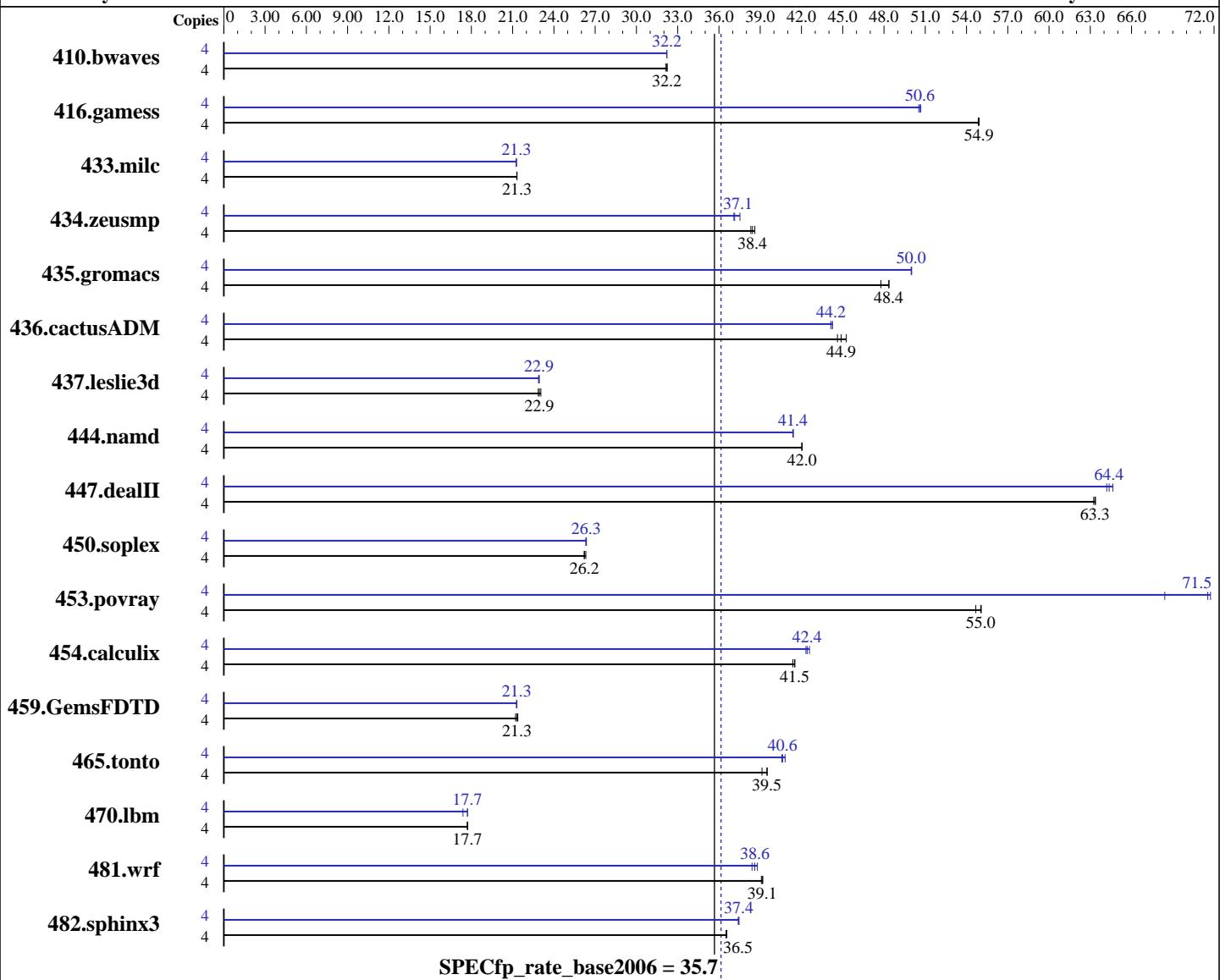
Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: May-2007

Hardware Availability: Mar-2007

Software Availability: Dec-2006



### Hardware

CPU Name: Intel Xeon 5130  
CPU Characteristics: 2.00 GHz, 4 MB L2, 1333 MHz system bus  
CPU MHz: 2000  
FPU: Integrated  
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
CPU(s) orderable: 1 to 2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 4 MB I+D on chip per chip

### Software

Operating System: SuSE Linux Enterprise Server 10 (EM64T)  
Compiler: Intel C++ Compiler for Intel EM64T-based applications, Version 9.1  
Auto Parallel: No

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## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

Fortran benchmarks:  
ifort

Benchmarks using both Fortran and C:  
icc ifort

## Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

## Base Optimization Flags

C benchmarks:  
-fast

C++ benchmarks:  
-fast

Fortran benchmarks:  
-fast

Benchmarks using both Fortran and C:  
-fast



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## Peak Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

Fortran benchmarks:  
ifort

Benchmarks using both Fortran and C:  
icc ifort

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:  
-prof\_gen(pass 1) -prof\_use(pass 2) -fast -auto\_ilp32

C++ benchmarks:  
-prof\_gen(pass 1) -prof\_use(pass 2) -fast -auto\_ilp32

Fortran benchmarks:  
-prof\_gen(pass 1) -prof\_use(pass 2) -fast

Benchmarks using both Fortran and C:  
-prof\_gen(pass 1) -prof\_use(pass 2) -fast -auto\_ilp32

The flags file that was used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/EM64T\\_Intel91\\_flags.html](http://www.spec.org/cpu2006/flags/EM64T_Intel91_flags.html)

You can also download the XML flags source by saving the following link:

[http://www.spec.org/cpu2006/flags/EM64T\\_Intel91\\_flags.xml](http://www.spec.org/cpu2006/flags/EM64T_Intel91_flags.xml)



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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.

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