



SPEC[®] CINT2006 Result

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Bull SAS

NovaScale R440
(Intel Xeon processor 5120,1.86GHz)

SPECint[®]_rate2006 = 37.6

SPECint_rate_base2006 = 36.3

CPU2006 license: 20

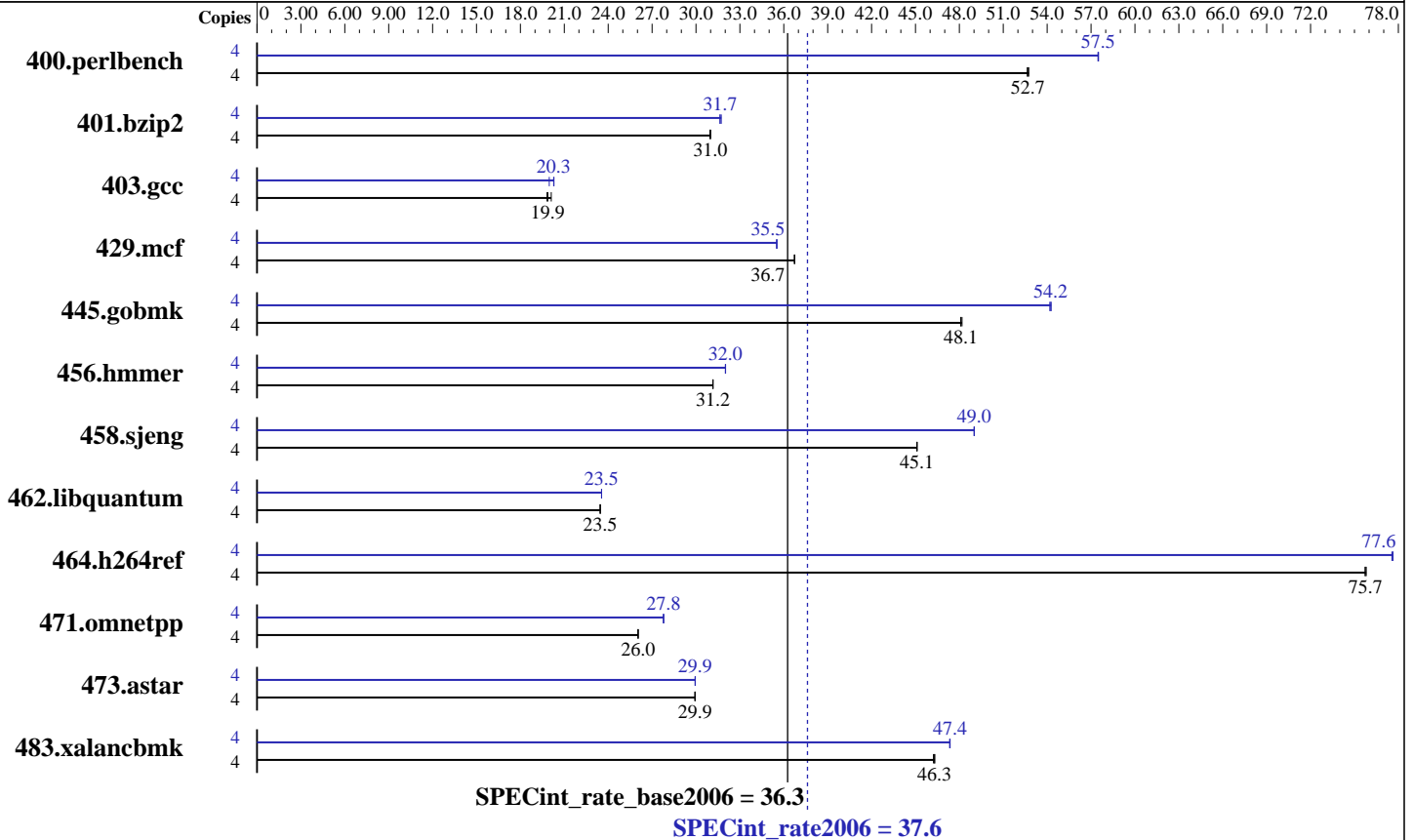
Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: May-2007

Hardware Availability: Feb-2007

Software Availability: Dec-2006



Hardware

CPU Name: Intel Xeon 5120
 CPU Characteristics: 1.86 GHz, 4 MB L2, 1066 MHz system bus
 CPU MHz: 1866
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 24 GB (12x2 GB) FB-DIMM PC2-5300F ECC CL5
 Disk Subsystem: 1x73 GB SAS, 15000 RPM
 Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise Edition X64 Edition Service Pack 1
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Package ID W_CC_C_9.1.033 Build no 20061103Z
 Microsoft Visual Studio .NET 2003 (lib & linker)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: MicroQuill SmartHeap Library 8.0 (shIW32M.lib)



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	742	52.6	<u>742</u>	<u>52.7</u>	741	52.7	4	680	57.5	680	57.5	<u>680</u>	<u>57.5</u>
401.bzip2	4	1247	31.0	<u>1246</u>	<u>31.0</u>	1245	31.0	4	1220	31.6	1217	31.7	<u>1219</u>	<u>31.7</u>
403.gcc	4	1601	20.1	<u>1620</u>	<u>19.9</u>	1625	19.8	4	1612	20.0	<u>1588</u>	<u>20.3</u>	1587	20.3
429.mcf	4	993	36.7	993	36.7	<u>993</u>	<u>36.7</u>	4	1027	35.5	1027	35.5	<u>1027</u>	<u>35.5</u>
445.gobmk	4	<u>872</u>	<u>48.1</u>	872	48.1	871	48.2	4	773	54.3	<u>774</u>	<u>54.2</u>	774	54.2
456.hammer	4	<u>1198</u>	<u>31.2</u>	1198	31.2	1198	31.2	4	<u>1166</u>	<u>32.0</u>	1166	32.0	1165	32.0
458.sjeng	4	<u>1073</u>	<u>45.1</u>	1073	45.1	1073	45.1	4	<u>987</u>	<u>49.0</u>	987	49.0	988	49.0
462.libquantum	4	3533	23.5	3533	23.5	<u>3533</u>	<u>23.5</u>	4	3520	23.5	<u>3520</u>	<u>23.5</u>	3519	23.6
464.h264ref	4	1169	75.7	<u>1169</u>	<u>75.7</u>	1168	75.8	4	1140	77.6	<u>1141</u>	<u>77.6</u>	1141	77.6
471.omnetpp	4	960	26.0	960	26.0	<u>960</u>	<u>26.0</u>	4	<u>900</u>	<u>27.8</u>	900	27.8	900	27.8
473.astar	4	<u>938</u>	<u>29.9</u>	938	29.9	938	29.9	4	<u>938</u>	<u>29.9</u>	938	29.9	938	30.0
483.xalancbmk	4	597	46.2	<u>596</u>	<u>46.3</u>	596	46.3	4	583	47.3	<u>583</u>	<u>47.4</u>	583	47.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

The NovaScale R440 and the NovaScale R460 models are electronically equivalent.
The results have been measured on a NovaScale R460 model.

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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Base Optimization Flags (Continued)

C++ benchmarks:
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

C++ benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/flags.20090714.00.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/flags.20090714.00.xml>

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