SPEC® CINT2006 Result

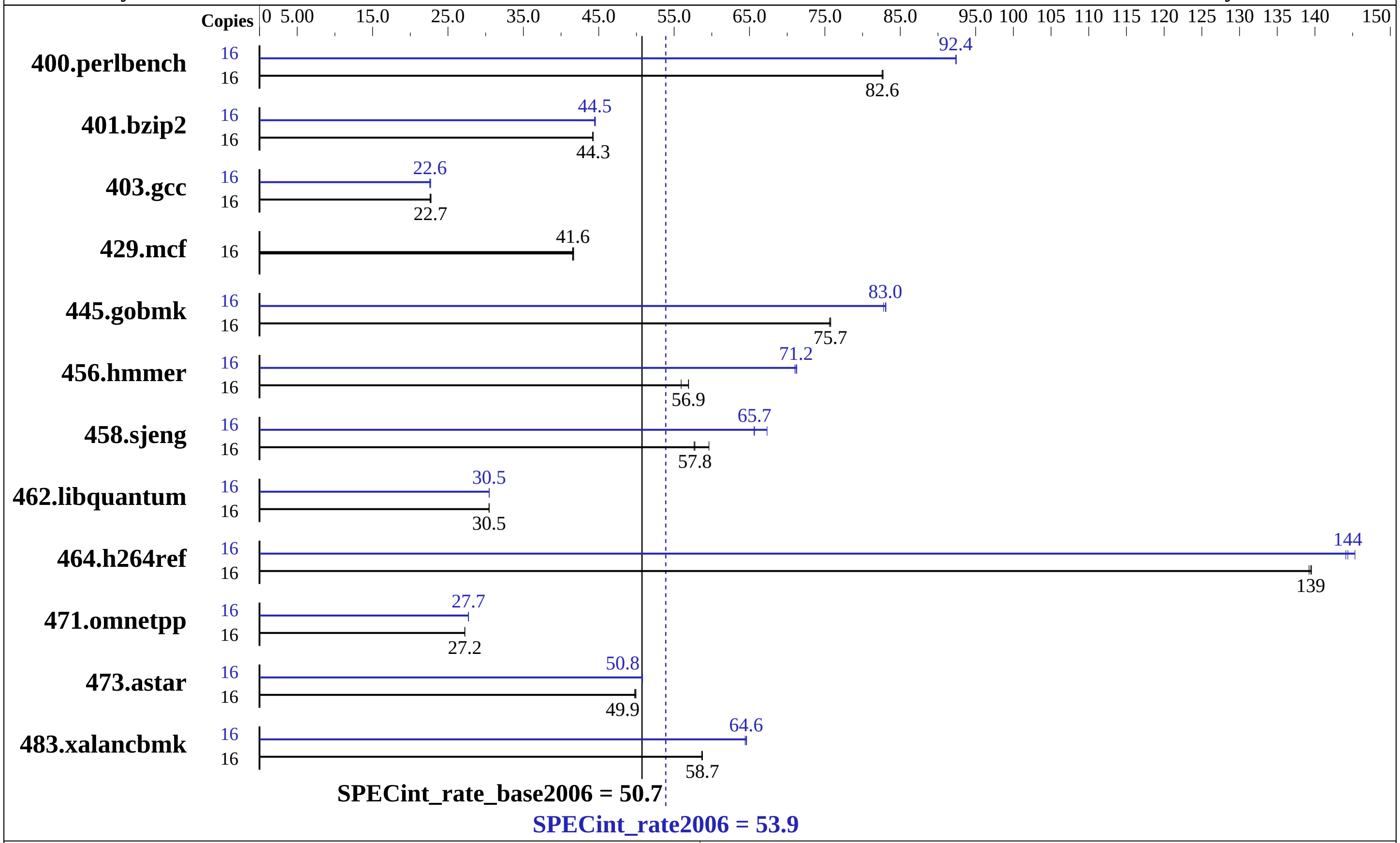
Bull SAS

NovaScale T880 (3.0 GHz, Intel Xeon 7120M)

SPECint®_rate2006 = 53.9

SPECint_rate_base2006 = 50.7

CPU2006 license: 20		**Test date:**	May-2007
Test sponsor:	Bull SAS	**Hardware Availability:**	Sep-2006
Tested by:	Bull SAS	**Software Availability:**	Nov-2006



Hardware

CPU Name:	Intel Xeon 7120M
CPU Characteristics:	3.0 GHz, 800 MHz bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	8 cores, 4 chips, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1,2,4 chips
Primary Cache:	12 K micro-ops I + 16 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (16x2 GB) DDR2 400 PC2-3200R-333
Disk Subsystem:	2x36 GB SAS 15000 RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise X64 Edition
Compiler:	Intel C++ Compiler for IA32 version 9.1 Package ID W_CC_C_9.1.033 Build no 20061103Z Microsoft Visual Studio .NET 2003 (lib & linker)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library 8.0 (shlW32M.lib)

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	**1892**	**82.6**	1893	82.6	1890	82.7	16	1691	92.4	**1692**	**92.4**	1692	92.4
401.bzip2	16	3488	44.3	3495	44.2	**3488**	**44.3**	16	**3472**	**44.5**	3474	44.4	3464	44.6
403.gcc	16	5693	22.6	5658	22.8	**5679**	**22.7**	16	5670	22.7	**5693**	**22.6**	5702	22.6
429.mcf	16	**3509**	**41.6**	3501	41.7	3514	41.5	16	**3509**	**41.6**	3501	41.7	3514	41.5
445.gobmk	16	2220	75.6	2215	75.8	**2216**	**75.7**	16	2028	82.8	**2022**	**83.0**	2020	83.1
456.hmmer	16	2669	55.9	**2624**	**56.9**	2622	56.9	16	2095	71.3	**2097**	**71.2**	2102	71.0
458.sjeng	16	3358	57.6	**3352**	**57.8**	3247	59.6	16	2875	67.3	**2948**	**65.7**	2951	65.6
462.libquantum	16	**10882**	**30.5**	10882	30.5	10894	30.4	16	**10883**	**30.5**	10882	30.5	10884	30.5
464.h264ref	16	2543	139	**2539**	**139**	2537	140	16	2457	144	**2453**	**144**	2437	145
471.omnetpp	16	3671	27.2	**3671**	**27.2**	3672	27.2	16	3608	27.7	**3608**	**27.7**	3609	27.7
473.astar	16	2258	49.8	**2252**	**49.9**	2249	49.9	16	**2210**	**50.8**	2213	50.8	2210	50.8
483.xalancbmk	16	**1880**	**58.7**	1879	58.8	1882	58.7	16	1714	64.4	1708	64.6	**1710**	**64.6**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Other Configuration Notes

The NovaScale T880 and the NovaScale R480 models are
electronically equivalent.
The results have been measured on a NovaScale R480 model.
```

Base Compiler Invocation

C benchmarks:
`icl -Qvc7.1 -Qc99`

C++ benchmarks:
`icl -Qvc7.1`

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`

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Base Portability Flags (Continued)

464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE

401.bzip2: Same as 400.perlbench

Continued on next page

Peak Optimization Flags (Continued)

403.gcc: `Same as 400.perlbench`

429.mcf: `basepeak = yes`

445.gobmk: `Same as 400.perlbench`

456.hmmer: `Same as 400.perlbench`

458.sjeng: `Same as 400.perlbench`

462.libquantum: `Same as 400.perlbench`

464.h264ref: `Same as 400.perlbench`

C++ benchmarks:
`-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/flags.20090714.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/flags.20090714.00.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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