



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

PRIMERGY TX300 S3, Intel Xeon processor 5160, 3.0 GHz

SPECint®\_rate2006 = 59.1

SPECint\_rate\_base2006 = 56.3

CPU2006 license: 22

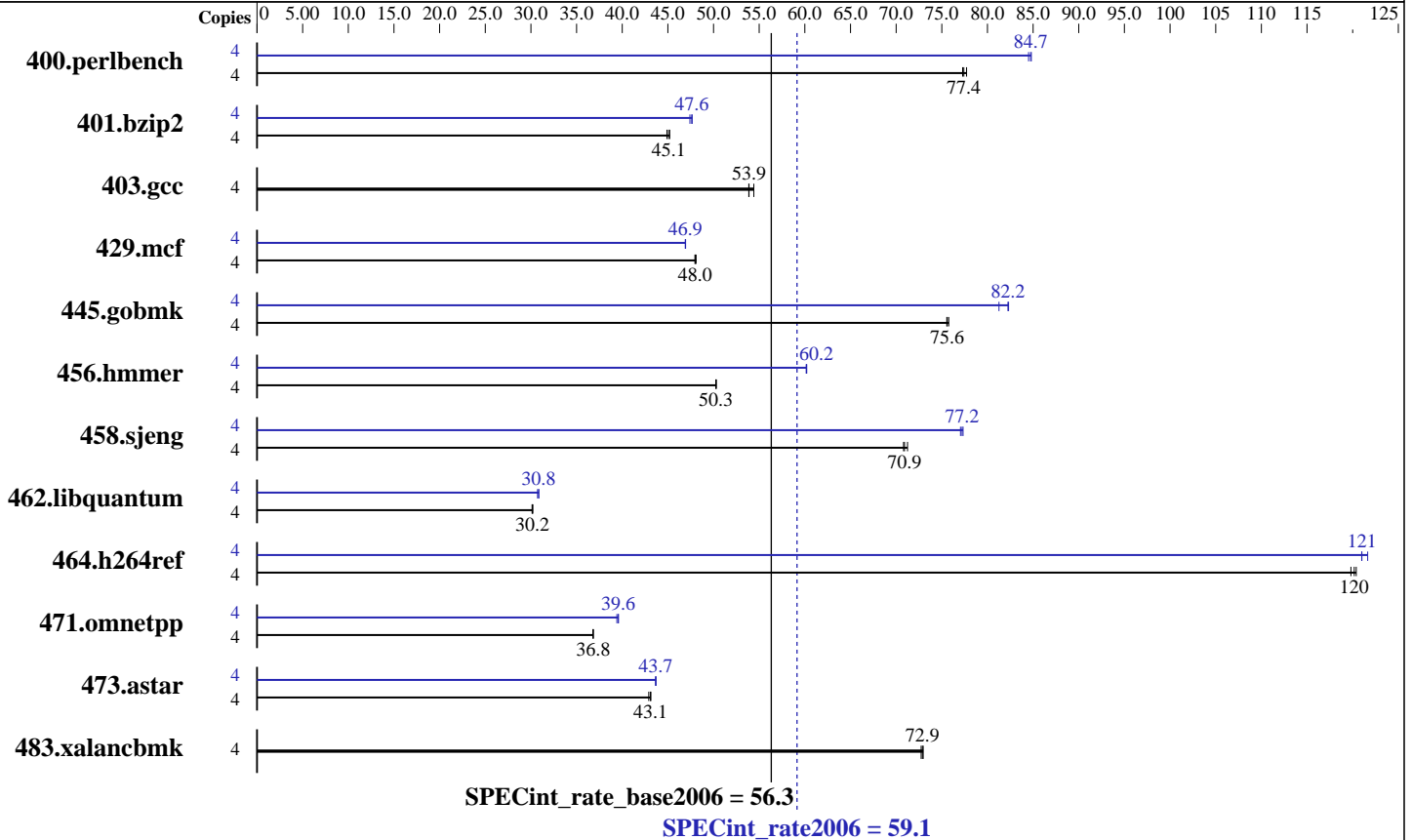
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: May-2007

Hardware Availability: Jul-2006

Software Availability: Feb-2007



### Hardware

CPU Name: Intel Xeon 5160  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (8x1 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)  
 Disk Subsystem: SAS (73GB 15400 rpm)  
 Other Hardware: None

### Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86\_64  
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l\_cc\_p\_9.1.047  
 Auto Parallel: No  
 File System: ext2  
 System State: Multiuser, Runlevel 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Smart Heap Library, Version 8.1



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<b>505</b>	<b>77.4</b>	506	77.3	503	77.7	4	462	84.5	<b>461</b>	<b>84.7</b>	461	84.8
401.bzip2	4	854	45.2	860	44.9	<b>856</b>	<b>45.1</b>	4	814	47.4	<b>811</b>	<b>47.6</b>	810	47.7
403.gcc	4	<b>598</b>	<b>53.9</b>	592	54.4	598	53.9	4	<b>598</b>	<b>53.9</b>	592	54.4	598	53.9
429.mcf	4	760	48.0	<b>760</b>	<b>48.0</b>	759	48.1	4	778	46.9	<b>778</b>	<b>46.9</b>	777	46.9
445.gobmk	4	555	75.5	554	75.8	<b>555</b>	<b>75.6</b>	4	510	82.3	516	81.3	<b>510</b>	<b>82.2</b>
456.hmmmer	4	743	50.2	<b>742</b>	<b>50.3</b>	742	50.3	4	620	60.2	621	60.1	<b>620</b>	<b>60.2</b>
458.sjeng	4	684	70.8	679	71.2	<b>682</b>	<b>70.9</b>	4	628	77.0	626	77.3	<b>627</b>	<b>77.2</b>
462.libquantum	4	<b>2748</b>	<b>30.2</b>	2751	30.1	2743	30.2	4	2685	30.9	<b>2687</b>	<b>30.8</b>	2698	30.7
464.h264ref	4	739	120	<b>737</b>	<b>120</b>	735	120	4	728	122	732	121	<b>731</b>	<b>121</b>
471.omnetpp	4	<b>679</b>	<b>36.8</b>	679	36.8	679	36.8	4	632	39.6	634	39.4	<b>632</b>	<b>39.6</b>
473.astar	4	<b>652</b>	<b>43.1</b>	651	43.1	654	42.9	4	643	43.7	<b>643</b>	<b>43.7</b>	643	43.7
483.xalancbmk	4	378	73.0	<b>379</b>	<b>72.9</b>	379	72.7	4	378	73.0	<b>379</b>	<b>72.9</b>	379	72.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
'/usr/bin/taskset' used to bind processes to CPUs

## General Notes

The system bus runs at 1333 MHz

All binaries were built with 32-bit Intel compiler except:  
401.bzip2, 456.hmmmer and 462.libquantum in peak were built with  
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:  
Adjacent Sector Prefetch = Disable

This result was measured on the PRIMERGY RX300 S3. The PRIMERGY RX300 S3 and  
the PRIMERGY TX300 S3 are electronically equivalent.

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## Base Compiler Invocation

C benchmarks:  
icc

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast

C++ benchmarks:  
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap\_8\_1/lib -lsmartheap

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:  
icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

456.hmmcr: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof\_gen(pass 1) -prof\_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-L/opt/SmartHeap\_8\_1/lib -lsmarheap

445.gobmk: Same as 429.mcf

456.hmmcr: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof\_gen(pass 1) -prof\_use(pass 2) -xP -O3 -ipo  
-no-prec-div -L/opt/SmartHeap\_8\_1/lib -lsmarheap

473.astar: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-L/opt/SmartHeap\_8\_1/lib -lsmarheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.html](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html)

You can also download the XML flags source by saving the following link:

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.xml](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml)



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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