SPEC® CINT2006 Result

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Fujitsu Siemens Computers

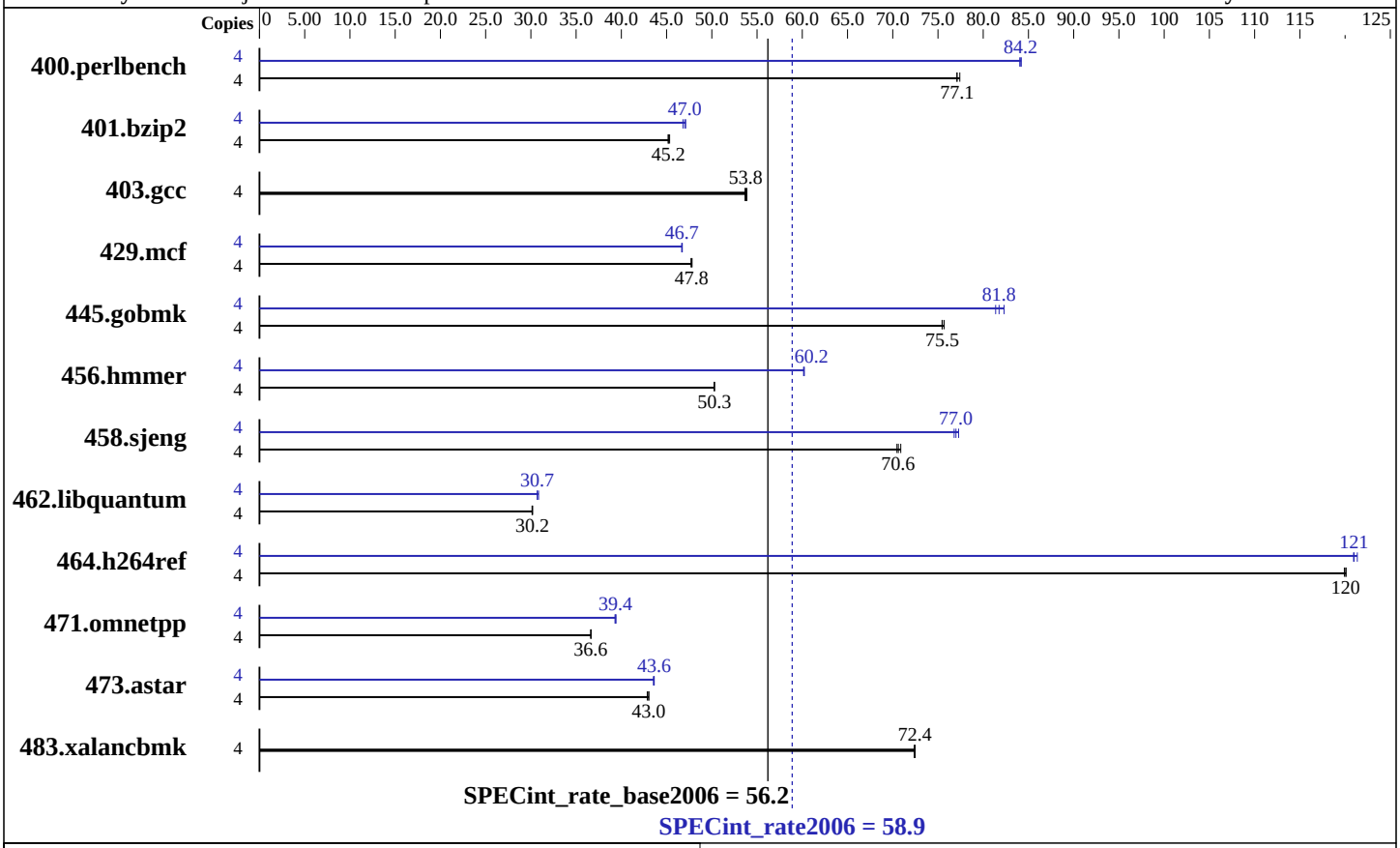
PRIMERGY RX200 S3, Intel Xeon processor 5160, 3.0 GHz

SPECint®_rate2006 = 58.9

SPECint_rate_base2006 = 56.2

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: May-2007
Hardware Availability: Jul-2006
Software Availability: Feb-2007



Hardware

CPU Name:	Intel Xeon 5160
CPU Characteristics:	1333 MHz system bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8x1 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	SAS (73GB 15400 rpm)
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
Auto Parallel:	No
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	505	77.4	**507**	**77.1**	507	77.1	4	464	84.2	**464**	**84.2**	465	84.0
401.bzip2	4	851	45.3	**854**	**45.2**	854	45.2	4	825	46.8	**821**	**47.0**	819	47.1
403.gcc	4	598	53.9	**599**	**53.8**	600	53.7	4	598	53.9	**599**	**53.8**	600	53.7
429.mcf	4	**764**	**47.8**	763	47.8	765	47.7	4	781	46.7	**781**	**46.7**	782	46.7
445.gobmk	4	554	75.7	556	75.4	**556**	**75.5**	4	**513**	**81.8**	510	82.3	516	81.4
456.hmmer	4	742	50.3	742	50.3	**742**	**50.3**	4	**620**	**60.2**	620	60.2	620	60.1
458.sjeng	4	683	70.9	**685**	**70.6**	687	70.5	4	**629**	**77.0**	626	77.3	630	76.8
462.libquantum	4	2751	30.1	**2747**	**30.2**	2746	30.2	4	2700	30.7	**2698**	**30.7**	2686	30.9
464.h264ref	4	738	120	**737**	**120**	737	120	4	732	121	729	121	**732**	**121**
471.omnetpp	4	**682**	**36.6**	682	36.6	682	36.6	4	636	39.3	634	39.4	**635**	**39.4**
473.astar	4	655	42.9	**653**	**43.0**	653	43.0	4	645	43.5	644	43.6	**644**	**43.6**
483.xalancbmk	4	381	72.4	**381**	**72.4**	381	72.5	4	381	72.4	**381**	**72.4**	381	72.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

General Notes

```
The system bus runs at 1333 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

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http://www.fujitsu-siemens.com/countries
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

401.bzip2: `-fast`

403.gcc: `basepeak = yes`

429.mcf: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast` `-L/opt/SmartHeap_8_1/lib -lsmartheap`

445.gobmk: `Same as 429.mcf`

456.hmmer: `Same as 400.perlbench`

458.sjeng: `Same as 429.mcf`

462.libquantum: `Same as 400.perlbench`

464.h264ref: `Same as 429.mcf`

C++ benchmarks:

471.omnetpp: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap`

473.astar: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast` `-L/opt/SmartHeap_8_1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml`

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