



# SPEC® CFP2006 Result

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## Supermicro Motherboard PDSM4+

**SPECfp®2006 = 11.1**  
**SPECfp\_base2006 = 10.6**

CPU2006 license: 001176

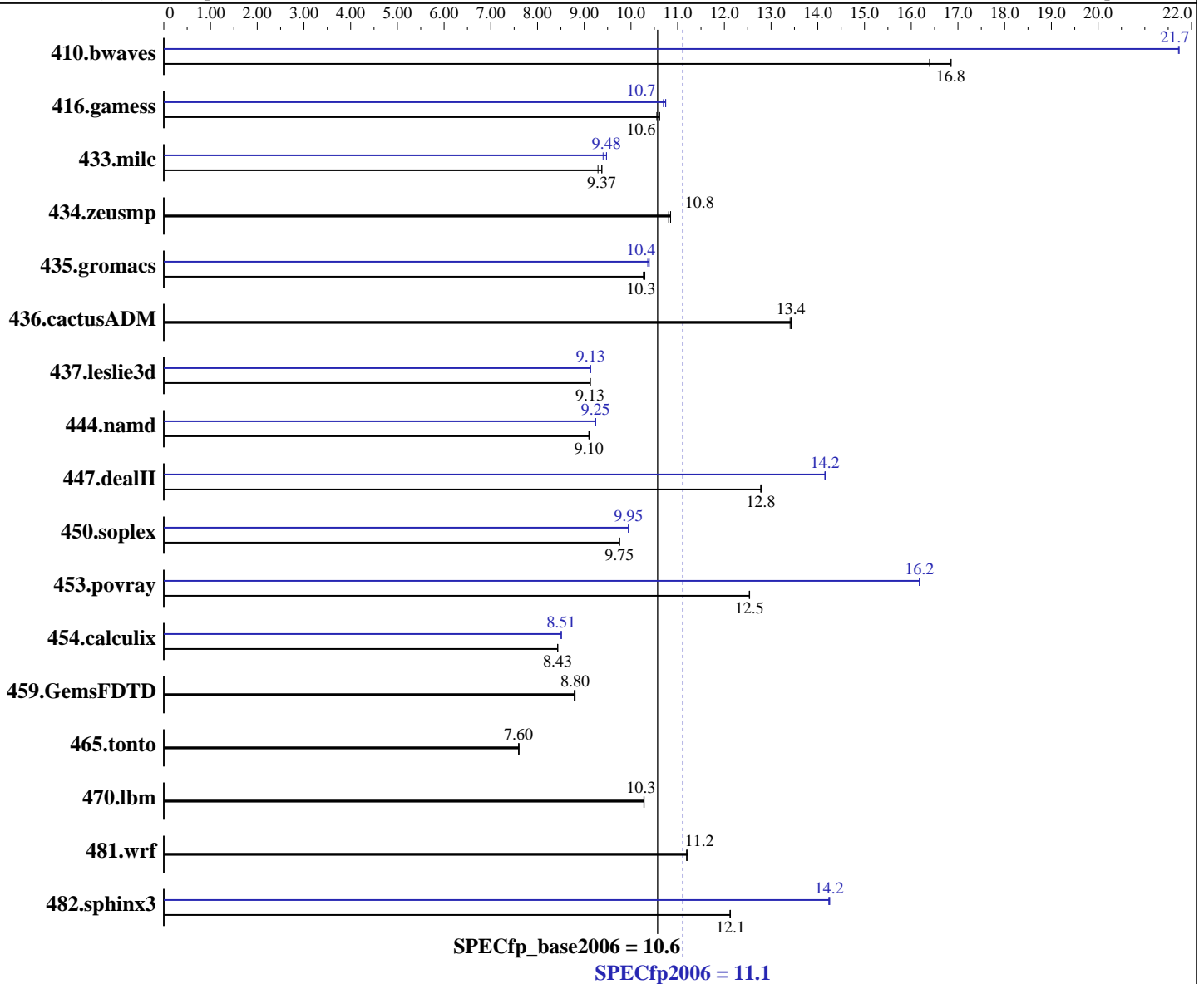
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Apr-2007

Hardware Availability: Apr-2007

Software Availability: Apr-2007



**Hardware**

CPU Name: Intel Core 2 Duo E4300  
 CPU Characteristics: 1.8GHz, 800MHz bus  
 CPU MHz: 1800  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 2 MB I+D on chip per chip

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**Software**

Operating System: Windows XP Professional w/ SP2  
 Compiler: Intel C++ Compiler for IA32 version 9.1  
 Build no 20070322Z  
 Intel Fortran Compiler for IA32 version 9.1  
 Build no 20070322Z  
 Auto Parallel: Yes  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit

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L3 Cache: None  
Other Cache: None  
Memory: 2 GB (4 X 512MB, DDR2 667MHz, CL5, ECC)  
Disk Subsystem: WD2500YS-01SHB1 250GB SATA II, 7200RPM  
Other Hardware: None

Peak Pointers: 32-bit  
Other Software: SmartHeap Library Version 8.0 from  
<http://www.microquill.com/>

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	829	16.4	<b>807</b>	<b>16.8</b>	806	16.9	626	21.7	625	21.7	<b>625</b>	<b>21.7</b>
416.gamess	1855	10.6	1845	10.6	<b>1846</b>	<b>10.6</b>	1831	10.7	1822	10.7	<b>1823</b>	<b>10.7</b>
433.milc	988	9.29	979	9.38	<b>979</b>	<b>9.37</b>	976	9.40	969	9.48	<b>969</b>	<b>9.48</b>
434.zeusmp	842	10.8	839	10.8	<b>839</b>	<b>10.8</b>	842	10.8	839	10.8	<b>839</b>	<b>10.8</b>
435.gromacs	695	10.3	693	10.3	<b>694</b>	<b>10.3</b>	689	10.4	687	10.4	<b>687</b>	<b>10.4</b>
436.cactusADM	891	13.4	890	13.4	<b>890</b>	<b>13.4</b>	891	13.4	890	13.4	<b>890</b>	<b>13.4</b>
437.leslie3d	<b>1030</b>	<b>9.13</b>	1030	9.13	1030	9.13	1029	9.13	<b>1029</b>	<b>9.13</b>	1030	9.13
444.namd	881	9.11	<b>881</b>	<b>9.10</b>	881	9.10	<b>867</b>	<b>9.25</b>	867	9.25	868	9.24
447.dealII	895	12.8	<b>895</b>	<b>12.8</b>	895	12.8	808	14.2	<b>808</b>	<b>14.2</b>	808	14.2
450.soplex	855	9.76	855	9.75	<b>855</b>	<b>9.75</b>	838	9.95	838	9.95	<b>838</b>	<b>9.95</b>
453.povray	424	12.5	<b>424</b>	<b>12.5</b>	424	12.5	329	16.2	<b>329</b>	<b>16.2</b>	329	16.2
454.calculix	978	8.43	<b>979</b>	<b>8.43</b>	979	8.43	970	8.51	<b>970</b>	<b>8.51</b>	970	8.51
459.GemsFDTD	1205	8.80	1208	8.79	<b>1205</b>	<b>8.80</b>	1205	8.80	1208	8.79	<b>1205</b>	<b>8.80</b>
465.tonto	1295	7.60	<b>1295</b>	<b>7.60</b>	1294	7.60	1295	7.60	<b>1295</b>	<b>7.60</b>	1294	7.60
470.lbm	1336	10.3	<b>1337</b>	<b>10.3</b>	1337	10.3	1336	10.3	<b>1337</b>	<b>10.3</b>	1337	10.3
481.wrf	998	11.2	996	11.2	<b>996</b>	<b>11.2</b>	998	11.2	996	11.2	<b>996</b>	<b>11.2</b>
482.sphinx3	<b>1607</b>	<b>12.1</b>	1607	12.1	1608	12.1	<b>1368</b>	<b>14.2</b>	1367	14.3	1369	14.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Tested systems can be used with CSE-823S-R500LP case,  
For a general system, a 420W (minimum) ATX12V power supply [8-pin +12V AND 24-pin is recommended to assure system stability].  
Product description located as of <http://www.supermicro.com/products/motherboard/Xeon3000/3010/PDSM4+.cfm>  
The system bus runs at 800 MHz

## Base Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99  
  
C++ benchmarks:  
icl -Qvc7.1

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**Test date:** Apr-2007  
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**Software Availability:** Apr-2007

## Base Compiler Invocation (Continued)

Fortran benchmarks:  
ifort

Benchmarks using both Fortran and C:  
icl -Qvc7.1 -Qc99 ifort

## Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore  
444.namd: -TP  
447.dealII: -DDEAL\_II\_MEMBER\_VAR\_SPECIALIZATION\_BUG  
-DBOOST\_NO\_INTRINSIC\_WCHAR\_T  
453.povray: -DSPEC\_CPU\_WINDOWS\_ICL  
454.calculix: -DSPEC\_CPU\_NOZMODIFIER -Qlowercase  
481.wrf: -DSPEC\_CPU\_WINDOWS\_ICL

## Base Optimization Flags

C benchmarks:  
-fast /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:  
-fast -Qcxx\_features /F950000000 shlw32m.lib  
-link /FORCE:MULTIPLE

Fortran benchmarks:  
-fast /F950000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:  
-fast /F950000000 -link /FORCE:MULTIPLE

## Peak Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99

C++ benchmarks:  
icl -Qvc7.1

Fortran benchmarks:  
ifort

Benchmarks using both Fortran and C:  
icl -Qvc7.1 -Qc99 ifort



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Tested by: Supermicro

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Hardware Availability: Apr-2007

Software Availability: Apr-2007

## Peak Portability Flags

```

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
           -DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

```

## Peak Optimization Flags

### C benchmarks:

```

433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000
          shlw32m.lib -link /FORCE:MULTIPLE

```

470.lbm: basepeak = yes

```

482.sphinx3: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxB -Qipo -O3
             -Qprec-div- /F950000000 shlw32m.lib
             -link /FORCE:MULTIPLE

```

### C++ benchmarks:

```

-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F950000000 shlw32m.lib -link /FORCE:MULTIPLE

```

### Fortran benchmarks:

```

410.bwaves: -QxW -Qparallel -Qipo -O3 -Qprec-div- /F950000000
            libguide.lib libguide40.lib -link /FORCE:MULTIPLE

```

416.gamess: Same as 410.bwaves

434.zeusmp: basepeak = yes

```

437.leslie3d: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000
              -link /FORCE:MULTIPLE

```

459.GemsFDTD: basepeak = yes

465.tonto: basepeak = yes

### Benchmarks using both Fortran and C:

```

435.gromacs: -QxW -Qparallel -Qipo -O3 -Qprec-div- /F950000000
            shlw32m.lib libguide.lib libguide40.lib
            -link /FORCE:MULTIPLE

```

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## Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast /F950000000  
-link /FORCE:MULTIPLE

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at  
<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.html>

You can also download the XML flags source by saving the following link:  
<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
Report generated on Tue Jul 22 11:41:33 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 29 May 2007.