



SPEC® CINT2006 Result

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Acer Incorporated

SPECint®_rate2006 = 46.3

Acer Altos R520 (Intel Xeon X5355, 2.66GHz)

SPECint_rate_base2006 = 43.4

CPU2006 license: 97

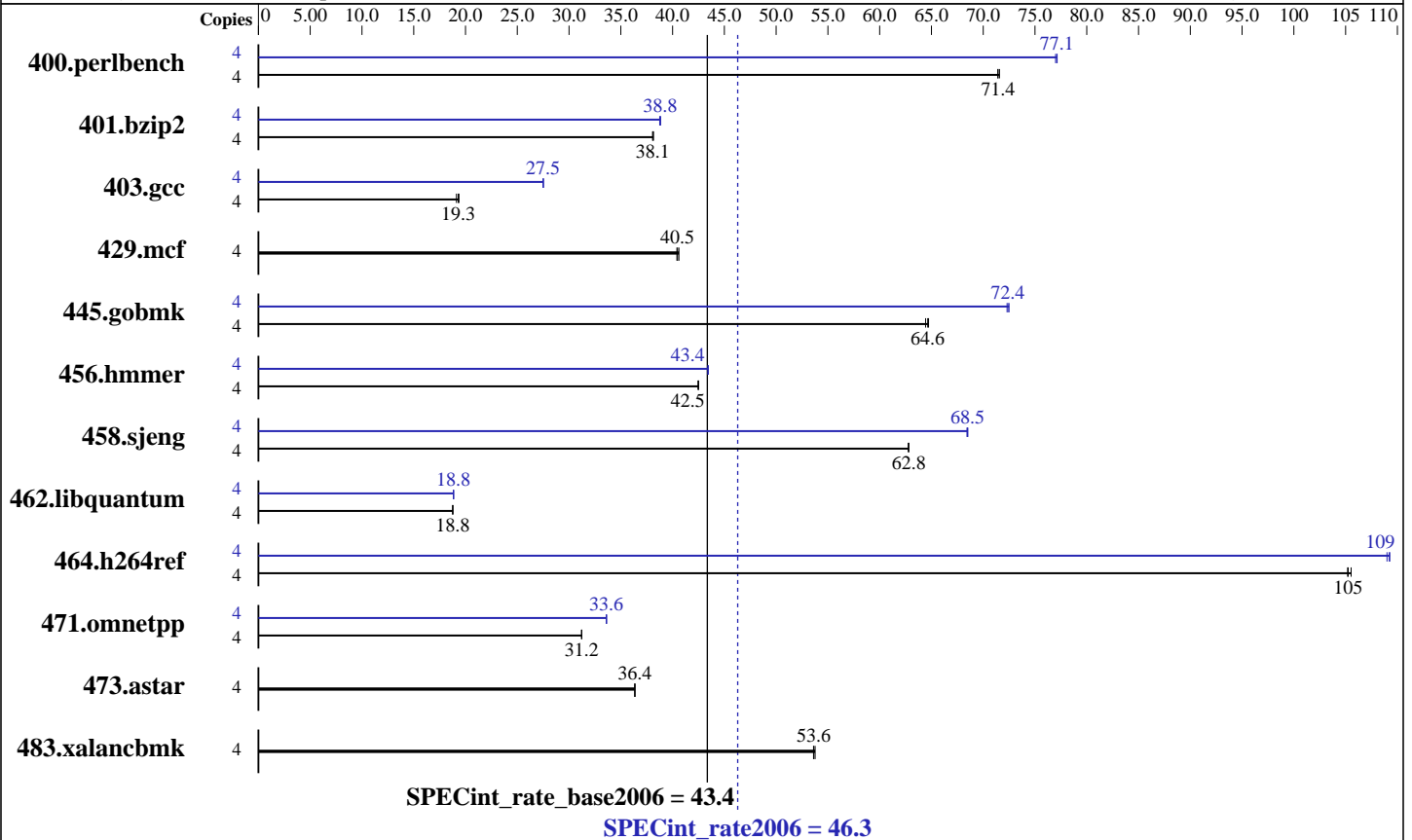
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Mar-2007

Hardware Availability: Nov-2006

Software Availability: Jul-2006



Hardware

CPU Name: Intel Xeon X5355
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)
 Disk Subsystem: 1 x 73GB 10000RPM SAS HDD
 Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition (Build 3790), Service Pack 1
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Package ID W_CC_C_9.1.028 Build 20060706Z
 Microsoft Visual Studio 2003 .Net (libr. & linker)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: MicroQuill Smart Heap Library, Version 7.4



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	547	71.4	547	71.4	546	71.6	4	507	77.1	508	77.0	507	77.1
401.bzip2	4	1014	38.1	1014	38.1	1012	38.2	4	996	38.8	994	38.8	995	38.8
403.gcc	4	1663	19.4	1669	19.3	1684	19.1	4	1171	27.5	1170	27.5	1171	27.5
429.mcf	4	898	40.6	903	40.4	901	40.5	4	898	40.6	903	40.4	901	40.5
445.gobmk	4	651	64.4	648	64.7	649	64.6	4	580	72.3	580	72.4	579	72.5
456.hammer	4	879	42.5	878	42.5	878	42.5	4	859	43.4	859	43.4	859	43.4
458.sjeng	4	771	62.8	771	62.8	771	62.8	4	707	68.4	706	68.5	707	68.5
462.libquantum	4	4416	18.8	4416	18.8	4416	18.8	4	4401	18.8	4396	18.9	4398	18.8
464.h264ref	4	839	106	842	105	841	105	4	811	109	810	109	812	109
471.omnetpp	4	801	31.2	801	31.2	801	31.2	4	744	33.6	744	33.6	744	33.6
473.astar	4	772	36.4	772	36.4	772	36.4	4	772	36.4	772	36.4	772	36.4
483.xalancbmk	4	515	53.6	515	53.6	513	53.8	4	515	53.6	515	53.6	513	53.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Disable hardware prefetching and disable adjacent cache line prefetch BIOS setting

This result was measured on the Acer Altos R520.
The Altos R720 and Altos R520 are electronically equivalent.

Base Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32



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Base Optimization Flags

C benchmarks:

`-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

C++ benchmarks:

`-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE`

Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:

`icl -Qvc7.1 -Qc99`

C++ benchmarks:

`icl -Qvc7.1`

Peak Portability Flags

`403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32`

Peak Optimization Flags

C benchmarks:

`400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE`

`401.bzip2: Same as 400.perlbench`

`403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
-link /FORCE:MULTIPLE`

`429.mcf: basepeak = yes`

`445.gobmk: Same as 400.perlbench`

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Peak Optimization Flags (Continued)

456.hmmcr: Same as 400.perlbench

458.sjeng: Same as 400.perlbench

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Acer-CPU2006-ic91-flags.20090714.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Acer-CPU2006-ic91-flags.20090714.00.xml>

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For other inquiries, please contact webmaster@spec.org.

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