SPEC® CINT2006 Result
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Fujitsu Siemens Computers

PRIMERGY TX300 S3, Intel Xeon processor E5310, 1.60 GHz

SPECint®_rate2006 = 62.9
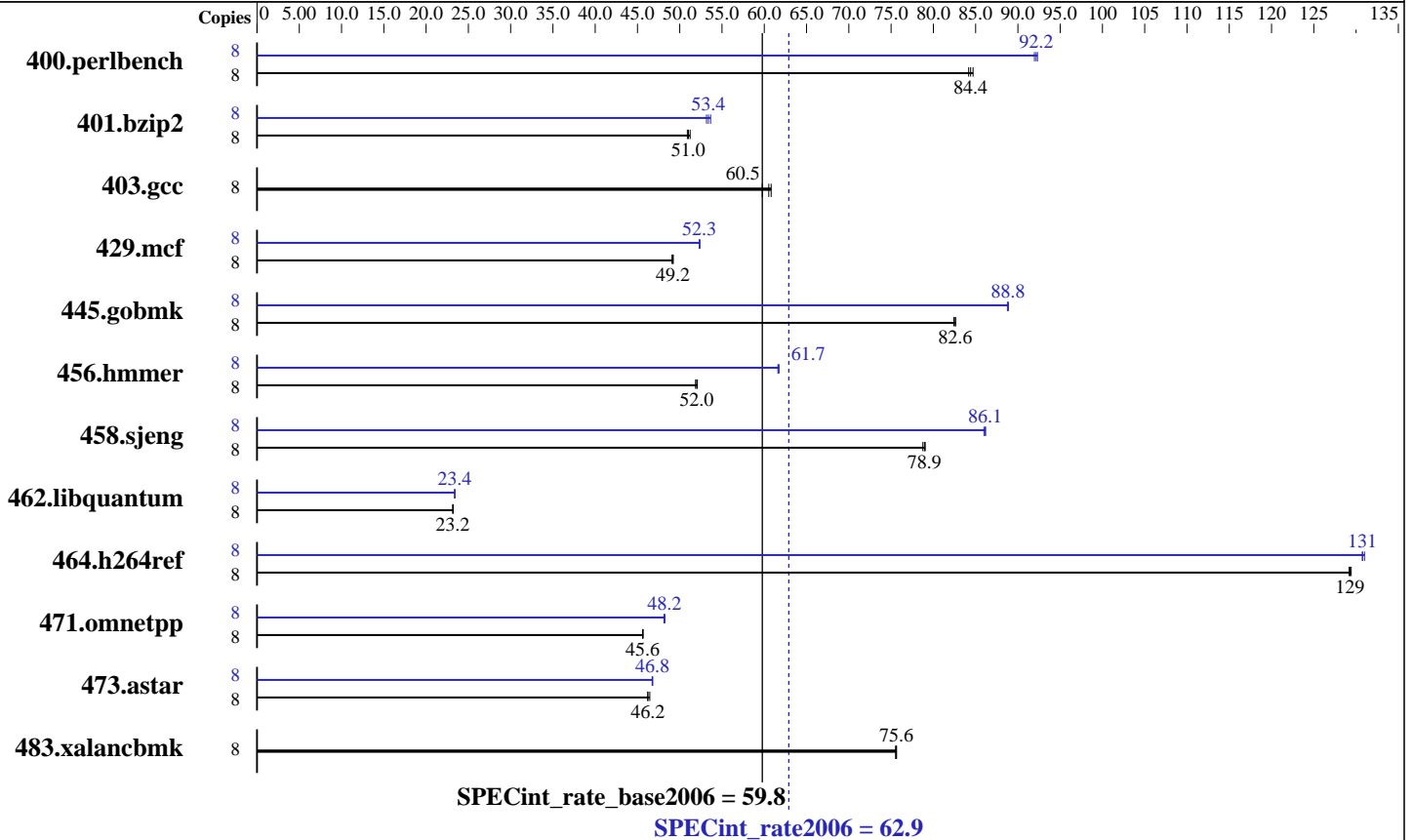
SPECint_rate_base2006 = 59.8

CPU2006 license: 22

Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Apr-2007

Hardware Availability: Nov-2006

Software Availability: Feb-2007

Copies

Benchmark	Copies	Peak	Base
400.perlbench	8	92.2	84.4
401.bzip2	8	53.4	51.0
403.gcc	8		60.5
429.mcf	8	52.3	49.2
445.gobmk	8	88.8	82.6
456.hmmer	8	61.7	52.0
458.sjeng	8	86.1	78.9
462.libquantum	8	23.4	23.2
464.h264ref	8	131	129
471.omnetpp	8	48.2	45.6
473.astar	8	46.8	46.2
483.xalancbmk	8		75.6

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Hardware

CPU Name:	Intel Xeon E5310
CPU Characteristics:	1067 MHz system bus
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	SAS (73GB 15400 rpm)
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
Auto Parallel:	No
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1

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Results Table

Benchmark	Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
400.perlbench	8	**926**	**84.4**	929	84.2	923	84.7	8	850	92.0	847	92.3	**848**	**92.2**
401.bzip2	8	**1513**	**51.0**	1507	51.2	1517	50.9	8	1439	53.7	**1446**	**53.4**	1452	53.2
403.gcc	8	**1064**	**60.5**	1059	60.8	1078	59.8	8	**1064**	**60.5**	1059	60.8	1078	59.8
429.mcf	8	**1483**	**49.2**	1486	49.1	1482	49.2	8	1394	52.3	**1394**	**52.3**	1393	52.4
445.gobmk	8	**1016**	**82.6**	1018	82.4	1016	82.6	8	944	88.9	945	88.8	**945**	**88.8**
456.hmmer	8	**1435**	**52.0**	1433	52.1	1439	51.9	8	1209	61.7	**1210**	**61.7**	1211	61.6
458.sjeng	8	**1226**	**78.9**	1225	79.0	1229	78.8	8	1126	86.0	1123	86.2	**1124**	**86.1**
462.libquantum	8	7157	23.2	**7154**	**23.2**	7145	23.2	8	7087	23.4	**7086**	**23.4**	7085	23.4
464.h264ref	8	1368	129	**1369**	**129**	1371	129	8	**1354**	**131**	1351	131	1354	131
471.omnetpp	8	**1096**	**45.6**	1097	45.6	1095	45.7	8	1036	48.2	**1038**	**48.2**	1039	48.1
473.astar	8	1209	46.5	**1215**	**46.2**	1215	46.2	8	1202	46.7	1200	46.8	**1200**	**46.8**
483.xalancbmk	8	**730**	**75.6**	731	75.5	730	75.6	8	**730**	**75.6**	731	75.5	730	75.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

    'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
    '/usr/bin/taskset' used to bind processes to CPUs

General Notes

    The system bus runs at 1067 MHz

    All binaries were built with 32-bit Intel compiler except:
    401.bzip2, 456.hmmer and 462.libquantum in peak were built with
    64-bit Intel compiler by changing the path for include and library files.

    BIOS configuration:
    Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

    This result was measured on the PRIMERGY RX300 S3. The PRIMERGY RX300 S3 and
    the PRIMERGY TX300 S3 are electronically equivalent.

    For information about Fujitsu Siemens Computers in your country please see:
    http://www.fujitsu-siemens.com/countries

Base Compiler Invocation

C benchmarks:
     icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
    icpc

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_X64
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -fast

C++ benchmarks:
    -xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc

    401.bzip2: /opt/intel/cce/9.1.047/bin/icc
            -I/opt/intel/cce/9.1.047/include
            -L/opt/intel/cce/9.1.047/lib

    456.hmmer: /opt/intel/cce/9.1.047/bin/icc
            -I/opt/intel/cce/9.1.047/include
            -L/opt/intel/cce/9.1.047/lib

    462.libquantum: /opt/intel/cce/9.1.047/bin/icc
            -I/opt/intel/cce/9.1.047/include
            -L/opt/intel/cce/9.1.047/lib

C++ benchmarks:
    icpc

Peak Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_X64
    401.bzip2: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

```
      456.hmmer: -DSPEC_CPU_LP64
  462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
  483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
    400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

       401.bzip2: -fast

         403.gcc: basepeak = yes

         429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast
                  -L/opt/SmartHeap_8_1/lib -lsmartheap

       445.gobmk: Same as 429.mcf

       456.hmmer: Same as 400.perlbench

       458.sjeng: Same as 429.mcf

   462.libquantum: Same as 400.perlbench

      464.h264ref: Same as 429.mcf
```

C++ benchmarks:

```
     471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo
                  -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

        473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast
                  -L/opt/SmartHeap_8_1/lib -lsmartheap

    483.xalancbmk: basepeak = yes
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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