SPEC® CFP2006 Result

Fujitsu Siemens Computers

PRIMERGY BX620 S3, Intel Xeon processor 5130, 2.0 GHz

$SPECfp®\_rate2006 = 37.2$

$SPECfp\_rate\_base2006 = 36.1$

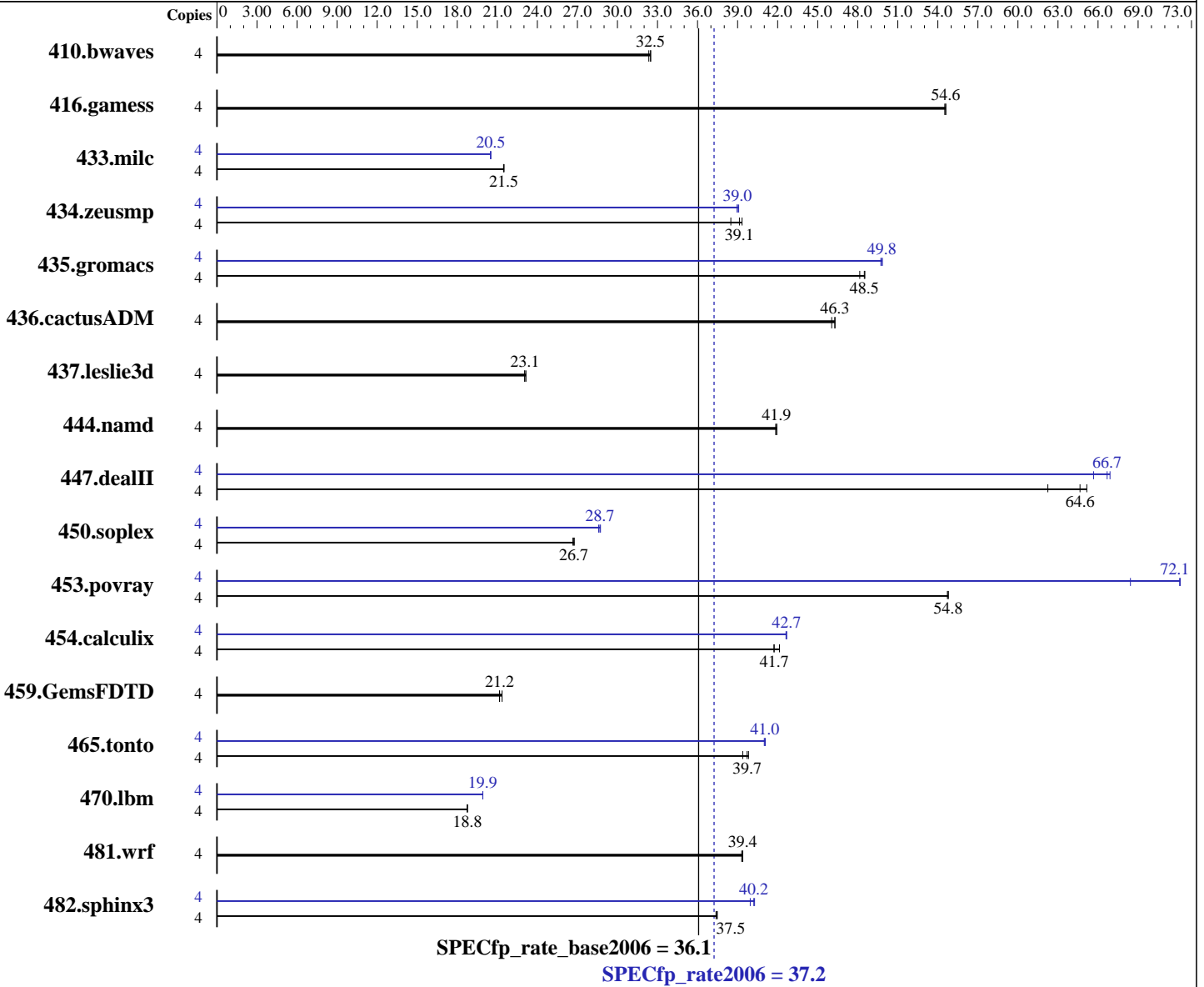|	
CPU2006 license: 22	**Test date:** Apr-2007
Test sponsor: Fujitsu Siemens Computers	**Hardware Availability:** Jul-2006
Tested by: Fujitsu Siemens Computers	**Software Availability:** Feb-2007

Results chart, Copies = 4 for all benchmarks:

- 410.bwaves: 32.5
- 416.gamess: 54.6
- 433.milc: 20.5 / 21.5
- 434.zeusmp: 39.0 / 39.1
- 435.gromacs: 49.8 / 48.5
- 436.cactusADM: 46.3
- 437.leslie3d: 23.1
- 444.namd: 41.9
- 447.dealII: 66.7 / 64.6
- 450.soplex: 28.7 / 26.7
- 453.povray: 72.1 / 54.8
- 454.calculix: 42.7 / 41.7
- 459.GemsFDTD: 21.2
- 465.tonto: 41.0 / 39.7
- 470.lbm: 19.9 / 18.8
- 481.wrf: 39.4
- 482.sphinx3: 40.2 / 37.5

SPECfp_rate_base2006 = 36.1

SPECfp_rate2006 = 37.2

Hardware

CPU Name:	Intel Xeon 5130
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip

Continued on next page

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
	Intel Fortran Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package ID: l_fc_p_9.1.043
Auto Parallel:	No
File System:	ext2

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L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8x1 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	SAS (36GB 10000 rpm)
Other Hardware:	None

System State:	Multiuser, Runlevel 3
Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Base						Peak					
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1680	32.4	1673	32.5	**1674**	**32.5**	4	1680	32.4	1673	32.5	**1674**	**32.5**
416.gamess	4	**1435**	**54.6**	1435	54.6	1437	54.5	4	**1435**	**54.6**	1435	54.6	1437	54.5
433.milc	4	1710	21.5	**1709**	**21.5**	1708	21.5	4	1791	20.5	**1791**	**20.5**	1791	20.5
434.zeusmp	4	**930**	**39.1**	925	39.3	946	38.5	4	**934**	**39.0**	934	39.0	931	39.1
435.gromacs	4	588	48.5	**589**	**48.5**	593	48.2	4	574	49.7	**574**	**49.8**	573	49.8
436.cactusADM	4	1032	46.3	**1033**	**46.3**	1038	46.0	4	1032	46.3	**1033**	**46.3**	1038	46.0
437.leslie3d	4	1631	23.1	1624	23.1	**1631**	**23.1**	4	1631	23.1	1624	23.1	**1631**	**23.1**
444.namd	4	765	41.9	**765**	**41.9**	766	41.9	4	765	41.9	**765**	**41.9**	766	41.9
447.dealII	4	735	62.2	702	65.2	**708**	**64.6**	4	684	66.9	697	65.7	**686**	**66.7**
450.soplex	4	**1248**	**26.7**	1246	26.8	1251	26.7	4	**1163**	**28.7**	1167	28.6	1161	28.7
453.povray	4	389	54.7	388	54.8	**388**	**54.8**	4	**295**	**72.1**	311	68.4	295	72.2
454.calculix	4	791	41.7	783	42.1	**791**	**41.7**	4	774	42.6	773	42.7	**774**	**42.7**
459.GemsFDTD	4	1987	21.4	**2004**	**21.2**	2005	21.2	4	1987	21.4	**2004**	**21.2**	2005	21.2
465.tonto	4	**991**	**39.7**	988	39.8	999	39.4	4	958	41.1	960	41.0	**959**	**41.0**
470.lbm	4	2929	18.8	2932	18.7	**2930**	**18.8**	4	2760	19.9	**2760**	**19.9**	2759	19.9
481.wrf	4	1137	39.3	1135	39.4	**1135**	**39.4**	4	1137	39.3	1135	39.4	**1135**	**39.4**
482.sphinx3	4	2080	37.5	2086	37.4	**2081**	**37.5**	4	**1939**	**40.2**	1952	39.9	1936	40.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

General Notes

The system bus runs at 1333 MHz

All binaries were built with 64-bit Intel compiler except:
433.milc, 434.zeusmp, 450.soplex, 470.lbm and 482.sphinx3 in peak were built with
32-bit Intel compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers in your country please see:

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Hardware Availability: Jul-2006
Software Availability: Feb-2007

General Notes (Continued)

http://www.fujitsu-siemens.com/countries

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Fortran benchmarks:
    ifort

Benchmarks using both Fortran and C:
    icc ifort

Base Portability Flags

        410.bwaves: -DSPEC_CPU_LP64
        416.gamess: -DSPEC_CPU_LP64
          433.milc: -DSPEC_CPU_LP64
        434.zeusmp: -DSPEC_CPU_LP64
       435.gromacs: -DSPEC_CPU_LP64 -nofor_main
      436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
       437.leslie3d: -DSPEC_CPU_LP64
          444.namd: -DSPEC_CPU_LP64
         447.dealII: -DSPEC_CPU_LP64
         450.soplex: -DSPEC_CPU_LP64
         453.povray: -DSPEC_CPU_LP64
        454.calculix: -DSPEC_CPU_LP64 -nofor_main
      459.GemsFDTD: -DSPEC_CPU_LP64
          465.tonto: -DSPEC_CPU_LP64
           470.lbm: -DSPEC_CPU_LP64
           481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
        482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
    -fast

C++ benchmarks:
    -fast

Fortran benchmarks:
    -fast

Continued on next page

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:
```
-fast
```

Peak Compiler Invocation

C benchmarks:
```
/opt/intel/cc/9.1.047/bin/icc -I/opt/intel/cc/9.1.047/include
-L/opt/intel/cc/9.1.047/lib
```

C++ benchmarks (except as noted below):
```
icpc
```
```
 450.soplex: /opt/intel/cc/9.1.047/bin/icpc
             -I/opt/intel/cc/9.1.047/include -L/opt/intel/cc/9.1.047/lib
```

Fortran benchmarks (except as noted below):
```
ifort
```
```
 434.zeusmp: /opt/intel/fc/9.1.043/bin/ifort
             -I/opt/intel/fc/9.1.043/include -L/opt/intel/fc/9.1.043/lib
```

Benchmarks using both Fortran and C:
```
icc ifort
```

Peak Portability Flags

```
   410.bwaves: -DSPEC_CPU_LP64
   416.gamess: -DSPEC_CPU_LP64
  435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
  437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
  454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

433.milc: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

470.lbm: `Same as 433.milc`

482.sphinx3: `-fast`

C++ benchmarks:

444.namd: `basepeak = yes`

447.dealII: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

450.soplex: `Same as 447.dealII`

453.povray: `Same as 447.dealII`

Fortran benchmarks:

410.bwaves: `basepeak = yes`

416.gamess: `basepeak = yes`

434.zeusmp: `-fast`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `basepeak = yes`

465.tonto: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

Benchmarks using both Fortran and C:

435.gromacs: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

436.cactusADM: `basepeak = yes`

454.calculix: `Same as 435.gromacs`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml`

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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