SPEC® CINT2006 Result

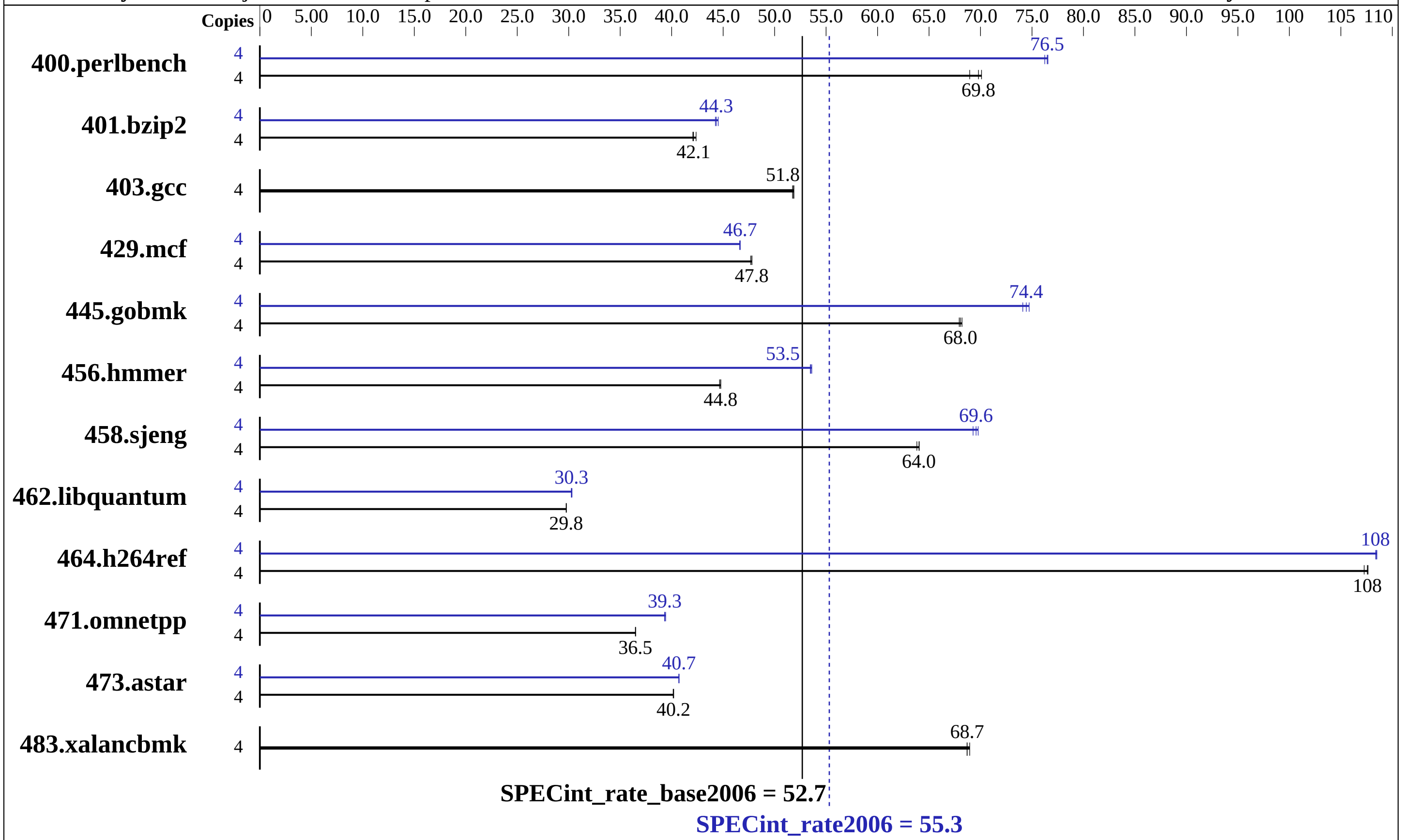
Fujitsu Siemens Computers

PRIMERGY BX620 S3, Intel Xeon processor 5150, 2.66 GHz

SPECint®_rate2006 = 55.3

SPECint_rate_base2006 = 52.7

CPU2006 license: 22		**Test date:**	Apr-2007
Test sponsor:	Fujitsu Siemens Computers	**Hardware Availability:**	Jul-2006
Tested by:	Fujitsu Siemens Computers	**Software Availability:**	Feb-2007



Hardware

CPU Name:	Intel Xeon 5150
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8x1 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	SAS (36GB 10000 rpm)
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
Auto Parallel:	No
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1

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Results Table

	Base							Peak						
Benchmark	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**
400.perlbench	4	567	69.0	**560**	**69.8**	557	70.1	4	511	76.5	**511**	**76.5**	513	76.2
401.bzip2	4	911	42.4	918	42.1	**916**	**42.1**	4	**871**	**44.3**	867	44.5	872	44.2
403.gcc	4	621	51.9	**621**	**51.8**	622	51.7	4	621	51.9	**621**	**51.8**	622	51.7
429.mcf	4	765	47.7	763	47.8	**763**	**47.8**	4	782	46.7	**782**	**46.7**	783	46.6
445.gobmk	4	618	67.9	615	68.2	**617**	**68.0**	4	562	74.7	**564**	**74.4**	566	74.1
456.hmmer	4	834	44.8	**834**	**44.8**	836	44.7	4	698	53.5	696	53.6	**697**	**53.5**
458.sjeng	4	756	64.0	758	63.8	**756**	**64.0**	4	699	69.3	694	69.8	**696**	**69.6**
462.libquantum	4	2786	29.8	2783	29.8	**2785**	**29.8**	4	2739	30.3	**2739**	**30.3**	2733	30.3
464.h264ref	4	822	108	825	107	**823**	**108**	4	816	109	817	108	**816**	**108**
471.omnetpp	4	685	36.5	**685**	**36.5**	685	36.5	4	**636**	**39.3**	636	39.3	634	39.4
473.astar	4	698	40.2	699	40.1	**699**	**40.2**	4	689	40.7	690	40.7	**690**	**40.7**
483.xalancbmk	4	400	68.9	**402**	**68.7**	402	68.7	4	400	68.9	**402**	**68.7**	402	68.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

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'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

General Notes

```
The system bus runs at 1333 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

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http://www.fujitsu-siemens.com/countries
```

Base Compiler Invocation

C benchmarks:
 icc

C++ benchmarks:
 icpc

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_X64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast`

C++ benchmarks:
`-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/cce/9.1.047/bin/icc -I/opt/intel/cce/9.1.047/include -L/opt/intel/cce/9.1.047/lib`

456.hmmer: `/opt/intel/cce/9.1.047/bin/icc -I/opt/intel/cce/9.1.047/include -L/opt/intel/cce/9.1.047/lib`

462.libquantum: `/opt/intel/cce/9.1.047/bin/icc -I/opt/intel/cce/9.1.047/include -L/opt/intel/cce/9.1.047/lib`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

401.bzip2: `-fast`

403.gcc: `basepeak = yes`

429.mcf: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast` `-L/opt/SmartHeap_8_1/lib -lsmartheap`

445.gobmk: `Same as 429.mcf`

456.hmmer: `Same as 400.perlbench`

458.sjeng: `Same as 429.mcf`

462.libquantum: `Same as 400.perlbench`

464.h264ref: `Same as 429.mcf`

C++ benchmarks:

471.omnetpp: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-xP -O3 -ipo` `-no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap`

473.astar: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast` `-L/opt/SmartHeap_8_1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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