SPEC® CINT2006 Result

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Dell Inc.

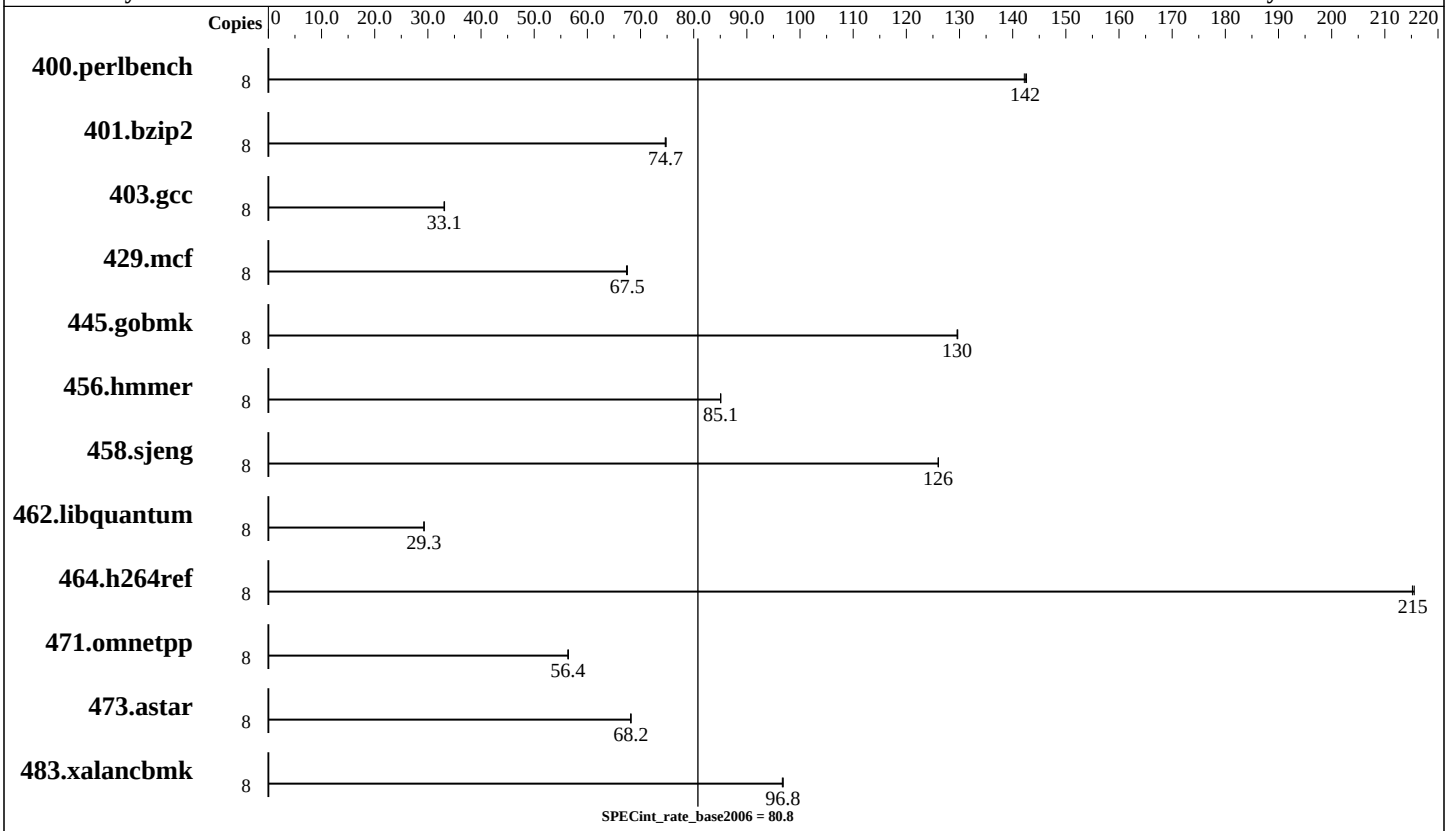
PowerEdge 1950 (Intel Xeon processor X5355, 2.66 GHz)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 80.8

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Mar-2007
Hardware Availability: Dec-2006
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	1333 MHz Bus Speed
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2 GB 667MHz CL5 DDR2 FB-DIMM SDRAM)
Disk Subsystem:	1 x 36GB SAS 15k RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler 9.1 for IA32 (20061103Z) Microsoft Visual Studio 2005
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	MicroQuill SmartHeap Library 8.0

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**549**	**142**	550	142	548	143							
401.bzip2	8	**1033**	**74.7**	1034	74.6	1032	74.8							
403.gcc	8	1943	33.1	1948	33.1	**1947**	**33.1**							
429.mcf	8	**1081**	**67.5**	1081	67.5	1083	67.4							
445.gobmk	8	**648**	**130**	648	130	647	130							
456.hmmer	8	877	85.1	**877**	**85.1**	877	85.1							
458.sjeng	8	768	126	768	126	**768**	**126**							
462.libquantum	8	5664	29.3	**5662**	**29.3**	5659	29.3							
464.h264ref	8	**822**	**215**	823	215	821	216							
471.omnetpp	8	886	56.4	**887**	**56.4**	887	56.4							
473.astar	8	824	68.2	**824**	**68.2**	823	68.2							
483.xalancbmk	8	**570**	**96.8**	571	96.7	570	96.8							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Hardware Prefetcher disabled in BIOS
Adjacent Cache Line Prefetch disabled in BIOS
```

Base Compiler Invocation

C benchmarks:

```
icl -Qc99
```

C++ benchmarks:

```
icl
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlW32M.lib           -link /FORCE:MULTIPLE
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlW32M.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/dell.cpu2006.ic91.flags.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/dell.cpu2006.ic91.flags.xml`

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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