



SPEC® CFP2006 Result

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Hewlett-Packard Company

SPECfp®_rate2006 = 43.7

ProLiant DL360 G5
(1.86 GHz, Intel Xeon processor E5320)

SPECfp_rate_base2006 = 43.0

CPU2006 license: 3

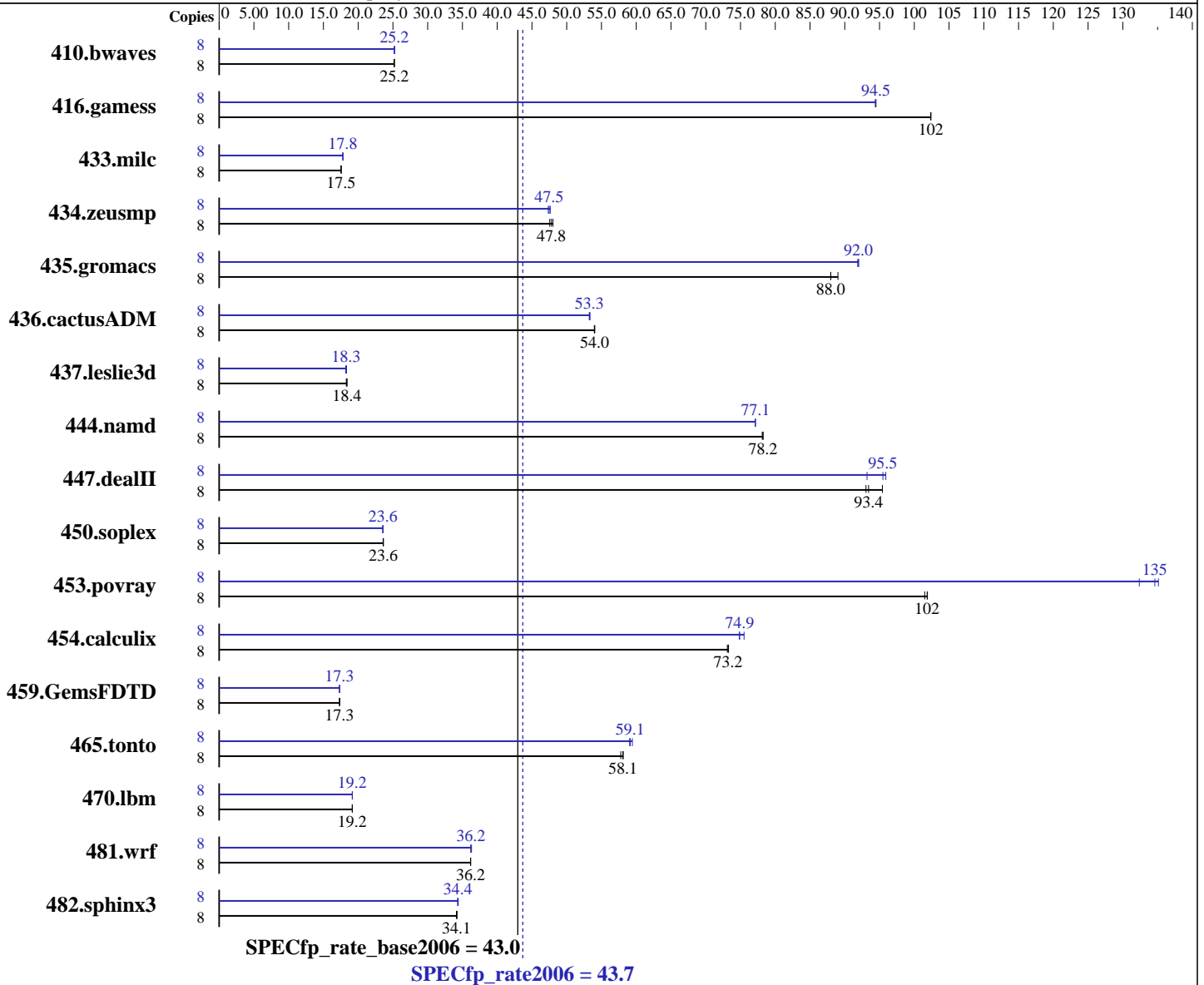
Test date: Feb-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2007

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon E5320
 CPU Characteristics: 1.86 GHz, 2x4 MB L2 shared, 1066 MHz system bus
 CPU MHz: 1860
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

Continued on next page

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64)
 kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ Compiler for Intel EM64T-based applications, Version 9.1
 Build 20061101, Package ID: 1_cc_c_9.1.045
 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1
 Build 20061101, Package ID: 1_fc_c_9.1.040
 Auto Parallel: No

Continued on next page



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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300F CL5)
Disk Subsystem: 2x72 GB 10k SAS
Other Hardware: None

File System: ext2
System State: Multi-user run level 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	4318	25.2	4315	25.2	4315	25.2	8	4305	25.3	4314	25.2	4315	25.2
416.gamess	8	1530	102	1530	102	1530	102	8	1658	94.5	1660	94.4	1658	94.5
433.milc	8	4184	17.6	4187	17.5	4186	17.5	8	4124	17.8	4125	17.8	4128	17.8
434.zeusmp	8	1516	48.0	1531	47.6	1523	47.8	8	1534	47.5	1528	47.6	1538	47.3
435.gromacs	8	649	88.0	642	89.0	649	88.0	8	621	92.0	621	92.0	622	91.9
436.cactusADM	8	1771	54.0	1770	54.0	1769	54.0	8	1795	53.3	1792	53.4	1795	53.3
437.leslie3d	8	4090	18.4	4111	18.3	4092	18.4	8	4119	18.3	4131	18.2	4103	18.3
444.namd	8	821	78.1	820	78.2	820	78.2	8	832	77.1	832	77.1	832	77.2
447.dealII	8	980	93.4	959	95.4	984	93.0	8	982	93.2	954	95.9	958	95.5
450.soplex	8	2829	23.6	2824	23.6	2826	23.6	8	2831	23.6	2832	23.6	2835	23.5
453.povray	8	418	102	419	101	418	102	8	322	132	315	135	316	135
454.calculix	8	903	73.1	901	73.2	901	73.3	8	874	75.5	882	74.8	881	74.9
459.GemsFDTD	8	4897	17.3	4903	17.3	4899	17.3	8	4905	17.3	4902	17.3	4903	17.3
465.tonto	8	1354	58.1	1355	58.1	1362	57.8	8	1333	59.1	1331	59.1	1324	59.4
470.lbm	8	5739	19.2	5739	19.2	5738	19.2	8	5738	19.2	5738	19.2	5737	19.2
481.wrf	8	2469	36.2	2471	36.2	2471	36.2	8	2461	36.3	2469	36.2	2467	36.2
482.sphinx3	8	4566	34.1	4556	34.2	4566	34.1	8	4539	34.4	4546	34.3	4533	34.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"/usr/bin/taskset" used to bind processes to CPUs.
Environment stack size set to 'unlimited'

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Continued on next page



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Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-fast

Fortran benchmarks:

-fast

Benchmarks using both Fortran and C:

-fast

Peak Compiler Invocation

C benchmarks:

icc

Continued on next page



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Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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