



SPEC® CINT2006 Result

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Sun Microsystems Sun Fire X4600 M2

SPECint®_rate2006 = Not Run SPECint_rate_base2006 = 76.9

CPU2006 license: 6

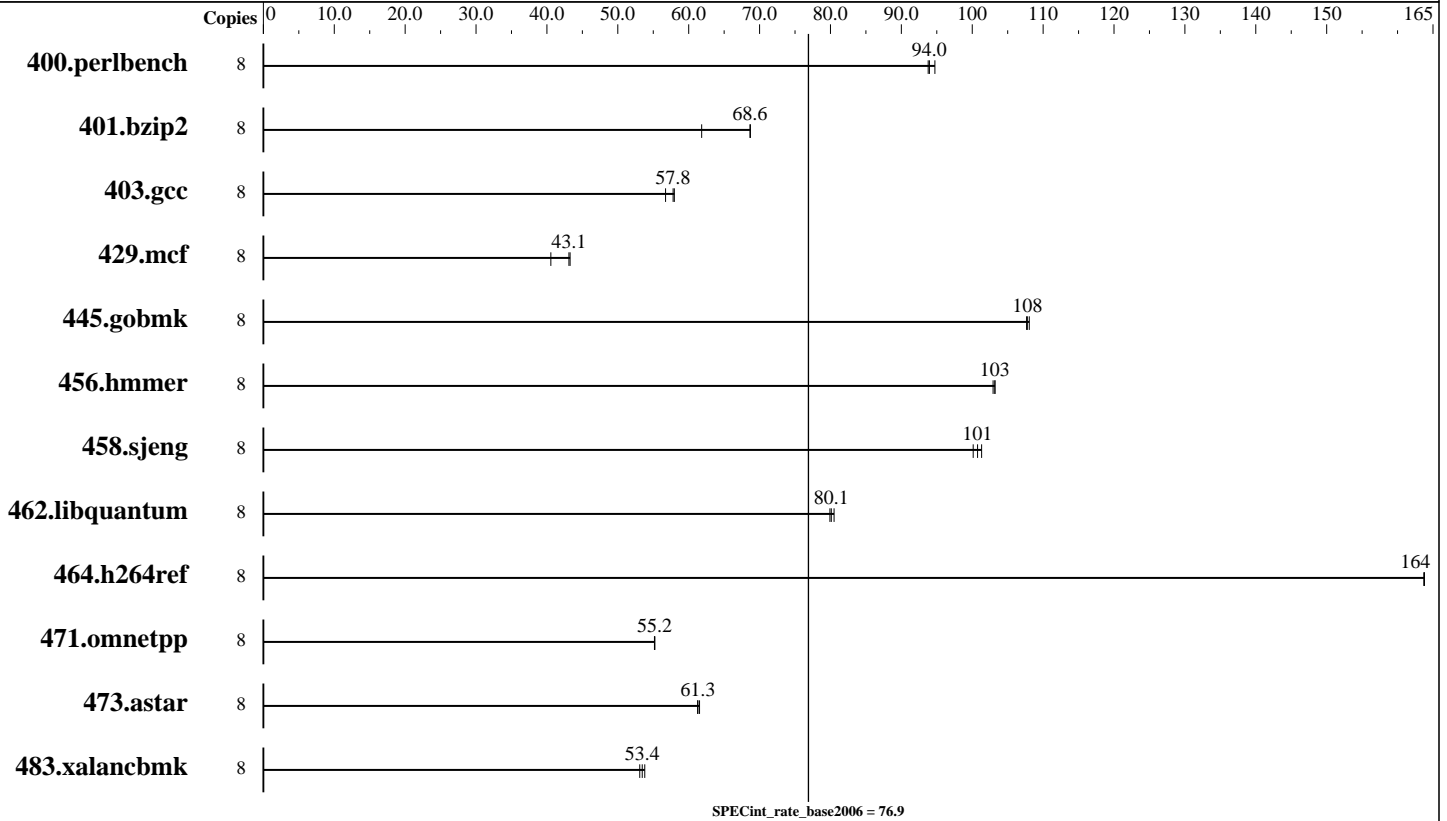
Test sponsor: Sun Microsystems

Tested by: Sun Microsystems

Test date: Feb-2007

Hardware Availability: Oct-2006

Software Availability: Aug-2006



Hardware

CPU Name: AMD Opteron 8220
 CPU Characteristics:
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip
 CPU(s) orderable: 4,8 chips
 Primary Cache: 64 KB I + 64 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (16x1GB, DDR2-667 CL5 ECC Reg)
 Disk Subsystem: SAS, 72 GB, 10K RPM
 Other Hardware: None

Software

Operating System: SuSE SLES9SP3 64-bit kernel
 Compiler: QLogic PathScale Compiler Suite, Release 2.5
 Auto Parallel: No
 File System: ext3
 System State: Multi-user, run level 3
 Base Pointers: 32/64-bit
 Peak Pointers: Not Applicable
 Other Software: --



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	825	94.7	<u>832</u>	<u>94.0</u>	833	93.8							
401.bzip2	8	1249	61.8	1124	68.7	<u>1125</u>	<u>68.6</u>							
403.gcc	8	1111	58.0	<u>1114</u>	<u>57.8</u>	1135	56.7							
429.mcf	8	1799	40.6	<u>1693</u>	<u>43.1</u>	1686	43.3							
445.gobmk	8	777	108	779	108	<u>779</u>	<u>108</u>							
456.hammer	8	<u>723</u>	<u>103</u>	725	103	723	103							
458.sjeng	8	967	100	<u>961</u>	<u>101</u>	955	101							
462.libquantum	8	2059	80.5	<u>2069</u>	<u>80.1</u>	2074	79.9							
464.h264ref	8	1081	164	1081	164	<u>1081</u>	<u>164</u>							
471.omnetpp	8	906	55.2	906	55.2	<u>906</u>	<u>55.2</u>							
473.astar	8	<u>916</u>	<u>61.3</u>	917	61.3	913	61.5							
483.xalancbmk	8	<u>1033</u>	<u>53.4</u>	1026	53.8	1040	53.1							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

taskset utility used to bind CPU(s) to processes

Base Compiler Invocation

C benchmarks:
pathcc

C++ benchmarks:
pathCC

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hammer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-Ofast

C++ benchmarks:

-Ofast -m32

Base Other Flags

C benchmarks:

-IPA:max_jobs=4

C++ benchmarks:

-IPA:max_jobs=4

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.30.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.30.xml

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For other inquiries, please contact webmaster@spec.org.

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