



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®_rate2006 = 61.0

ProLiant BL460c
(1.86 GHz, Intel Xeon processor E5320)

SPECint_rate_base2006 = 59.2

CPU2006 license: 3

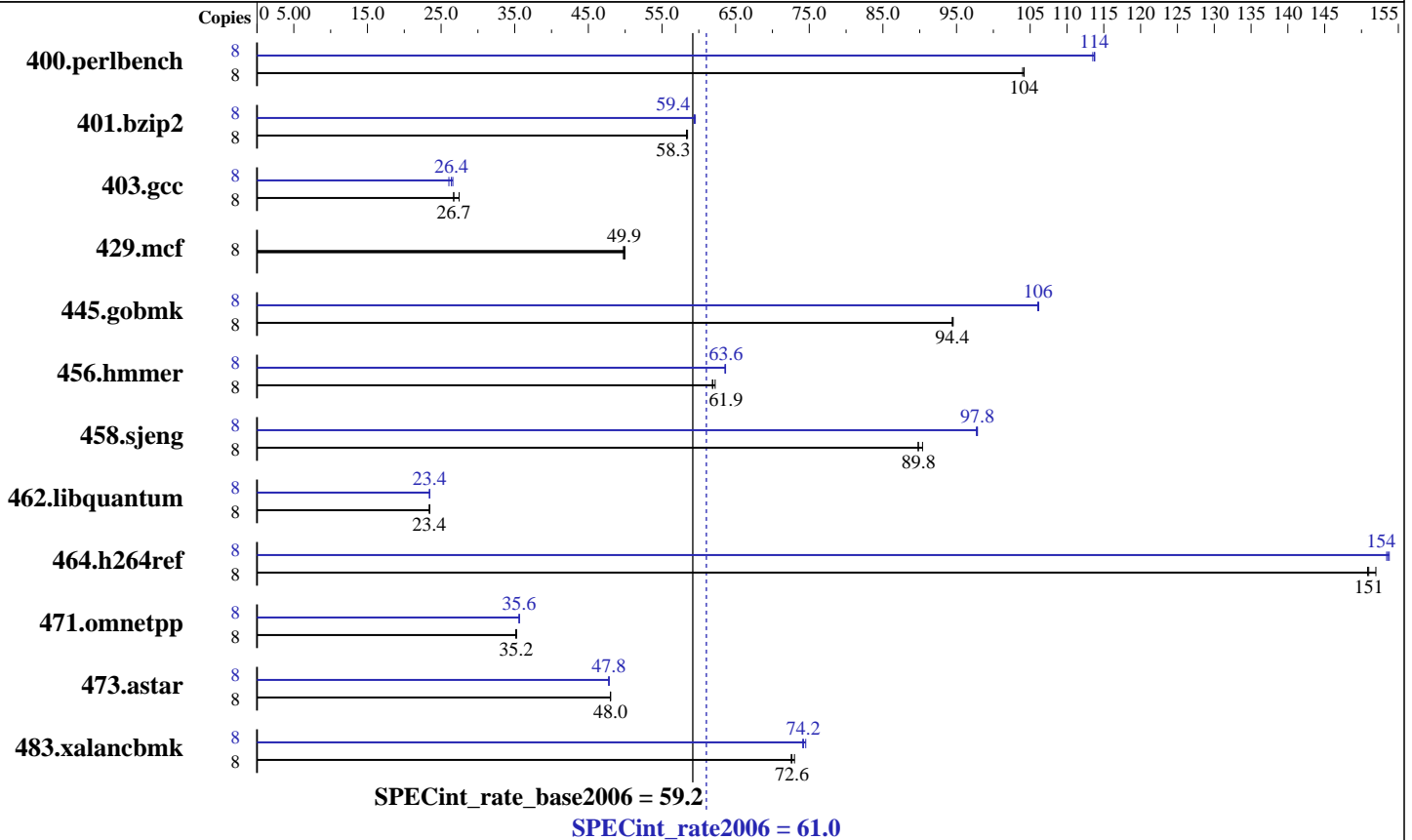
Test date: Feb-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2007

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon E5320
 CPU Characteristics: 1.86 GHz, 2x4 MB L2 shared, 1066 MHz system bus
 CPU MHz: 1860
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB PC2-5300 CL5)
 Disk Subsystem: 1x72 GB 10k SAS
 Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise x64 Edition SP1
 Compiler: Intel C++ Compiler for 32-bit applications, Version 9.1, Build 20061103Z
 Package ID: W_CC_C_9.1.033
 Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: MicroQuill SmartHeap Library 8.0



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Results Table

| Benchmark | Base | | | | | | Peak | | | | | | | |
|----------------|--------|-------------|-------------|-------------|-------------|-------------|-------------|--------|-------------|-------------|-------------|-------------|-------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 8 | 750 | 104 | 750 | 104 | 752 | 104 | 8 | 687 | 114 | 689 | 114 | 687 | 114 |
| 401.bzip2 | 8 | 1324 | 58.3 | 1320 | 58.5 | 1323 | 58.3 | 8 | 1300 | 59.4 | 1297 | 59.5 | 1300 | 59.4 |
| 403.gcc | 8 | 2346 | 27.5 | 2410 | 26.7 | 2411 | 26.7 | 8 | 2440 | 26.4 | 2421 | 26.6 | 2471 | 26.1 |
| 429.mcf | 8 | 1463 | 49.9 | 1466 | 49.8 | 1460 | 50.0 | 8 | 1463 | 49.9 | 1466 | 49.8 | 1460 | 50.0 |
| 445.gobmk | 8 | 889 | 94.4 | 888 | 94.5 | 889 | 94.4 | 8 | 791 | 106 | 791 | 106 | 791 | 106 |
| 456.hammer | 8 | 1200 | 62.2 | 1207 | 61.9 | 1206 | 61.9 | 8 | 1175 | 63.5 | 1173 | 63.6 | 1173 | 63.6 |
| 458.sjeng | 8 | 1071 | 90.4 | 1078 | 89.8 | 1078 | 89.8 | 8 | 989 | 97.8 | 989 | 97.8 | 991 | 97.7 |
| 462.libquantum | 8 | 7083 | 23.4 | 7076 | 23.4 | 7067 | 23.5 | 8 | 7071 | 23.4 | 7074 | 23.4 | 7074 | 23.4 |
| 464.h264ref | 8 | 1165 | 152 | 1174 | 151 | 1173 | 151 | 8 | 1154 | 153 | 1151 | 154 | 1153 | 154 |
| 471.omnetpp | 8 | 1418 | 35.2 | 1423 | 35.1 | 1420 | 35.2 | 8 | 1404 | 35.6 | 1405 | 35.6 | 1403 | 35.6 |
| 473.astar | 8 | 1170 | 48.0 | 1170 | 48.0 | 1170 | 48.0 | 8 | 1176 | 47.8 | 1175 | 47.8 | 1174 | 47.8 |
| 483.xalancbmk | 8 | 756 | 73.0 | 760 | 72.6 | 761 | 72.5 | 8 | 741 | 74.5 | 745 | 74.1 | 744 | 74.2 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch disabled in BIOS.

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Continued on next page



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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE
```

```
401.bzip2: Same as 400.perlbench
```

```
403.gcc: Same as 400.perlbench
```

```
429.mcf: basepeak = yes
```

```
445.gobmk: Same as 400.perlbench
```

```
456.hmmmer: Same as 400.perlbench
```

```
458.sjeng: Same as 400.perlbench
```

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Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.xml>

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For other inquiries, please contact webmaster@spec.org.

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