



SPEC® CFP2006 Result

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Itautec

SPECfp®_rate2006 = 44.6

Servidor Itautec LX211 (Intel Xeon E5335)

SPECfp_rate_base2006 = 44.6

CPU2006 license: 9001

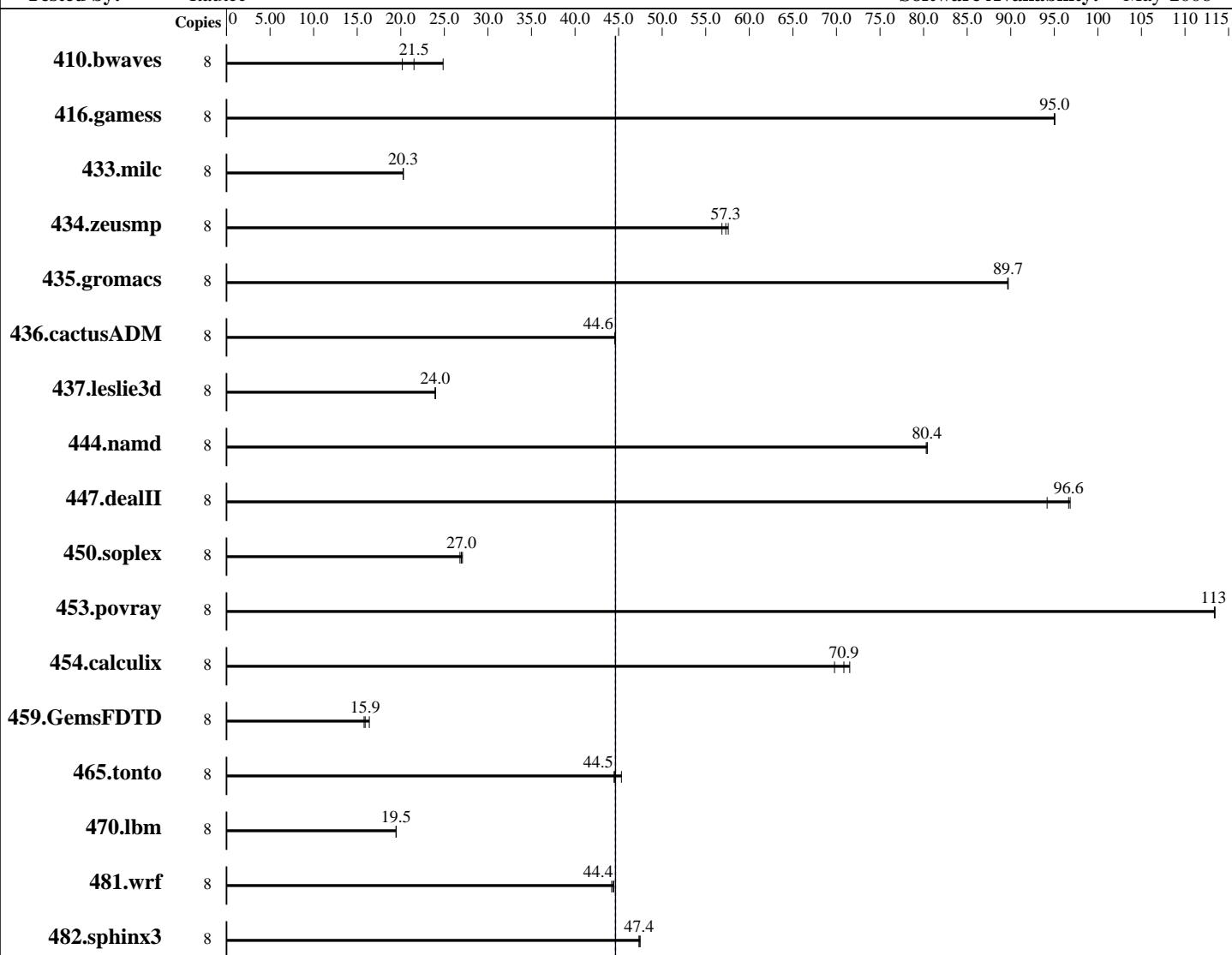
Test date: Feb-2007

Test sponsor: Itautec

Hardware Availability: Feb-2007

Tested by: Itautec

Software Availability: May-2006



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Hardware

CPU Name: Intel Xeon E5335
CPU Characteristics: 1333MHz system bus
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1-2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler: Intel C++ Compiler for IA32 version 9.1
Package ID W_CC_C_9.1.025 Build no 20060519Z
Intel Fortran Compiler for IA32 version 9.1
Package ID W_FC_C_9.1.025 Build no 20060519Z
Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)
Auto Parallel: No
File System: NTFS

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L3 Cache: None
 Other Cache: None
 Memory: 6 GB (6x1GB DDR2-RAM PC2-5300F CAS 5-5-5)
 Disk Subsystem: 120 GB SATA, 7200RPM
 Other Hardware: None

System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: Microquill SmartHeap Library v.8.0 for SMP

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---------------|--------|-------------|-------------|-------------|-------------|-------------|-------------|--------|-------------|-------------|-------------|-------------|-------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 8 | 5052 | 21.5 | 5388 | 20.2 | 4373 | 24.9 | 8 | 5052 | 21.5 | 5388 | 20.2 | 4373 | 24.9 |
| 416.gamess | 8 | 1649 | 95.0 | 1648 | 95.1 | 1648 | 95.0 | 8 | 1649 | 95.0 | 1648 | 95.1 | 1648 | 95.0 |
| 433.milc | 8 | 3620 | 20.3 | 3618 | 20.3 | 3624 | 20.3 | 8 | 3620 | 20.3 | 3618 | 20.3 | 3624 | 20.3 |
| 434.zeusmp | 8 | 1264 | 57.6 | 1280 | 56.9 | 1271 | 57.3 | 8 | 1264 | 57.6 | 1280 | 56.9 | 1271 | 57.3 |
| 435.gromacs | 8 | 637 | 89.6 | 637 | 89.7 | 637 | 89.7 | 8 | 637 | 89.6 | 637 | 89.7 | 637 | 89.7 |
| 436.cactusADM | 8 | 2146 | 44.6 | 2146 | 44.6 | 2143 | 44.6 | 8 | 2146 | 44.6 | 2146 | 44.6 | 2143 | 44.6 |
| 437.leslie3d | 8 | 3138 | 24.0 | 3140 | 24.0 | 3136 | 24.0 | 8 | 3138 | 24.0 | 3140 | 24.0 | 3136 | 24.0 |
| 444.namd | 8 | 799 | 80.3 | 798 | 80.4 | 798 | 80.4 | 8 | 799 | 80.3 | 798 | 80.4 | 798 | 80.4 |
| 447.dealII | 8 | 945 | 96.8 | 972 | 94.2 | 947 | 96.6 | 8 | 945 | 96.8 | 972 | 94.2 | 947 | 96.6 |
| 450.soplex | 8 | 2490 | 26.8 | 2464 | 27.1 | 2475 | 27.0 | 8 | 2490 | 26.8 | 2464 | 27.1 | 2475 | 27.0 |
| 453.povray | 8 | 375 | 113 | 375 | 113 | 375 | 113 | 8 | 375 | 113 | 375 | 113 | 375 | 113 |
| 454.calculix | 8 | 923 | 71.5 | 932 | 70.9 | 946 | 69.8 | 8 | 923 | 71.5 | 932 | 70.9 | 946 | 69.8 |
| 459.GemsFDTD | 8 | 5334 | 15.9 | 5376 | 15.8 | 5179 | 16.4 | 8 | 5334 | 15.9 | 5376 | 15.8 | 5179 | 16.4 |
| 465.tonto | 8 | 1768 | 44.5 | 1770 | 44.5 | 1737 | 45.3 | 8 | 1768 | 44.5 | 1770 | 44.5 | 1737 | 45.3 |
| 470.lbm | 8 | 5647 | 19.5 | 5647 | 19.5 | 5645 | 19.5 | 8 | 5647 | 19.5 | 5647 | 19.5 | 5645 | 19.5 |
| 481.wrf | 8 | 2022 | 44.2 | 2014 | 44.4 | 2011 | 44.4 | 8 | 2022 | 44.2 | 2014 | 44.4 | 2011 | 44.4 |
| 482.sphinx3 | 8 | 3292 | 47.4 | 3293 | 47.3 | 3284 | 47.5 | 8 | 3292 | 47.4 | 3293 | 47.3 | 3284 | 47.5 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

 icl -Qvc7.1 -Qc99

C++ benchmarks:

 icl -Qvc7.1

Fortran benchmarks:

 ifort

Benchmarks using both Fortran and C:

 icl -Qvc7.1 -Qc99 ifort



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Base Portability Flags

```
436.cactusADM: -Qlowercase /assume:underscore  
444.namd: -TP  
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG  
             -DBOOST_NO_INTRINSIC_WCHAR_T  
453.povray: -DSPEC_CPU_WINDOWS_ICL  
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase  
481.wrf: -DSPEC_CPU_WINDOWS_ICL
```

Base Optimization Flags

C benchmarks:

```
-fast /F950000000 sh1SMPMt.lib           -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-fast -Qcxx_features /F950000000 sh1SMPMt.lib  
      -link /FORCE:MULTIPLE
```

Fortran benchmarks:

```
-fast /F950000000           -link /FORCE:MULTIPLE
```

Benchmarks using both Fortran and C:

```
-fast /F950000000           -link /FORCE:MULTIPLE
```

Peak Optimization Flags

C benchmarks:

```
433.milc: basepeak = yes  
470.lbm: basepeak = yes  
482.sphinx3: basepeak = yes
```

C++ benchmarks:

```
444.namd: basepeak = yes  
447.dealII: basepeak = yes  
450.soplex: basepeak = yes  
453.povray: basepeak = yes
```

Fortran benchmarks:

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Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes
416.gamess: basepeak = yes
434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: basepeak = yes
465.tonto: basepeak = yes

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes
436.cactusADM: basepeak = yes
454.calculix: basepeak = yes
481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.xml>

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For other inquiries, please contact webmaster@spec.org.

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