SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

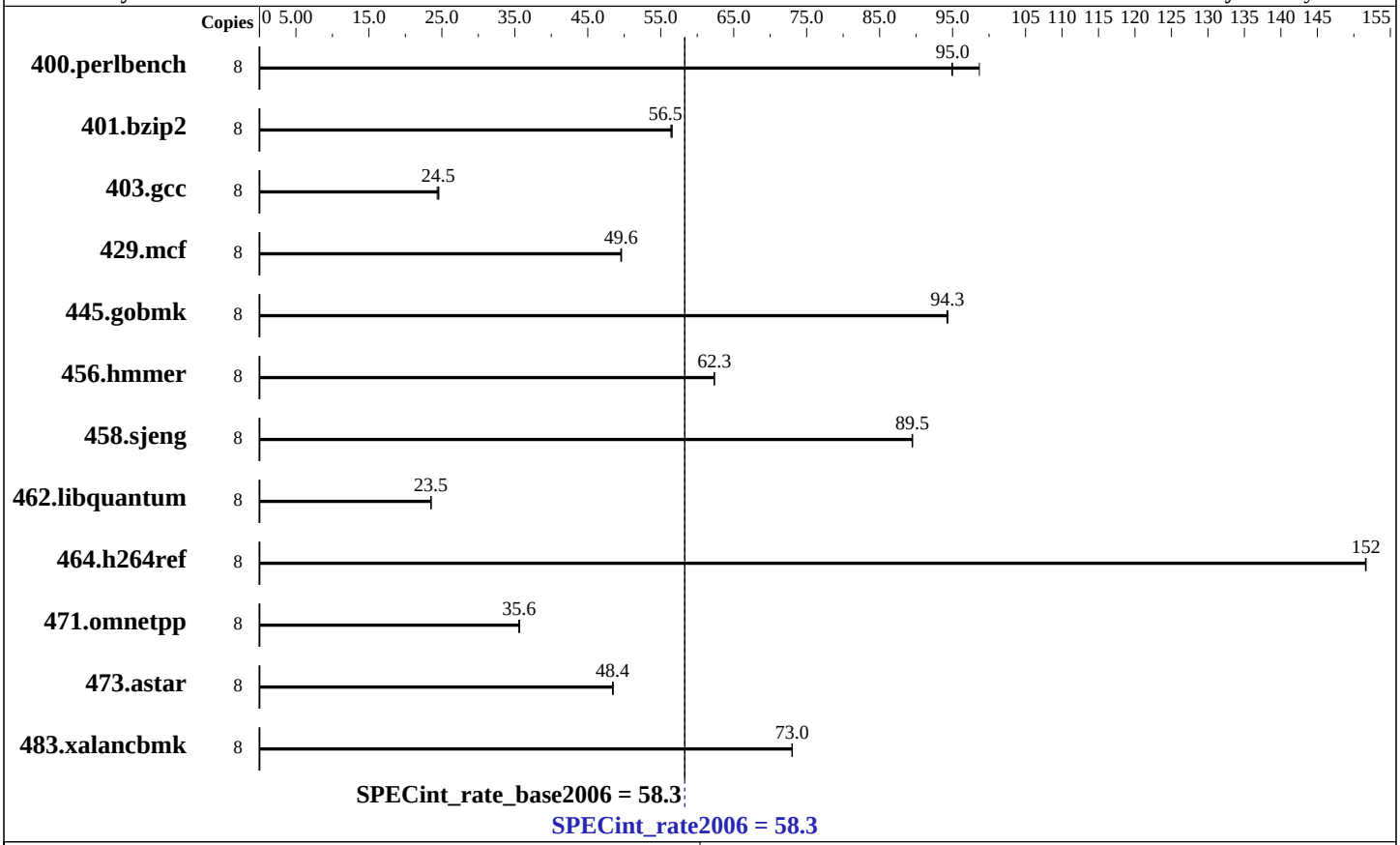
Servidor Itautec MX201 (Intel Xeon E5320)

SPECint®_rate2006 = 58.3

SPECint_rate_base2006 = 58.3

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Feb-2007
Hardware Availability: Feb-2007
Software Availability: May-2006



Hardware

CPU Name:	Intel Xeon E5320
CPU Characteristics:	1066MHz system bus
CPU MHz:	1860
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1-2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8x1GB DDR2-RAM PC2-5300F CAS 5-5-5)
Disk Subsystem:	73 GB SCSI, 10000RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Package ID W_CC_C_9.1.025 Build no 20060519Z Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	Microquill SmartHeap Library v.8.0 for SMP

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

Servidor Itautec MX201 (Intel Xeon E5320)

SPECint_rate2006 = 58.3

SPECint_rate_base2006 = 58.3

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Feb-2007
Hardware Availability: Feb-2007
Software Availability: May-2006

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	792	98.7	**823**	**95.0**	824	94.9	8	792	98.7	**823**	**95.0**	824	94.9
401.bzip2	8	**1367**	**56.5**	1365	56.6	1369	56.4	8	**1367**	**56.5**	1365	56.6	1369	56.4
403.gcc	8	2644	24.4	2619	24.6	**2628**	**24.5**	8	2644	24.4	2619	24.6	**2628**	**24.5**
429.mcf	8	1470	49.6	1473	49.5	**1472**	**49.6**	8	1470	49.6	1473	49.5	**1472**	**49.6**
445.gobmk	8	890	94.3	**890**	**94.3**	891	94.2	8	890	94.3	**890**	**94.3**	891	94.2
456.hmmer	8	1197	62.4	1198	62.3	**1197**	**62.3**	8	1197	62.4	1198	62.3	**1197**	**62.3**
458.sjeng	8	1081	89.5	1082	89.5	**1082**	**89.5**	8	1081	89.5	1082	89.5	**1082**	**89.5**
462.libquantum	8	7049	23.5	**7051**	**23.5**	7054	23.5	8	7049	23.5	**7051**	**23.5**	7054	23.5
464.h264ref	8	1168	152	**1167**	**152**	1167	152	8	1168	152	**1167**	**152**	1167	152
471.omnetpp	8	1406	35.6	1402	35.7	**1405**	**35.6**	8	1406	35.6	1402	35.7	**1405**	**35.6**
473.astar	8	**1160**	**48.4**	1158	48.5	1160	48.4	8	**1160**	**48.4**	1158	48.5	1160	48.4
483.xalancbmk	8	756	73.0	756	73.0	**756**	**73.0**	8	756	73.0	756	73.0	**756**	**73.0**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
This result was measured on the Servidor Itautec MX221.
The Servidor Itautec MX221 and the Servidor Itautec MX201
are electronically equivalent.
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlSMPMt.lib            -link /FORCE:MULTIPLE
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
-fast -Qcxx_features /F512000000 shlSMPMt.lib
-link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Optimization Flags

C benchmarks:

400.perlbench: basepeak = yes

401.bzip2: basepeak = yes

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: basepeak = yes

456.hmmer: basepeak = yes

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.xml

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

Servidor Itautec MX201 (Intel Xeon E5320)

SPECint_rate2006 = 58.3

SPECint_rate_base2006 = 58.3

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Feb-2007
Hardware Availability: Feb-2007
Software Availability: May-2006

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:30:30 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 7 March 2007.