



SPEC® CINT2006 Result

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IBM Corporation

SPECint®2006 = **Not Run**

IBM BladeCenter HS21 XM (Intel Xeon 5160)

SPECint_base2006 = **16.9**

CPU2006 license: 11

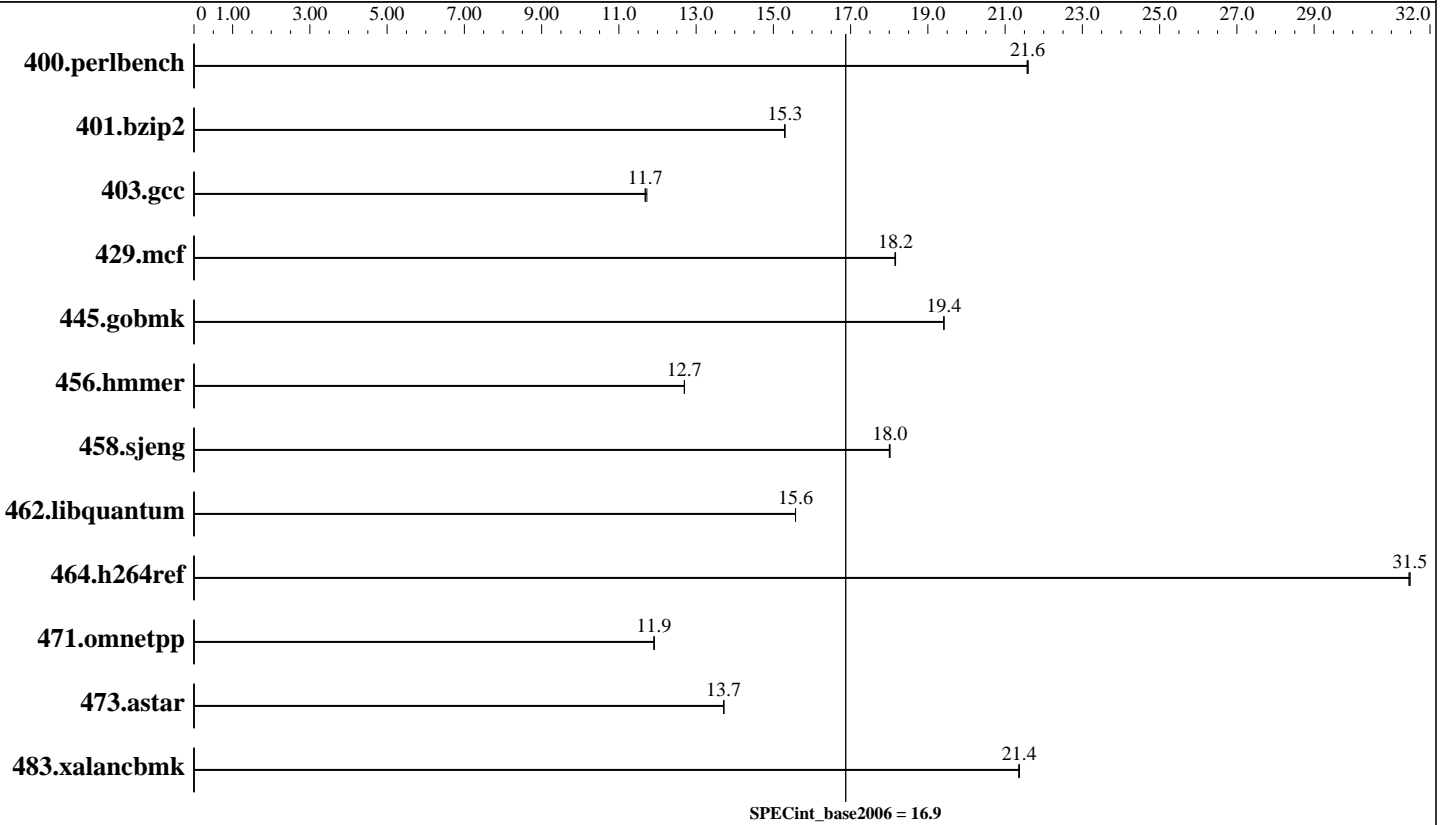
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jan-2007

Hardware Availability: Feb-2007

Software Availability: Aug-2006



Hardware

CPU Name: Intel Xeon 5160
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2GB DDR2-5300F ECC)
 Disk Subsystem: 1 x 74 GB SAS, 1000 RPM
 Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Build no 20060816
 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Smart Heap Library, Version 8



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Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|----------------|------------|-------------|-------------|-------------|------------|-------------|---------|-------|---------|-------|---------|-------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 453 | 21.6 | 452 | 21.6 | 453 | 21.6 | | | | | | |
| 401.bzip2 | 631 | 15.3 | 631 | 15.3 | 631 | 15.3 | | | | | | |
| 403.gcc | 687 | 11.7 | 689 | 11.7 | 689 | 11.7 | | | | | | |
| 429.mcf | 502 | 18.2 | 502 | 18.2 | 503 | 18.1 | | | | | | |
| 445.gobmk | 540 | 19.4 | 540 | 19.4 | 540 | 19.4 | | | | | | |
| 456.hammer | 735 | 12.7 | 735 | 12.7 | 735 | 12.7 | | | | | | |
| 458.sjeng | 672 | 18.0 | 672 | 18.0 | 672 | 18.0 | | | | | | |
| 462.libquantum | 1330 | 15.6 | 1330 | 15.6 | 1330 | 15.6 | | | | | | |
| 464.h264ref | 703 | 31.5 | 704 | 31.5 | 703 | 31.5 | | | | | | |
| 471.omnetpp | 525 | 11.9 | 525 | 11.9 | 525 | 11.9 | | | | | | |
| 473.astar | 512 | 13.7 | 512 | 13.7 | 512 | 13.7 | | | | | | |
| 483.xalancbmk | 323 | 21.4 | 323 | 21.4 | 323 | 21.4 | | | | | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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