



SPEC® CINT2006 Result

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IBM Corporation

SPECint®2006 = Not Run

IBM BladeCenter HS21 XM (Intel Xeon E5345)

SPECint_base2006 = 13.8

CPU2006 license: 11

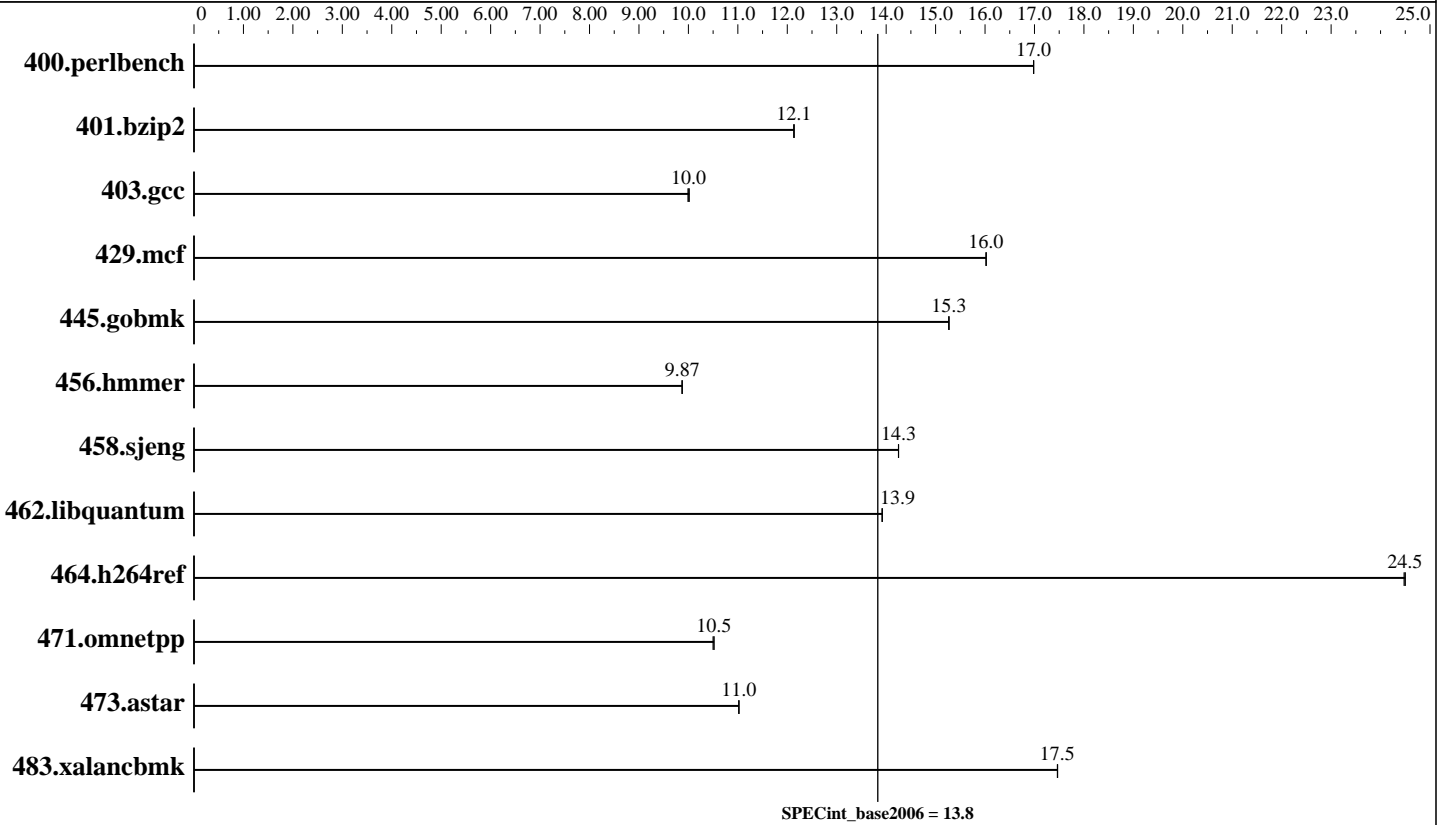
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jan-2007

Hardware Availability: Feb-2007

Software Availability: Aug-2006



Hardware

CPU Name: Intel Xeon E5345
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 2333
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2GB DDR2-5300F ECC)
 Disk Subsystem: 1 x 74 GB SAS, 1000 RPM
 Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
 Compiler: Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Smart Heap Library, Version 8



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	575	17.0	<u>575</u>	<u>17.0</u>	575	17.0						
401.bzip2	795	12.1	<u>795</u>	<u>12.1</u>	796	12.1						
403.gcc	806	9.99	804	10.0	<u>805</u>	<u>10.0</u>						
429.mcf	<u>569</u>	<u>16.0</u>	569	16.0	569	16.0						
445.gobmk	687	15.3	687	15.3	<u>687</u>	<u>15.3</u>						
456.hammer	945	9.87	945	9.87	<u>945</u>	<u>9.87</u>						
458.sjeng	849	14.2	849	14.3	<u>849</u>	<u>14.3</u>						
462.libquantum	<u>1489</u>	<u>13.9</u>	1489	13.9	1488	13.9						
464.h264ref	903	24.5	<u>904</u>	<u>24.5</u>	904	24.5						
471.omnetpp	595	10.5	<u>594</u>	<u>10.5</u>	594	10.5						
473.astar	<u>637</u>	<u>11.0</u>	637	11.0	637	11.0						
483.xalancbmk	<u>395</u>	<u>17.5</u>	395	17.5	395	17.5						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml>

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For other inquiries, please contact webmaster@spec.org.

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