



SPEC® CFP2006 Result

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Hewlett-Packard Company

SPECfp®_rate2006 = 34.1

ProLiant DL380 G5
(2.66 GHz, Intel Xeon processor X5355)

SPECfp_rate_base2006 = 33.6

CPU2006 license: 3

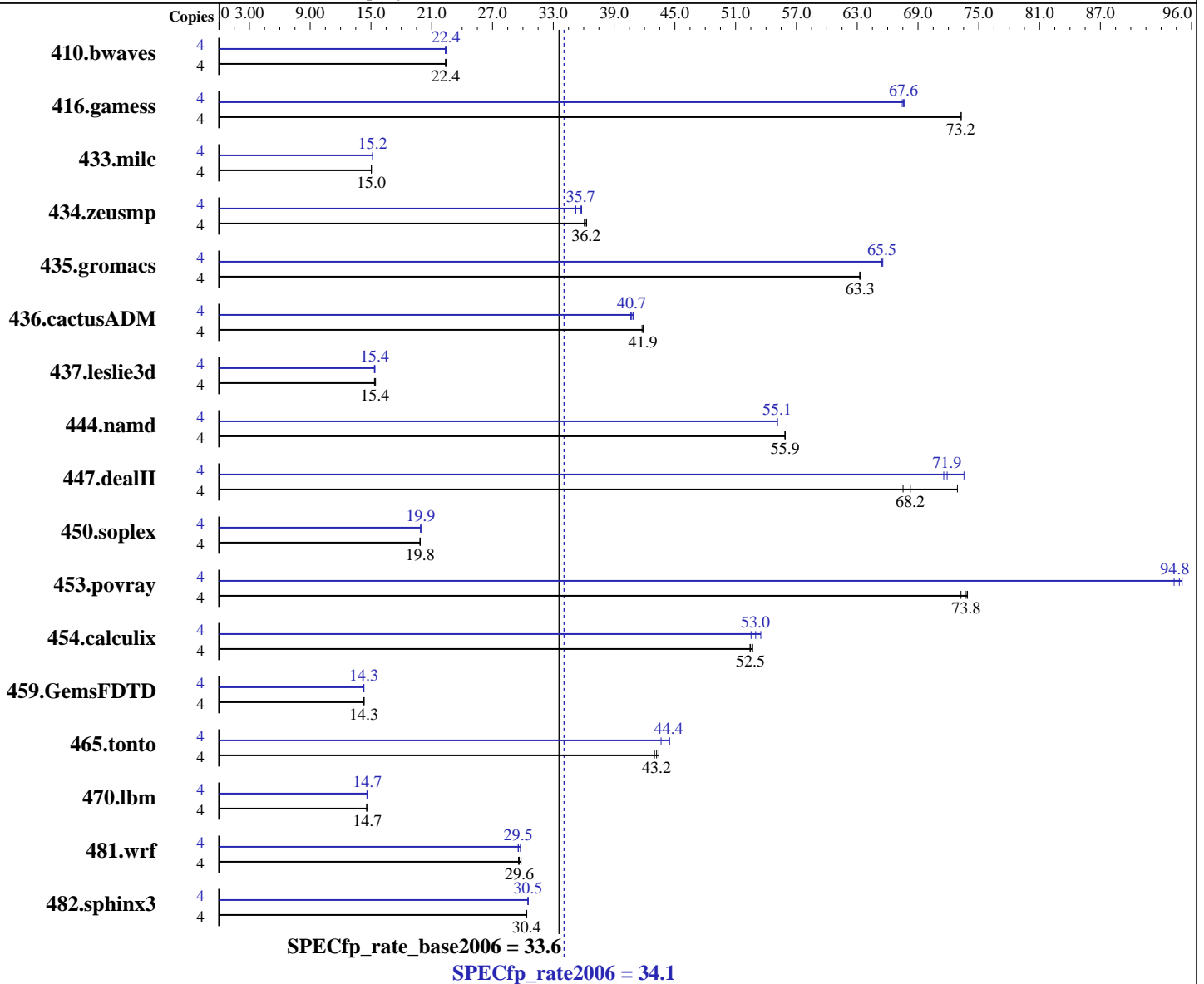
Test date: Jan-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2007

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon X5355
 CPU Characteristics: 2.66 GHz, 2x4 MB L2 shared, 1333 MHz bus
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

Continued on next page

Software

Operating System: SuSE Linux Enterprise Server 10 EM64T kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ Compiler for Intel EM64T-based applications, Version 9.1
 Package ID l_cc_c_9.1.045 Build no 20061101
 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1
 Package ID l_fc_c_9.1.040 Build no 20061101
 Auto Parallel: No

Continued on next page



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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300 CL5)
Disk Subsystem: 4x36 GB 10 K SAS
Other Hardware: None

File System: ext2
System State: Multi-user run level 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	4	2426	22.4	2431	22.4	2428	22.4	4	2433	22.3	2428	22.4	2427	22.4		
416.gamess	4	1069	73.3	1071	73.2	1070	73.2	4	1161	67.4	1158	67.6	1159	67.6		
433.milc	4	2443	15.0	2441	15.0	2442	15.0	4	2421	15.2	2419	15.2	2420	15.2		
434.zeusmp	4	1003	36.3	1004	36.2	1009	36.1	4	1017	35.8	1033	35.2	1020	35.7		
435.gromacs	4	451	63.3	451	63.3	452	63.2	4	437	65.4	436	65.5	436	65.5		
436.cactusADM	4	1145	41.8	1141	41.9	1142	41.9	4	1176	40.6	1173	40.7	1169	40.9		
437.leslie3d	4	2433	15.5	2442	15.4	2450	15.3	4	2445	15.4	2452	15.3	2444	15.4		
444.namd	4	574	55.9	574	55.9	574	55.9	4	582	55.1	582	55.1	582	55.1		
447.dealII	4	628	72.9	678	67.5	671	68.2	4	640	71.5	637	71.9	622	73.5		
450.soplex	4	1681	19.8	1679	19.9	1681	19.8	4	1674	19.9	1674	19.9	1676	19.9		
453.povray	4	288	73.9	289	73.8	291	73.2	4	224	95.1	226	94.3	224	94.8		
454.calculix	4	626	52.7	630	52.4	629	52.5	4	628	52.5	623	53.0	617	53.5		
459.GemsFDTD	4	2967	14.3	2962	14.3	2973	14.3	4	2967	14.3	2971	14.3	2967	14.3		
465.tonto	4	911	43.2	906	43.4	916	43.0	4	886	44.4	902	43.6	885	44.5		
470.lbm	4	3750	14.7	3775	14.6	3751	14.7	4	3763	14.6	3751	14.7	3750	14.7		
481.wrf	4	1507	29.6	1511	29.6	1499	29.8	4	1502	29.8	1512	29.5	1512	29.5		
482.sphinx3	4	2569	30.4	2566	30.4	2568	30.4	4	2557	30.5	2555	30.5	2555	30.5		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"ulimit -s unlimited" set

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Continued on next page



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Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-fast

Fortran benchmarks:

-fast

Benchmarks using both Fortran and C:

-fast

Peak Compiler Invocation

C benchmarks:

icc

Continued on next page



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Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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