



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®_rate2006 = 45.4

ProLiant ML350 G5
(2.66 GHz, Intel Xeon processor X5355)

SPECint_rate_base2006 = 44.0

CPU2006 license: 3

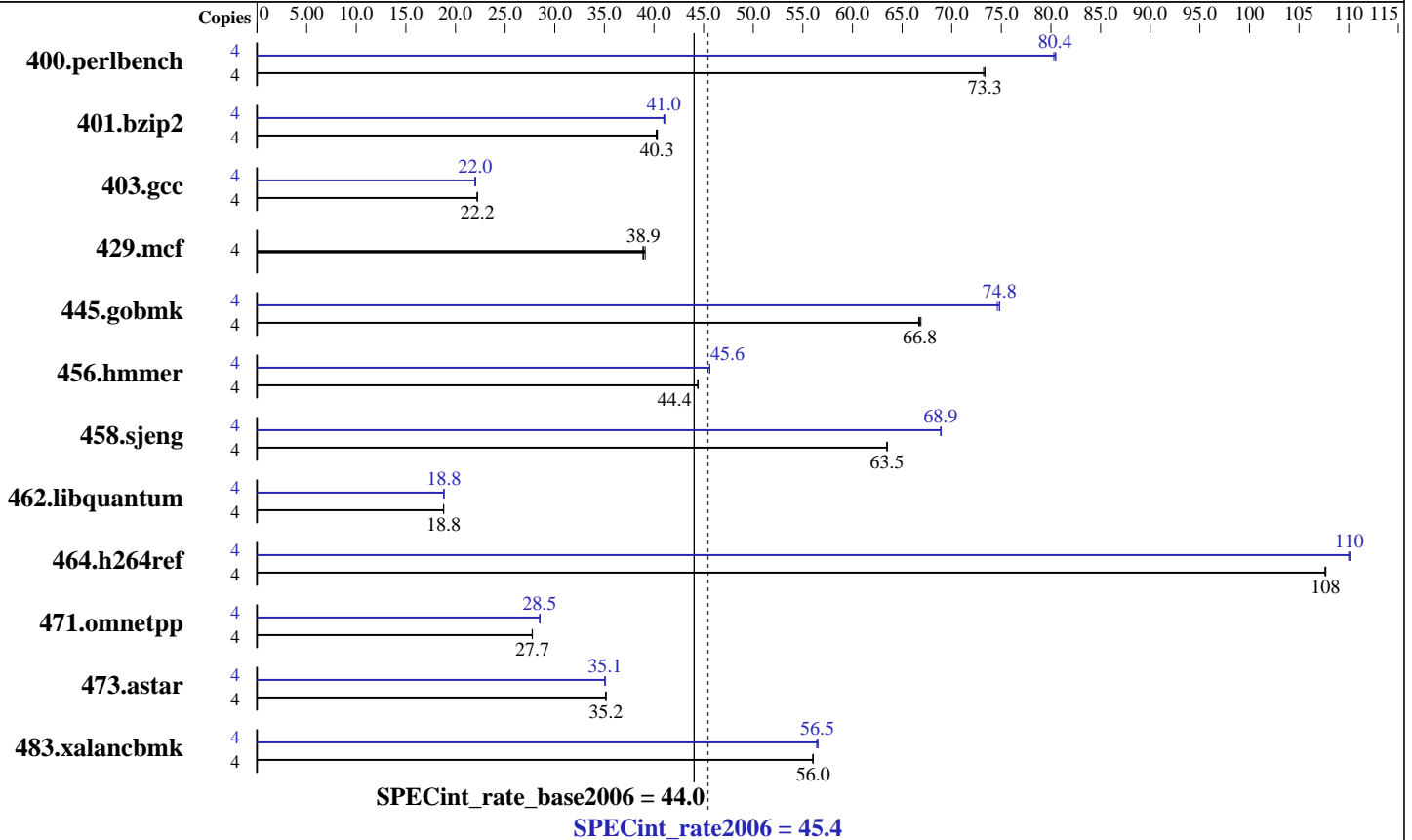
Test date: Feb-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2007

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon X5355
 CPU Characteristics: 2.66 GHz, 2x4 MB L2 shared, 1333 MHz bus
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4x2 GB PC2-5300F CL5)
 Disk Subsystem: 4x36 GB 10 K SAS
 Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise X64 Edition
 Compiler: Intel C++ Compiler 9.1 for 32-bit apps, Build 20060323Z
 Package ID: W_CC_P_9.1.020
 Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: MicroQuill SmartHeap Library 8.0



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	533	73.4	<u>533</u>	<u>73.3</u>	534	73.2	4	<u>486</u>	<u>80.4</u>	486	80.5	487	80.3
401.bzip2	4	960	40.2	<u>957</u>	<u>40.3</u>	957	40.3	4	941	41.0	939	41.1	<u>941</u>	<u>41.0</u>
403.gcc	4	1452	22.2	1449	22.2	<u>1450</u>	<u>22.2</u>	4	<u>1465</u>	<u>22.0</u>	1462	22.0	1466	22.0
429.mcf	4	933	39.1	938	38.9	<u>937</u>	<u>38.9</u>	4	933	39.1	938	38.9	<u>937</u>	<u>38.9</u>
445.gobmk	4	629	66.7	<u>628</u>	<u>66.8</u>	627	66.9	4	561	74.8	<u>561</u>	<u>74.8</u>	563	74.6
456.hammer	4	840	44.4	840	44.4	<u>840</u>	<u>44.4</u>	4	818	45.6	<u>818</u>	<u>45.6</u>	818	45.6
458.sjeng	4	762	63.5	763	63.5	<u>762</u>	<u>63.5</u>	4	<u>702</u>	<u>68.9</u>	702	68.9	703	68.9
462.libquantum	4	4409	18.8	<u>4408</u>	<u>18.8</u>	4407	18.8	4	<u>4401</u>	<u>18.8</u>	4400	18.8	4402	18.8
464.h264ref	4	822	108	<u>822</u>	<u>108</u>	823	108	4	805	110	<u>804</u>	<u>110</u>	804	110
471.omnetpp	4	901	27.7	<u>901</u>	<u>27.7</u>	902	27.7	4	878	28.5	<u>878</u>	<u>28.5</u>	877	28.5
473.astar	4	799	35.2	<u>799</u>	<u>35.2</u>	799	35.1	4	801	35.1	<u>801</u>	<u>35.1</u>	801	35.1
483.xalancbmk	4	<u>492</u>	<u>56.0</u>	492	56.0	493	56.0	4	489	56.4	<u>489</u>	<u>56.5</u>	488	56.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch disabled in BIOS.

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE
```

```
401.bzip2: Same as 400.perlbench
```

```
403.gcc: Same as 400.perlbench
```

```
429.mcf: basepeak = yes
```

```
445.gobmk: Same as 400.perlbench
```

```
456.hmmmer: Same as 400.perlbench
```

```
458.sjeng: Same as 400.perlbench
```

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Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.xml>

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For other inquiries, please contact webmaster@spec.org.

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