



SPEC® CINT2006 Result

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IBM Corporation

SPECint®_rate2006 = Not Run

IBM System x3500 (Intel Xeon E5335)

SPECint_rate_base2006 = 67.2

CPU2006 license: 11

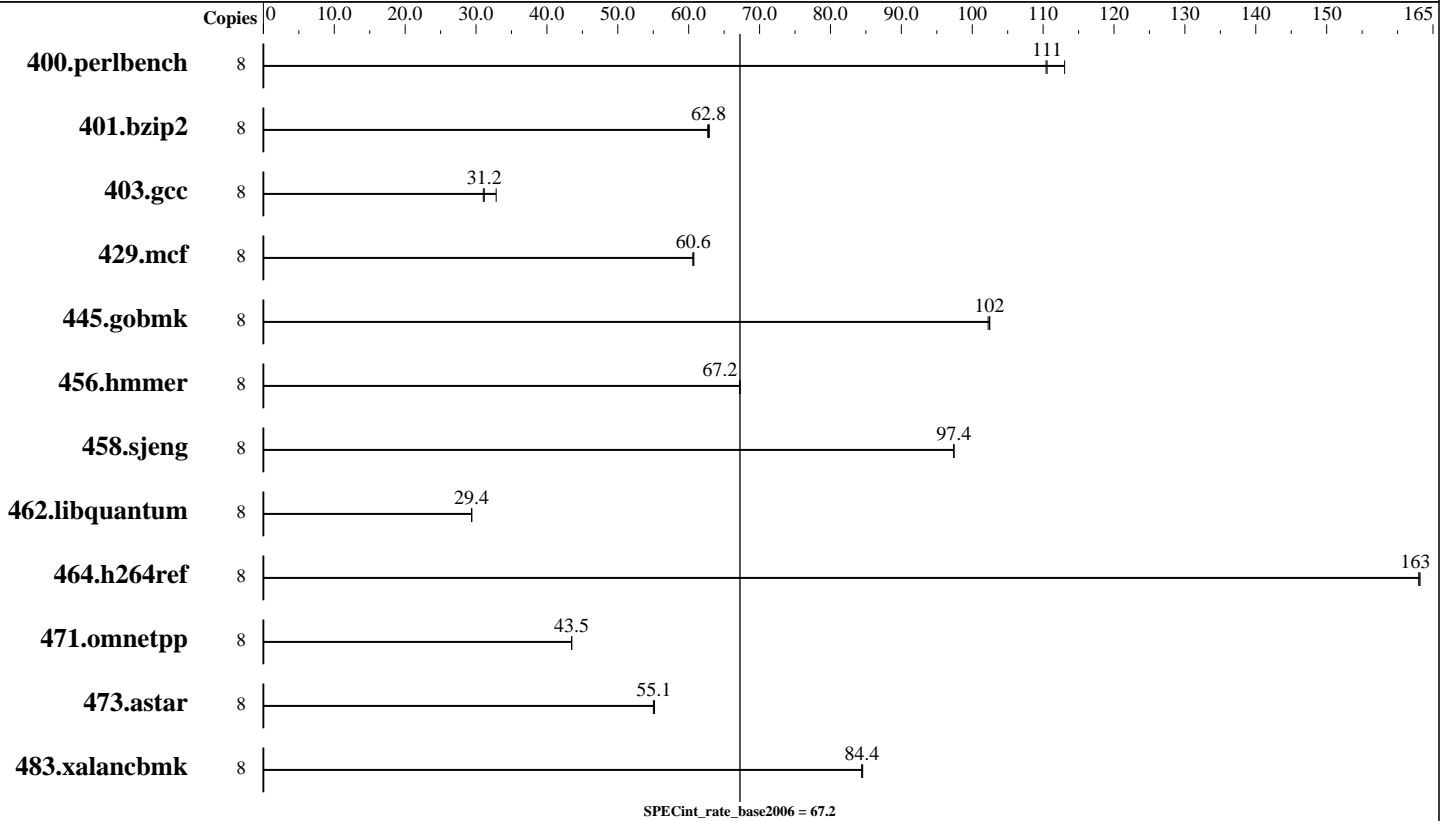
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jan-2007

Hardware Availability: Feb-2007

Software Availability: Aug-2006



Hardware

CPU Name: Intel Xeon E5335
 CPU Characteristics: 1066MHz system bus
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (8 x 1GB DDR2-5300F ECC)
 Disk Subsystem: 1 x 80 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
 Compiler: Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Smart Heap Library, Version 8



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	691	113	708	110	707	111							
401.bzip2	8	1232	62.7	1227	62.9	1229	62.8							
403.gcc	8	1961	32.8	2066	31.2	2076	31.0							
429.mcf	8	1203	60.6	1201	60.7	1204	60.6							
445.gobmk	8	821	102	819	102	819	102							
456.hammer	8	1110	67.2	1110	67.2	1111	67.2							
458.sjeng	8	994	97.4	994	97.4	994	97.4							
462.libquantum	8	5642	29.4	5642	29.4	5642	29.4							
464.h264ref	8	1086	163	1086	163	1085	163							
471.omnetpp	8	1150	43.5	1150	43.5	1150	43.5							
473.astar	8	1018	55.2	1019	55.1	1021	55.0							
483.xalancbmk	8	653	84.5	654	84.4	654	84.4							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

This result was measured on an IBM System X 3400. IBM System X 3400 and IBM System X 3500 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
 icl -Qvc7.1 -Qc99

C++ benchmarks:
 icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
 464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
 -fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:14:07 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 8 February 2007.