



SPEC[®] CINT2006 Result

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IBM Corporation

SPECint[®]_rate2006 = Not Run

IBM System x3800 (Intel Xeon 7150N)

SPECint_rate_base2006 = 66.8

CPU2006 license: 11

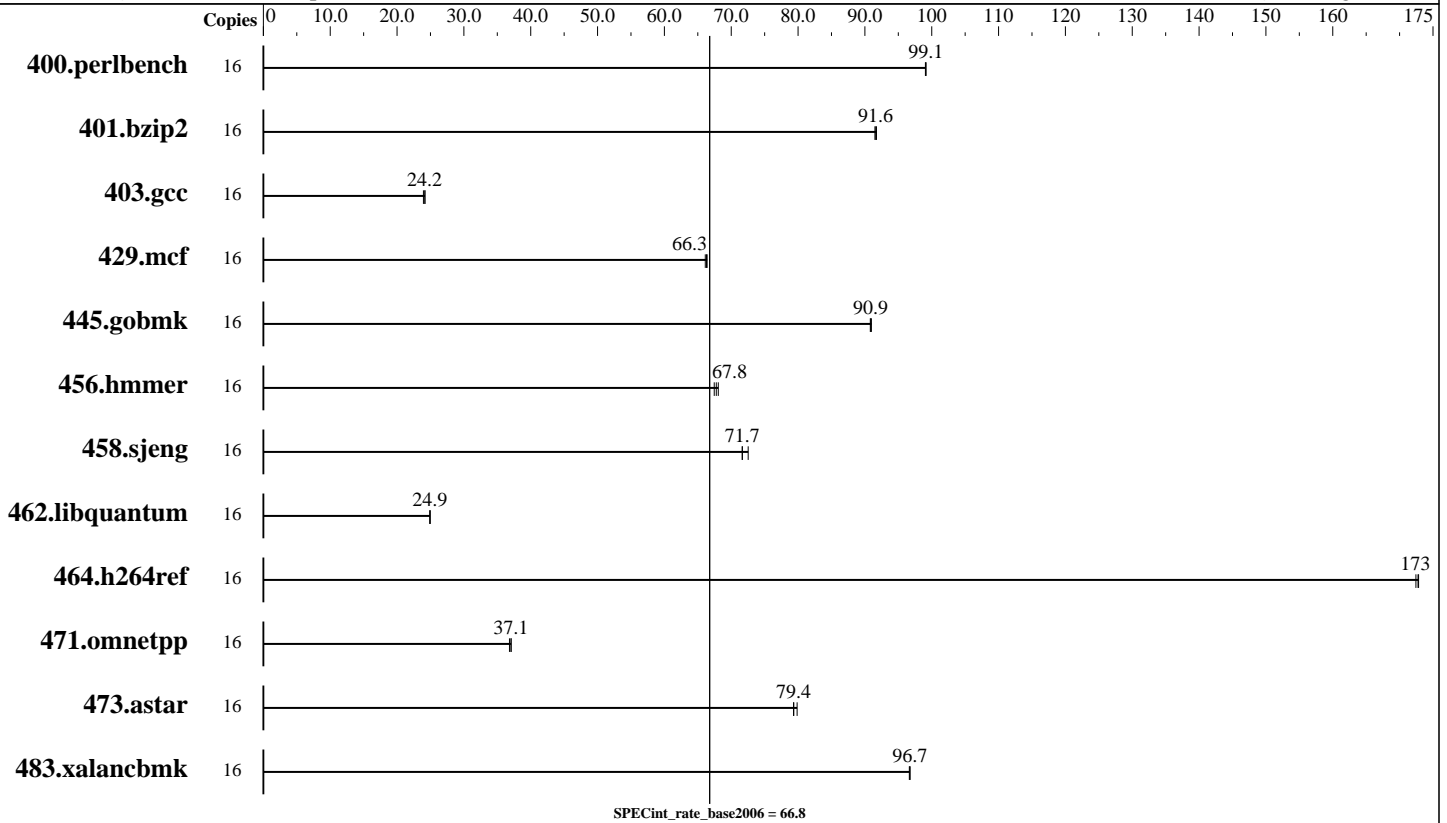
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jan-2007

Hardware Availability: Jan-2007

Software Availability: Aug-2006



Hardware

CPU Name: Intel Xeon 7150N
 CPU Characteristics: 667 MHz bus
 CPU MHz: 3500
 FPU: Integrated
 CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2,4 chips
 Primary Cache: 12 K micro-ops I + 16 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core
 L3 Cache: 16 MB I+D on chip per chip
 Other Cache: None
 Memory: 32 GB (16 x 2048 MB ECC PC2-3200)
 Disk Subsystem: 73 GB SAS, 10k RPM
 Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Build no 20060816
 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Smart Heap Library, Version 8



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	1577	99.1	<u>1577</u>	<u>99.1</u>	1578	99.1							
401.bzip2	16	1683	91.7	1687	91.5	<u>1685</u>	<u>91.6</u>							
403.gcc	16	5379	23.9	5327	24.2	<u>5332</u>	<u>24.2</u>							
429.mcf	16	<u>2201</u>	<u>66.3</u>	2198	66.4	2207	66.1							
445.gobmk	16	<u>1847</u>	<u>90.9</u>	1845	91.0	1848	90.8							
456.hammer	16	<u>2203</u>	<u>67.8</u>	2213	67.5	2193	68.1							
458.sjeng	16	2669	72.5	<u>2701</u>	<u>71.7</u>	2702	71.6							
462.libquantum	16	13301	24.9	<u>13299</u>	<u>24.9</u>	13298	24.9							
464.h264ref	16	2053	172	<u>2050</u>	<u>173</u>	2049	173							
471.omnetpp	16	2716	36.8	<u>2699</u>	<u>37.1</u>	2698	37.1							
473.astar	16	1407	79.9	1416	79.3	<u>1415</u>	<u>79.4</u>							
483.xalancbmk	16	<u>1141</u>	<u>96.7</u>	1142	96.6	1141	96.7							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Bios Settings

Hardware Prefetch enabled

Memory Array set to High-Performance Memory Array

Base Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32

464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:

-fast /F512000000 shlw32m.lib

-link /FORCE:MULTIPLE

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:15:00 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 8 February 2007.