



SPEC® CINT2006 Result

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IBM Corporation

SPECint®2006 = Not Run

IBM System x3200 (Intel Xeon X3210)

SPECint_base2006 = 13.7

CPU2006 license: 11

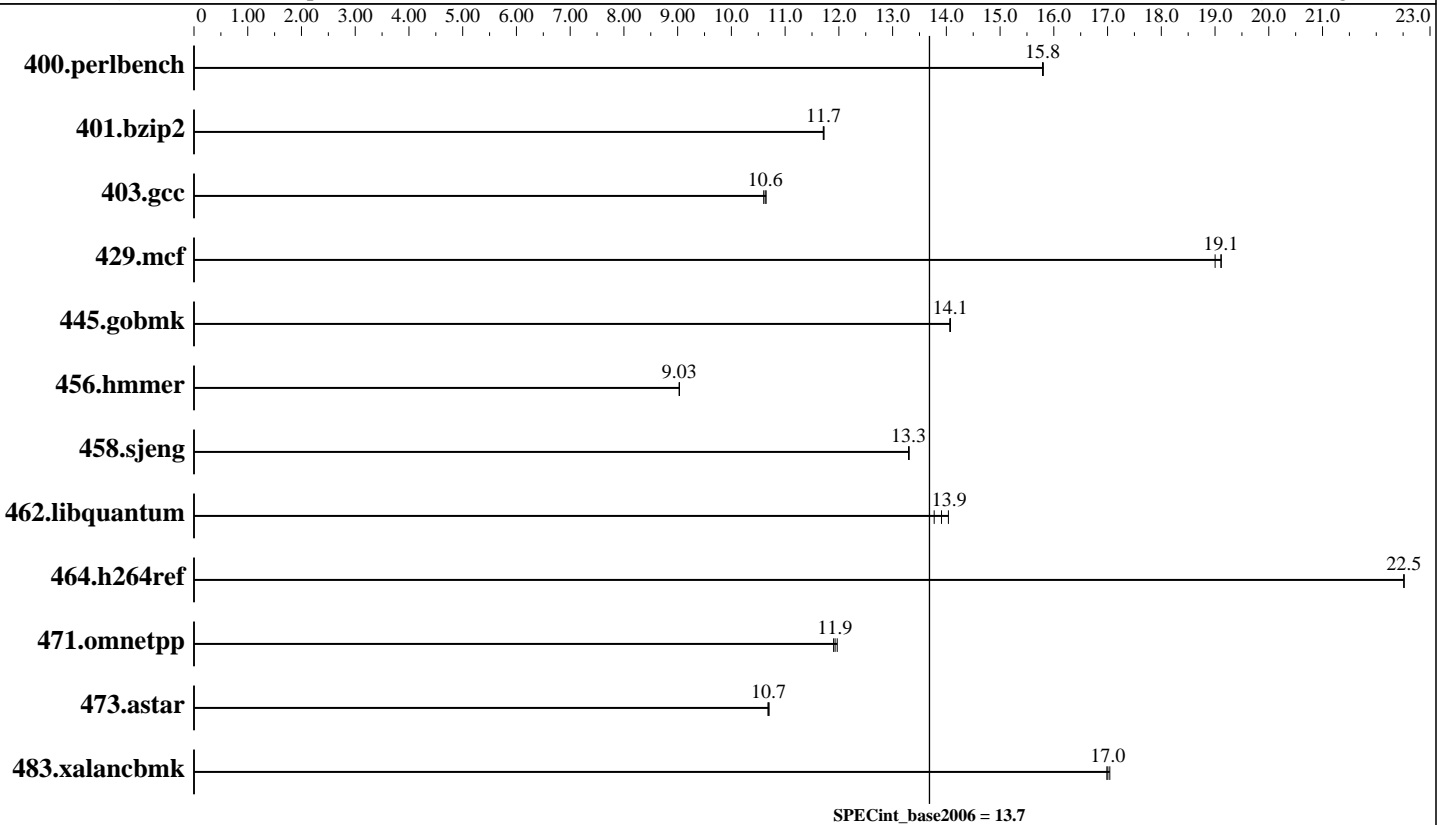
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jan-2007

Hardware Availability: Feb-2007

Software Availability: Aug-2006



Hardware

CPU Name: Intel Xeon X3210
 CPU Characteristics: 1066MHz system bus
 CPU MHz: 2133
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4 x 2GB PC2-5300 ECC)
 Disk Subsystem: 1 x 73 GB SAS, 10000 RPM
 Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
 Compiler: Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Smart Heap Library, Version 8



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	619	15.8	619	15.8	618	15.8						
401.bzip2	824	11.7	823	11.7	824	11.7						
403.gcc	759	10.6	757	10.6	756	10.6						
429.mcf	480	19.0	477	19.1	477	19.1						
445.gobmk	746	14.1	745	14.1	745	14.1						
456.hammer	1033	9.03	1033	9.03	1033	9.03						
458.sjeng	910	13.3	910	13.3	909	13.3						
462.libquantum	1504	13.8	1490	13.9	1476	14.0						
464.h264ref	983	22.5	983	22.5	983	22.5						
471.omnetpp	525	11.9	524	11.9	522	12.0						
473.astar	657	10.7	657	10.7	656	10.7						
483.xalancbmk	406	17.0	406	17.0	405	17.0						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
C++ benchmarks:
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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