



# SPEC<sup>®</sup> CINT2006 Result

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IBM Corporation  
IBM System x3800

SPECint<sup>®</sup>2006 = **Not Run**  
SPECint\_base2006 = **7.54**

CPU2006 license: 11

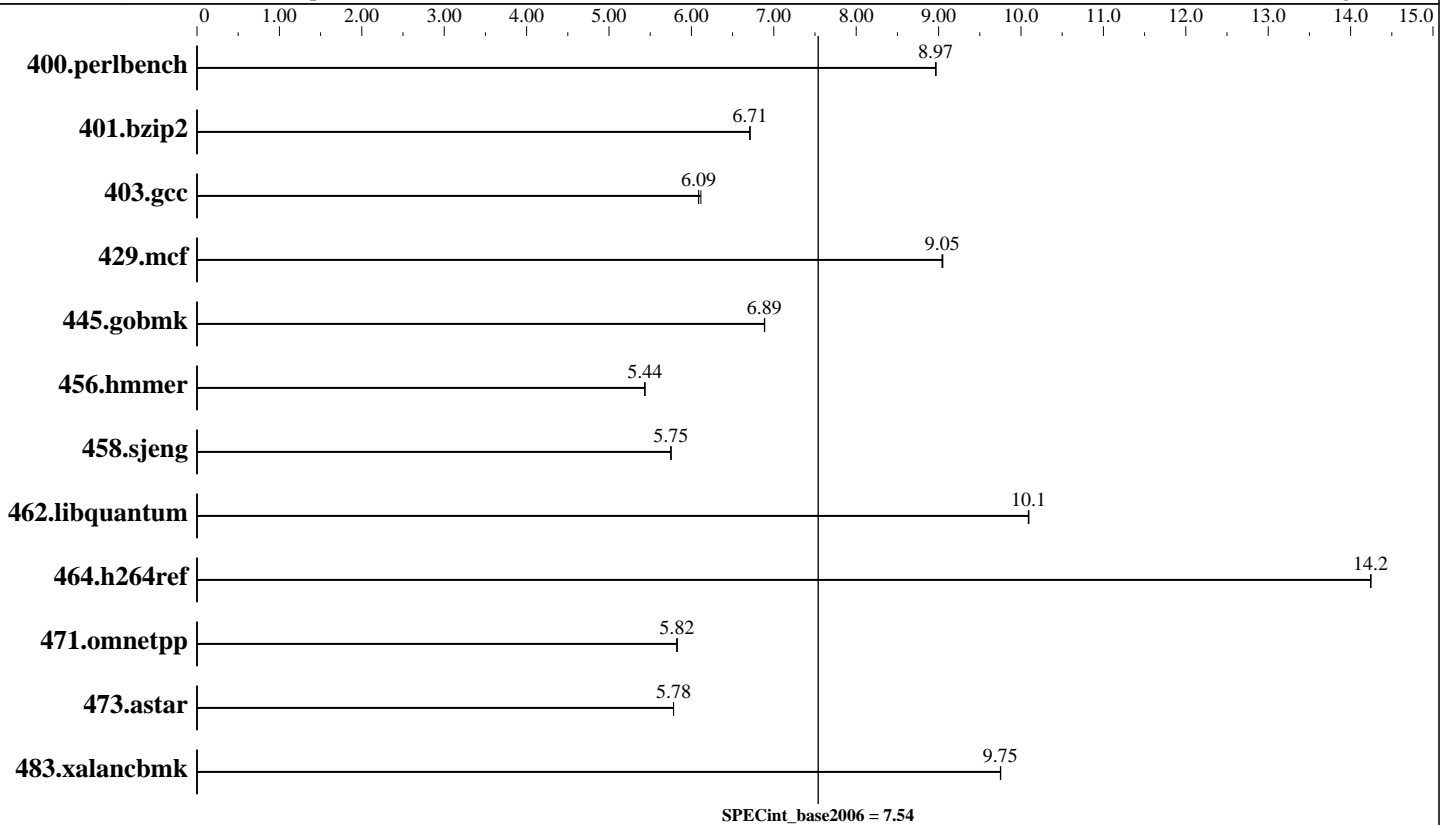
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jan-2007

Hardware Availability: Oct-2006

Software Availability: Aug-2006



## Hardware

CPU Name: Intel Xeon 7110N  
 CPU Characteristics: 667 MHz bus  
 CPU MHz: 2500  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2,4 chips  
 Primary Cache: 12 K micro-ops I + 16 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core  
 L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 32 GB (16 x 2048 MB ECC PC2-3200)  
 Disk Subsystem: 73 GB SAS, 10k RPM  
 Other Hardware: None

## Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)  
 Compiler: Intel C++ Compiler for IA32 version 9.1  
 Build no 20060816  
 Microsoft Visual Studio .Net 2003 (for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: Smart Heap Library, Version 8



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	1089	8.97	<b>1090</b>	<b>8.97</b>	1090	8.96						
401.bzip2	<b>1438</b>	<b>6.71</b>	1438	6.71	1438	6.71						
403.gcc	<b>1322</b>	<b>6.09</b>	1323	6.09	1316	6.12						
429.mcf	1008	9.05	<b>1008</b>	<b>9.05</b>	1009	9.04						
445.gobmk	1522	6.89	<b>1523</b>	<b>6.89</b>	1523	6.89						
456.hammer	1717	5.44	1717	5.44	<b>1717</b>	<b>5.44</b>						
458.sjeng	<b>2104</b>	<b>5.75</b>	2104	5.75	2103	5.75						
462.libquantum	2054	10.1	2053	10.1	<b>2053</b>	<b>10.1</b>						
464.h264ref	1553	14.2	1554	14.2	<b>1554</b>	<b>14.2</b>						
471.omnetpp	<b>1073</b>	<b>5.82</b>	1074	5.82	1072	5.83						
473.astar	<b>1214</b>	<b>5.78</b>	1214	5.78	1214	5.78						
483.xalancbmk	707	9.75	708	9.75	<b>708</b>	<b>9.75</b>						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Bios Settings  
Hardware Prefetch enabled  
Memory Array set to High-Performance Memory Array

## Base Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99

C++ benchmarks:  
icl -Qvc7.1

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Base Optimization Flags

C benchmarks:  
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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## Base Optimization Flags (Continued)

C++ benchmarks:  
-fast -Qcxx\_features /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at  
<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.html>

You can also download the XML flags source by saving the following link:  
<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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