



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

SPECint®_rate2006 = 50.3

CELSIUS R640, Intel Xeon 5160 processor

SPECint_rate_base2006 = 48.8

CPU2006 license: 22

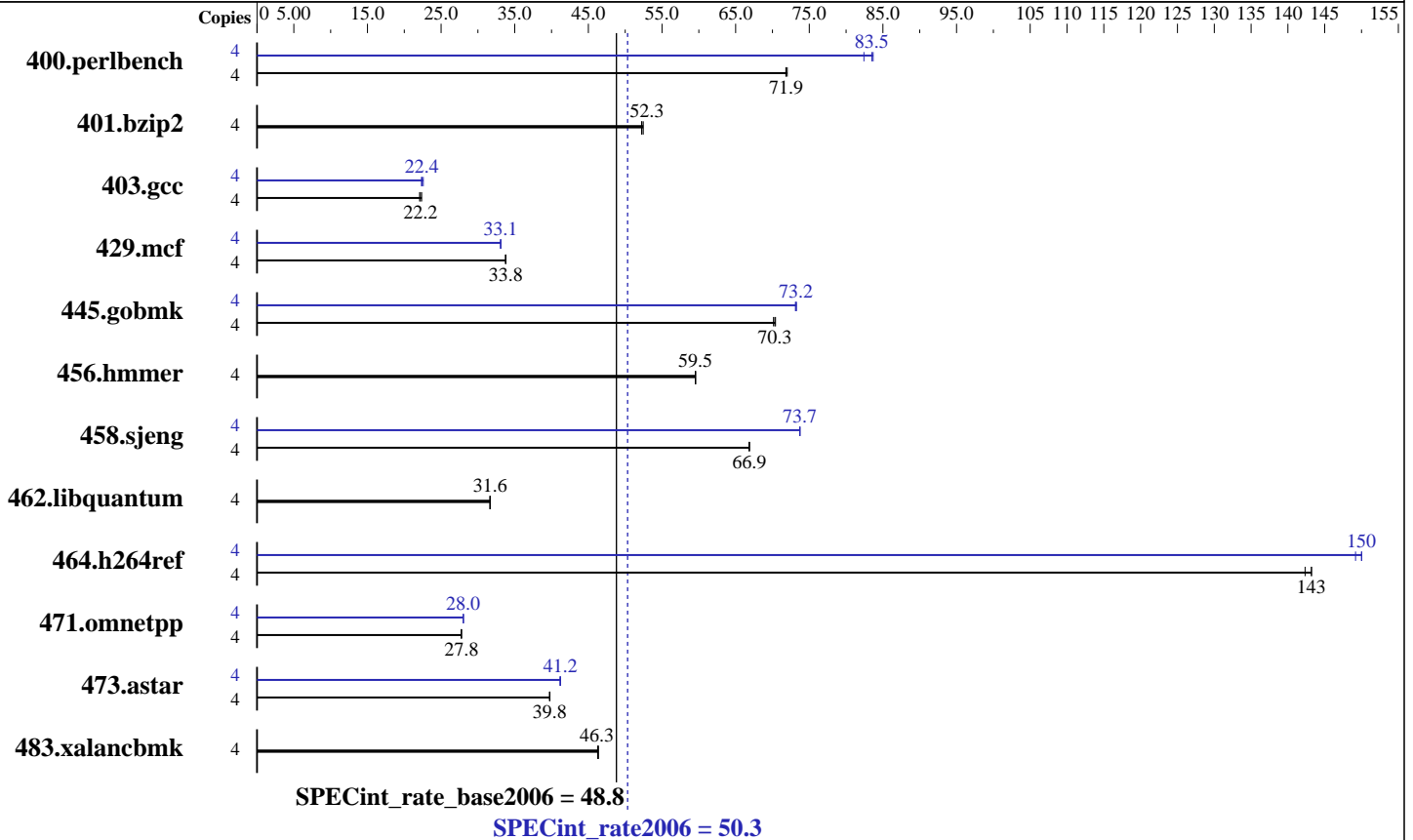
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Nov-2006

Hardware Availability: Sep-2006

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon 5160
 CPU Characteristics: Dual Core, 3.0 GHz
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (8x1 GB DDR2 5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: SATA II 7200 rpm
 Other Hardware: None

Software

Operating System: Windows XP Professional x64 Edition
 Compiler: Intel C++ Compiler for EM64T version 9.1
 - Build 20061104, Package-ID W_CC_C_9.1.033
 Microsoft Visual Studio 2005 (libr. & linker)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other Software: Smart Heap Library, Version 8



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|-------------|-------------|-------------|-------------|------------|-------------|--------|-------------|-------------|-------------|-------------|------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 4 | 544 | 71.8 | 543 | 72.0 | 544 | 71.9 | 4 | 474 | 82.4 | 468 | 83.5 | 467 | 83.7 |
| 401.bzip2 | 4 | 738 | 52.3 | 736 | 52.5 | 739 | 52.2 | 4 | 738 | 52.3 | 736 | 52.5 | 739 | 52.2 |
| 403.gcc | 4 | 1458 | 22.1 | 1450 | 22.2 | 1440 | 22.4 | 4 | 1437 | 22.4 | 1442 | 22.3 | 1426 | 22.6 |
| 429.mcf | 4 | 1080 | 33.8 | 1080 | 33.8 | 1081 | 33.7 | 4 | 1102 | 33.1 | 1102 | 33.1 | 1103 | 33.1 |
| 445.gobmk | 4 | 597 | 70.3 | 598 | 70.1 | 596 | 70.4 | 4 | 573 | 73.2 | 574 | 73.1 | 573 | 73.3 |
| 456.hmmer | 4 | 627 | 59.5 | 627 | 59.5 | 627 | 59.6 | 4 | 627 | 59.5 | 627 | 59.5 | 627 | 59.6 |
| 458.sjeng | 4 | 724 | 66.9 | 724 | 66.8 | 724 | 66.9 | 4 | 656 | 73.7 | 657 | 73.7 | 656 | 73.7 |
| 462.libquantum | 4 | 2619 | 31.6 | 2619 | 31.6 | 2618 | 31.7 | 4 | 2619 | 31.6 | 2619 | 31.6 | 2618 | 31.7 |
| 464.h264ref | 4 | 622 | 142 | 618 | 143 | 618 | 143 | 4 | 593 | 149 | 590 | 150 | 590 | 150 |
| 471.omnetpp | 4 | 900 | 27.8 | 902 | 27.7 | 901 | 27.8 | 4 | 892 | 28.0 | 892 | 28.0 | 893 | 28.0 |
| 473.astar | 4 | 707 | 39.7 | 706 | 39.8 | 706 | 39.8 | 4 | 681 | 41.2 | 682 | 41.2 | 682 | 41.1 |
| 483.xalancbmk | 4 | 597 | 46.3 | 596 | 46.3 | 596 | 46.3 | 4 | 597 | 46.3 | 596 | 46.3 | 596 | 46.3 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

For information about Fujitsu Siemens Computers in your country please see:
<http://www.fujitsu-siemens.com/countries>

Base Compiler Invocation

C benchmarks:
icl -Qvc8 -Qc99

C++ benchmarks:
icl -Qvc8

Base Portability Flags

400.perlbench: -D_Complex= -DSPEC_CPU_P64 -DSPEC_CPU_NO_NEED_VA_COPY
-DSPEC_CPU_WIN64_X64

401.bzip2: -D_Complex= -DSPEC_CPU_P64

403.gcc: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64

429.mcf: -D_Complex= -DSPEC_CPU_P64

445.gobmk: -D_Complex= -DSPEC_CPU_P64

456.hmmer: -D_Complex= -DSPEC_CPU_P64

458.sjeng: -DSPEC_CPU_P64

462.libquantum: -DSPEC_CPU_P64

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Base Portability Flags (Continued)

464.h264ref: -D_Complex= -DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTTYPES
471.omnetpp: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
473.astar: -DSPEC_CPU_P64
483.xalancbmk: -DSPEC_CPU_P64 -Qoption,cpp,--no_wchar_t_keyword

Base Optimization Flags

C benchmarks:

-fast -F512000000 shlw32M.lib -link -FORCE:MULTIPLE

C++ benchmarks:

-fast -Qcxx-features -F512000000 shlw32M.lib -link -FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qvc8 -Qc99

C++ benchmarks:

icl -Qvc8

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000
shlw32M.lib -link -FORCE:MULTIPLE

401.bzip2: basepeak = yes

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Peak Optimization Flags (Continued)

403.gcc: Same as 400.perlbench

429.mcf: Same as 400.perlbench

445.gobmk: Same as 400.perlbench

456.hmmmer: basepeak = yes

458.sjeng: Same as 400.perlbench

462.libquantum: basepeak = yes

464.h264ref: Same as 400.perlbench

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features
-F512000000 shlw32M.lib -link -FORCE:MULTIPLE

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.12.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.12.xml

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For other inquiries, please contact webmaster@spec.org.

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