



SPEC® CINT2006 Result

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Supermicro

(Test Sponsor: Advanced Micro Devices)

SPECint®_rate2006 = Not Run

Supermicro X7DBE (Intel Xeon 5160)

SPECint_rate_base2006 = 45.2

CPU2006 license: 49

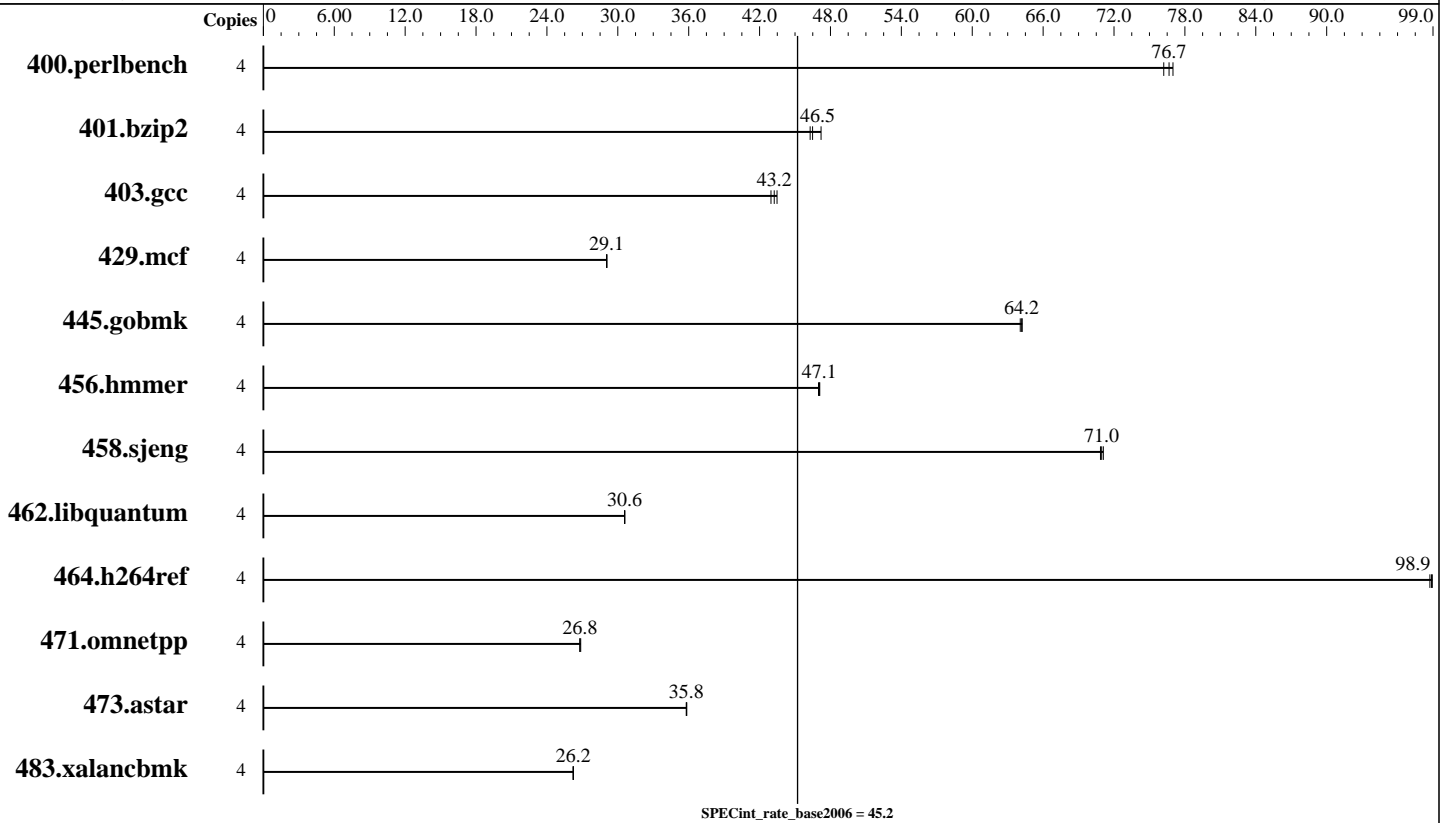
Test sponsor: Advanced Micro Devices

Tested by: Advanced Micro Devices

Test date: Apr-2006

Hardware Availability: Jul-2006

Software Availability: Jul-2006



Hardware

CPU Name: Intel Xeon 5160
 CPU Characteristics: 3.0GHz 1333MHz System Bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (8 x 1 GB DDR2-667 CL5 ECC FB)
 Disk Subsystem: 250GB SATA
 Other Hardware: None

Software

Operating System: SUSE Linux 10.1 (for x86_64)
 Compiler: SUSE gcc, g++ 4.1 (for x86_64)
 Auto Parallel: No
 File System: ext3
 System State: runlevel 3
 Base Pointers: 64-bit
 Peak Pointers: Not Applicable
 Other Software: None



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	508	77.0	513	76.2	<u>510</u>	<u>76.7</u>							
401.bzip2	4	818	47.2	834	46.3	<u>830</u>	<u>46.5</u>							
403.gcc	4	741	43.5	749	43.0	<u>745</u>	<u>43.2</u>							
429.mcf	4	<u>1255</u>	<u>29.1</u>	1255	29.1	1256	29.0							
445.gobmk	4	655	64.1	653	64.2	<u>654</u>	<u>64.2</u>							
456.hammer	4	<u>793</u>	<u>47.1</u>	792	47.1	794	47.0							
458.sjeng	4	<u>682</u>	<u>71.0</u>	681	71.1	683	70.9							
462.libquantum	4	2709	30.6	2711	30.6	<u>2711</u>	<u>30.6</u>							
464.h264ref	4	897	98.7	<u>895</u>	<u>98.9</u>	895	98.9							
471.omnetpp	4	931	26.9	933	26.8	<u>933</u>	<u>26.8</u>							
473.astar	4	783	35.8	<u>784</u>	<u>35.8</u>	784	35.8							
483.xalancbmk	4	<u>1052</u>	<u>26.2</u>	1052	26.2	1053	26.2							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

The system can be assembled using an SSI EEB form-factor case, and a Zippy/Emacs 700 watt PSL-6701P Power Supply.

Other Configuration Notes

Taskset utility used to bind process to cores

Base Compiler Invocation

C benchmarks:

gcc

C++ benchmarks:

g++

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hammer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

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Base Portability Flags (Continued)

464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-O3 -fno-inline-functions -funroll-loops

C++ benchmarks:

-O3 -funroll-loops

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.04.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.04.xml

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:08:22 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 3 October 2006.