SPEC® CINT2006 Result

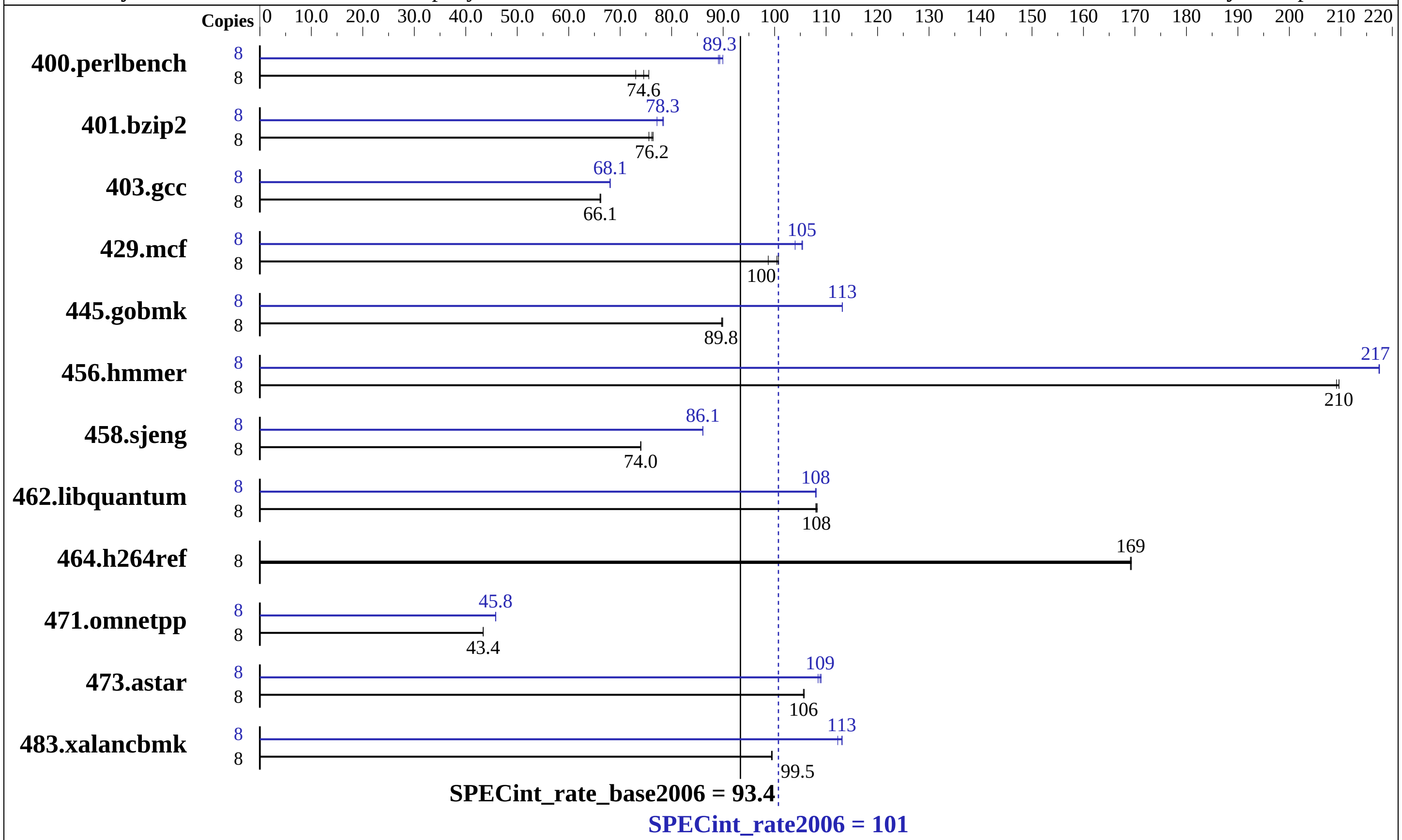
Hewlett-Packard Company

HP Integrity rx7640 (1.6GHz/18MB Dual-Core Intel Itanium 2)

SPECint®_rate2006 = 101

SPECint_rate_base2006 = 93.4

CPU2006 license: 03		**Test date:**	Sep-2006
Test sponsor:	Hewlett-Packard Company	**Hardware Availability:**	Sep-2006
Tested by:	Hewlett-Packard Company	**Software Availability:**	Sep-2006



Hardware

CPU Name:	Dual-Core Intel Itanium 2 9040
CPU Characteristics:	1.6GHz/18MB, 533MHz FSB
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	8 cores, 4 chips, 2 cores/chip
CPU(s) orderable:	1-8 chips
Primary Cache:	16 KB I + 16 KB D on chip per core
Secondary Cache:	1 MB I + 256 KB D on chip per core
L3 Cache:	9 MB I+D on chip per core
Other Cache:	None
Memory:	32 GB (16x2GB DIMMs)
Disk Subsystem:	36GB 15K RPM SCSI
Other Hardware:	None

Software

Operating System:	HPUX11i-TCOE B.11.23.0609
Compiler:	HP C/aC++ Developer's Bundle C.11.23.12
Auto Parallel:	No
File System:	vxfs
System State:	Multi-user
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill Smartheap 8.0

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	1070	73.0	**1048**	**74.6**	1034	75.6	8	877	89.1	**875**	**89.3**	869	89.9
401.bzip2	8	1021	75.6	**1014**	**76.2**	1010	76.4	8	1001	77.2	**987**	**78.3**	984	78.4
403.gcc	8	**974**	**66.1**	975	66.1	972	66.2	8	**946**	**68.1**	946	68.1	947	68.0
429.mcf	8	739	98.8	**726**	**100**	724	101	8	702	104	**693**	**105**	692	105
445.gobmk	8	936	89.7	933	89.9	**934**	**89.8**	8	**742**	**113**	742	113	742	113
456.hmmer	8	356	210	357	209	**356**	**210**	8	**343**	**217**	343	218	343	217
458.sjeng	8	1307	74.0	1309	74.0	**1308**	**74.0**	8	**1125**	**86.1**	1125	86.1	1125	86.0
462.libquantum	8	1531	108	1535	108	**1533**	**108**	8	1533	108	1536	108	**1535**	**108**
464.h264ref	8	1047	169	1046	169	**1046**	**169**	8	1047	169	1046	169	**1046**	**169**
471.omnetpp	8	**1152**	**43.4**	1152	43.4	1152	43.4	8	1092	45.8	**1091**	**45.8**	1091	45.8
473.astar	8	532	106	531	106	**532**	**106**	8	518	108	515	109	**516**	**109**
483.xalancbmk	8	**555**	**99.5**	555	99.4	555	99.5	8	492	112	**488**	**113**	488	113

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
The system had the September 2006 HP-UX 11i v2 Technical Computing
Operating Environment (TCOE) and compilers installed, along with
the following patches:

PHSS_34858  linker + fdp cumulative patch
PHSS_34853  Math Library Cumulative Patch
PHSS_34854  Integrity Unwind Library
PHSS_34855  HP C Compiler (A.06.12)
PHSS_34856  aC++ Compiler (A.06.12)
PHSS_34857  u2comp/be/plugin library patch
PHSS_34395  FORTRAN I/O Library [libIO77]
PHSS_34397  FORTRAN Intrinsics [libF90 B.11.23.17]
PHSS_34399  Fortran Product Patch, v3.1 to v3.1.1
PHKL_34020  Perfmon enhancements and Itanium Dual-Core

The following kernel tunables were set, in addition to the defaults
set by the Technical Computing OE:

dbc_max_pct=20
dbc_min_pct=20
maxdsiz=3221225472
maxssiz=401604608
```

Platform Notes

```
The system was configured as a single partition with 1 cell and
4 processors (8 cores) per cell.

The following config file entry was used to bind
processes to cells using the HP-UX "mpsched" utility:
submit = let "MYNUM=$SPECCOPYNUM" ; let "LDOM=\$MYNUM/8" ; mpsched -l \$LDOM $command
```

Base Compiler Invocation

C benchmarks:
`/opt/ansic/bin/cc -Ae`

C++ benchmarks:
`/opt/aCC/bin/aCC -Aa`

Base Portability Flags

400.perlbench: -DSPEC_CPU_HPUX_IA64
403.gcc: -DSPEC_CPU_HPUX
462.libquantum: -DSPEC_CPU_HPUX
483.xalancbmk: -DSPEC_CPU_HPUX_IA64

Base Optimization Flags

C benchmarks:
`+Ofaster +Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M -Wl,+pi,64M -Wl,-N`

C++ benchmarks:
`+Ofaster +Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M -Wl,+pi,64M -Wl,-N /usr/lib/hpux32/libCsup.a /opt/smartheap/SmartHeap_8/lib/libsmartheap.a`

Peak Compiler Invocation

C benchmarks:
`/opt/ansic/bin/cc -Ae`

C++ benchmarks:
`/opt/aCC/bin/aCC -Aa`

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Peak Portability Flags

400.perlbench: -DSPEC_CPU_HPUX_IA64
403.gcc: -DSPEC_CPU_HPUX
462.libquantum: -DSPEC_CPU_HPUX
483.xalancbmk: -DSPEC_CPU_HPUX_IA64

Peak Optimization Flags

C benchmarks:

400.perlbench: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2) +Ofaster +Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M -Wl,+pi,64M -Wl,-N

401.bzip2: Same as 400.perlbench

403.gcc: Same as 400.perlbench

429.mcf: Same as 400.perlbench

445.gobmk: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2) +Ofaster +Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M -Wl,+pi,64M +Odataprefetch=direct

456.hmmer: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2) +Ofaster +Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M -Wl,+pi,64M

458.sjeng: Same as 445.gobmk

462.libquantum: Same as 456.hmmer

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2) +Ofaster +Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M -Wl,+pi,64M /usr/lib/hpux32/libCsup.a /opt/smartheap/SmartHeap_8/lib/libsmartheap.a

473.astar: +Ofaster +Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M -Wl,+pi,64M +Onoparmsoverlap /usr/lib/hpux32/libCsup.a /opt/smartheap/SmartHeap_8/lib/libsmartheap.a

483.xalancbmk: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2) +Ofaster +Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M -Wl,+pi,64M +Onoparmsoverlap /usr/lib/hpux32/libCsup.a /opt/smartheap/SmartHeap_8/lib/libsmartheap.a

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.06.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.06.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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