



SPEC® CINT2006 Result

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Intel Corporation

Intel DG965WH motherboard
(2.93 GHz, Intel Core 2 Extreme processor
X6800)

SPECint®2006 = **Not Run**

SPECint_base2006 = **18.5**

CPU2006 license: 13

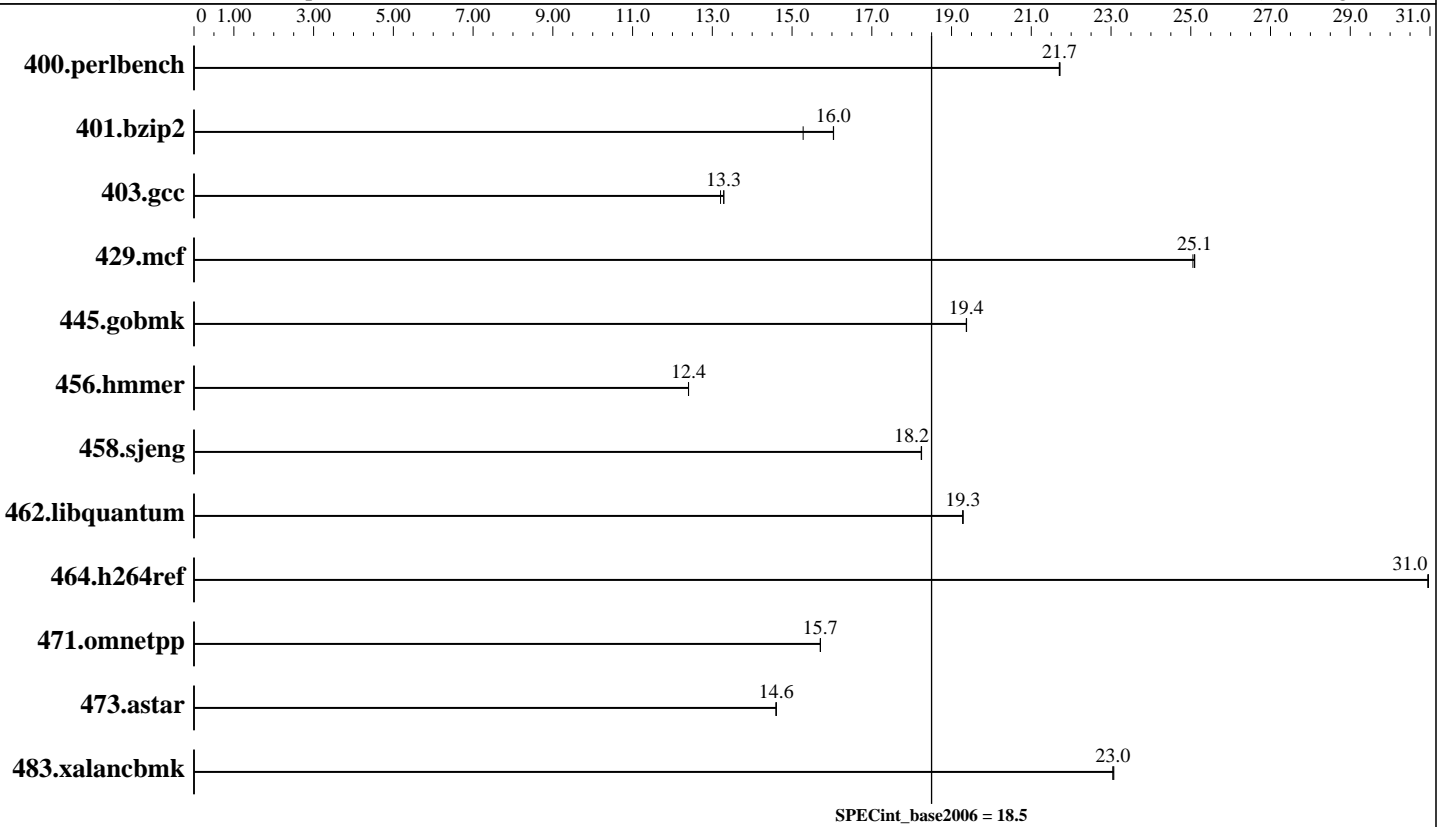
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Aug-2006

Hardware Availability: Jul-2006

Software Availability: Aug-2006



Hardware

CPU Name: Intel Core 2 Extreme X6800
 CPU Characteristics: 2.93 GHz, 1066 MHz bus
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 2 GB (2 1GB Corsair TWIN2X2048-8500C5 DDR2 1066 CL5-5-5)
 Disk Subsystem: Maxtor DiamondMax 10 6B300S0 300GB NCQ Serial ATA (7200 RPM, 16MB cache)
 Other Hardware: None

Software

Operating System: Windows XP Professional SP2 32-bit
 Compiler: Intel C++ Compiler 9.1 for IA32
 Build 20060816Z
 Microsoft Visual Studio .Net 2003 (for libraries)
 SmartHeap Library Version 8.0 from
<http://www.microquill.com/>
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: None



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	450	21.7	450	21.7	450	21.7						
401.bzip2	632	15.3	602	16.0	602	16.0						
403.gcc	610	13.2	606	13.3	606	13.3						
429.mcf	363	25.1	363	25.1	364	25.1						
445.gobmk	541	19.4	542	19.4	541	19.4						
456.hammer	752	12.4	752	12.4	752	12.4						
458.sjeng	663	18.2	663	18.2	663	18.2						
462.libquantum	1075	19.3	1075	19.3	1074	19.3						
464.h264ref	715	31.0	715	30.9	715	31.0						
471.omnetpp	398	15.7	398	15.7	398	15.7						
473.astar	481	14.6	481	14.6	481	14.6						
483.xalancbmk	299	23.1	299	23.0	299	23.0						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Memory timings set manually to DDR2-800 5-5-5-15 in the bios

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.03.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.03.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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