



CINT2000 Result

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Fujitsu Siemens Computers

PRIMERGY Econel200 S2, Intel Xeon processor 5130, 2.0 GHz

SPECint_rate2000 = 87.5

SPECint_rate_base2000 = 87.5

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Nov-2006 | Hardware Avail: Jul-2006 | Software Avail: May-2006

| Benchmark | Base Copies | Base Runtime | Base Ratio | Copies | Runtime | Ratio |
|-------------|-------------|--------------|------------|--------|---------|-------|
| 164.gzip | 4 | 116 | 56.0 | 4 | 116 | 56.0 |
| 175.vpr | 4 | 105 | 62.0 | 4 | 105 | 62.0 |
| 176.gcc | 4 | 51.0 | 100.0 | 4 | 51.0 | 100.0 |
| 181.mcf | 4 | 92.2 | 90.6 | 4 | 92.2 | 90.6 |
| 186.crafty | 4 | 54.9 | 84.5 | 4 | 54.9 | 84.5 |
| 197.parser | 4 | 118 | 70.9 | 4 | 118 | 70.9 |
| 252.eon | 4 | 50.3 | 120 | 4 | 50.3 | 120 |
| 253.perlbnk | 4 | 76.4 | 109 | 4 | 76.4 | 109 |
| 254.gap | 4 | 62.8 | 81.3 | 4 | 62.8 | 81.3 |
| 255.vortex | 4 | 59.8 | 147 | 4 | 59.8 | 147 |
| 256.bzip2 | 4 | 109 | 64.1 | 4 | 109 | 64.1 |
| 300.twolf | 4 | 132 | 106 | 4 | 132 | 106 |

Hardware

CPU: Intel Xeon processor 5130 (2.0 GHz, 4MB L2, 1333 MHz system bus)
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 32KB(I) + 32KB(D) on chip, per core
Secondary Cache: 4MB(I+D) on chip, per chip
L3 Cache: N/A
Other Cache: N/A
Memory: 4x1GB DDR2-RAM PC2-5300F (CAS 5-5-5)
Disk Subsystem: Samsung HD160JJ (SATA, 7.2krpm, 160GB)
Other Hardware: none

Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler: Intel C++ Compiler 9.1
Build 20060323Z (for 32-bit applications),
Microsoft Visual Studio .Net 2003
7.1.3088 (for libraries),
SmartHeap Library Version 8.0
from <http://www.microquill.com>
File System: NTFS
System State: Default

Notes/Tuning Information

GENERAL

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Portability flags

176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Base tuning flags

for C programs: -fast +FDO shlw32m.lib
for C++ programs: -fast -Qcxx_features +FDO shlw32m.lib

Peak tuning flags

same as baseline (basepeak=true set globally)

The system bus runs at 1333 MHz

This result was measured with 32-bit binaries using the 32-bit version of the operating system.



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Notes/Tuning Information (Continued)

BIOS Configuration:

Adjacent Sector Prefetch = Disable

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