



CINT2000 Result

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Fujitsu Siemens Computers

PRIMERGY RX200 S3, Intel Xeon processor E5310, 1.60 GHz

SPECint_rate2000 = 133

SPECint_rate_base2000 = 133

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Nov-2006 | Hardware Avail: Nov-2006 | Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.zip	8	147	88.6	8	147	88.6
175.vpr	8	133	97.6	8	133	97.6
176.gcc	8	67.0	152	8	67.0	152
181.mcf	8	156	107	8	156	107
186.crafty	8	68.5	135	8	68.5	135
197.parser	8	148	113	8	148	113
252.eon	8	63.2	191	8	63.2	191
253.perlbnk	8	96.0	174	8	96.0	174
254.gap	8	96.1	106	8	96.1	106
255.vortex	8	76.7	230	8	76.7	230
256.bzip2	8	140	99.4	8	140	99.4
300.twolf	8	164	170	8	164	170

Hardware

CPU: Intel Xeon processor E5310 (1.60 GHz, 2x4MB L2, 1066 MHz system bus)
CPU MHz: 1600
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 32KB(I) + 32KB(D) on chip, per core
Secondary Cache: 8MB(I+D) on chip, per chip (4MB shared per 2 cores)
L3 Cache: N/A
Other Cache: N/A
Memory: 8x1GB DDR2-RAM PC2-5300F (CAS 5-5-5)
Disk Subsystem: Seagate ST373454SS (SAS, 15.4krpm, 73GB)
Other Hardware: none

Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler: Intel C++ Compiler 9.1
Build 20060323Z (for 32-bit applications),
Microsoft Visual Studio .Net 2003
7.1.3088 (for libraries),
SmartHeap Library Version 8.0
from <http://www.microquill.com>
File System: NTFS
System State: Default

Notes/Tuning Information

GENERAL

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Portability flags

176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Base tuning flags

for C programs: -fast +FDO shlw32m.lib
for C++ programs: -fast -Qcxx_features +FDO shlw32m.lib

Peak tuning flags

same as baseline (basepeak=true set globally)

The system bus runs at 1066 MHz

This result was measured with 32-bit binaries using the 32-bit version of the operating system.



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Notes/Tuning Information (Continued)

BIOS Configuration:

Adjacent Sector Prefetch = Disable

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